GATE CONTROLLED DEVICE - Insulated Gate Bi-polar Transistor - Capsule Type

	PDF Data Sheet Available	New Part Number	V _{CES}	V _{GES}	I _C (Note 2)	te I _{CRM} (Note 2)	I _F	I _{FRM}	P _{MAX}	T _j (Note 3)	T _{stg}	Rth(j-hs) (IGBT)			Rth(j-hs) (Diode)			Wt (Typ)	Mounting Force	Outine No.
			(V)	(V)	(A)	(A)	(A)	(A)	(W)	(°C)	(°C)	Double (K/kW)	Collector (K/kW)	Emitter (K/kW)	Double (K/kW)	Cathode (K/kW)	Anode (K/kW)	(g)	(kN)	(Note 4)
WTC40AAC18	Υ	T0400NA18A	1800	+/- 20	400	800	400	800	1800	-60 to +150	-60 to +150	55	90	140	84	138	218	50	8.0 - 12.0	n/a
WTC140AAC18	Υ	T1400TA18A	1800	+/- 20	1400	2800	1400	2800	6600	-60 to +150	-60 to +150	15	25	39	35	52	104	120	20.0 - 30.0	n/a

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Note 1 Unless otherwise indicated, $T_j = 125^{\circ}C$

Note 2 $T_C = 90^{\circ}C$

Note 3 For operation below -40°C or above +125°C, please contact the Factory.

Note 4 Please refer to data sheets for outline drawings