


- ① Substrate: 1.59mm ±0.18mm [0.0625" ±0.007"] FR4/G10 or equivalent high temp material. 17µm [1/2 oz.] Cu clad. SnPb plating.
- ④ Leads: material- BeCu Alloy 194; plating- 60/40 SnPb 3.80-10.16µm [150-400µ"].
- ⑤ Test points: material- Phosphor Bronze; plating- Sn over 1.27µm [50µ"] Ni. Gold flash on contact end.

Description: Carrier Adapter

32 position PLCC ZIF socket with test probes to surface mountable PLCC emulator foot. Two piece adapter connects via a gold plated mini grid receptacle.

Tolerances: diameters ±0.03mm [±0.001"], PCB perimeters ±0.13mm [±0.005"], PCB thicknesses ±0.18mm [±0.007"], pitches (from true position) ±0.08mm [±0.003"], all other tolerances ±0.13mm [±0.005"] unless stated otherwise.

	CA-PLCC32-Z-J-T-01 Drawing	Status: Released	Scale 2:1	Rev: G
	© 1994 IRONWOOD ELECTRONICS, INC. PO BOX 21151 ST. PAUL, MN 55121 Tele: (651) 452-8100 www.ironwoodelectronics.com	Drawing: Matt Lund		Date: 8/17/94
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