

GaAs SP3T 2.5 V High Power Switch DC - 2.5 GHz

Features

- Low Voltage Operation: 2.5 V
- Low Harmonics: -74 dBc at +34 dBm & 1 GHz
- Low Insertion Loss: 0.5 dB at 1 GHz
- High Isolation: 18.5 dB at 2 GHz
- 3 mm 12-Lead PQFN Package
- 0.5 micron GaAs PHEMT Process

Description

M/A-COM's SW-489 is a GaAs PHEMT MMIC single pole three throw (SP3T) high power switch in a low cost 3 mm 12-lead PQFN package. The SW-489 is ideally suited for applications where high power, low control voltage, low insertion loss, high isolation, small size and low cost are required.

Typical applications are for GSM and DCS handset systems that connect separate transmit and receive functions to a common antenna, as well as other handset and related applications. The SW-489 can be used in all systems operating up to 2.5 GHz requiring high power at low control voltage.

The SW-489 is fabricated using a 0.5 micron gate length GaAs PHEMT process. The process features full passivation for performance and reliability.

Ordering Information¹

Part Number	Package
SW-489	Bulk Packaging
SW-489TR	1000 piece reel
SW-489SMB	Sample Test Board

1. Reference Application Note M513 for reel size information.

Absolute Maximum Ratings ^{2,3}

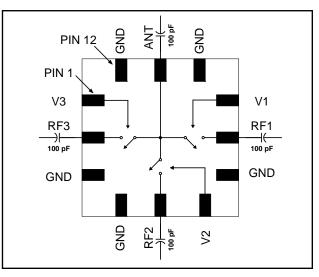
Parameter	Absolute Maximum		
Input Power (0.5 - 2.5 GHz, 2.5 V Control)	+38 dBm		
Operating Voltage	+8.5 V		
Operating Temperature	-40°C to +85°C		
Storage Temperature	-65°C to +150°C		

2. Exceeding any one or combination of these limits may cause permanent damage to this device.

 M/A-COM does not recommend sustained operation near these survivability limits.

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Functional Schematic



Pin Configuration

Pin No.	Pin Name	Description		
1	V3	Control 3		
2	RF3	RF Port 3		
3	GND	RF Ground		
4	GND	RF Ground		
5	RF2	RF Port 2		
6	V2	Control 2		
7	GND	RF Ground		
8	RF1	RF Port 1		
9	V1	Control 1		
10	GND	RF Ground		
11	ANT	Antenna Port		
12	GND	RF Ground		
13	GND (paddle) ⁴	RF Ground		

4. The exposed pad centered on the package bottom must be connected to RF and DC ground.

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Rev. V2

Electrical Specifications: $T_A = 25^{\circ}C$, $P_{IN} = +34$ dBm, $V_C = 0 / 2.5 V$, $Z_0 = 50 \Omega^{5}$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Insertion Loss ⁶	DC – 1 GHz 1 – 2 GHz 2 - 2.5 GHz	dB dB dB		0.5 0.6 0.8	0.65 0.8 1.0
Isolation	DC – 1 GHz 1 – 2 GHz 2 - 2.5 GHz	dB dB dB	23 18 15	25 18.5 16	
Return Loss	DC – 2.5 GHz	dB	_	20	
P1dB	_	dBm	—	38	_
2 nd Harmonic	1 GHz, P _{IN} = +34 dBm	dBc	—	-74	-65
3 rd Harmonic	1 GHz, P _{IN} = +34 dBm	dBc	_	-72	-65
Trise, Tfall	10% to 90% RF, 90% to 10% RF	μS	_	1	
Ton, Toff	50% control to 90% RF, and 50% control to 10% RF	μS	—	1	_
Transients	In Band	mV	—	10	_
Control Current	V _C = 2.5 V	μΑ	_	20	80

5. For positive voltage control, external DC blocking capacitors are required on all RF ports.

6. Insertion loss can be optimized by varying the DC blocking capacitor value, e.g. 1000 pF for 100 - 500 MHz, 100 pF for 0.5 - 2.5 GHz.

Truth Table 7,8

V1	V2	V3	ANT - RF1	ANT - RF2	ANT - RF3
1	0	0	On	Off	Off
0	1	0	Off	On	Off
0	0	1	Off	Off	On

7. Differential voltage, V(state 1) - V(state 0), must be +2.3 V minimum and less than 5.2 V.

8. $0 = 0 \pm 0.2$ V, 1 = +2.5 to +5 V

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

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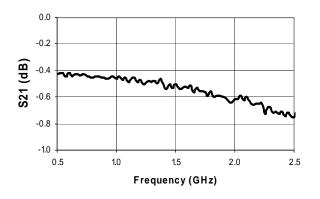


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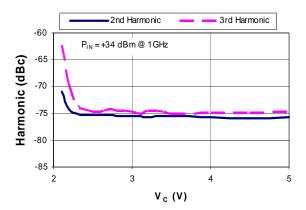
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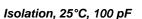
Typical Performance Curves

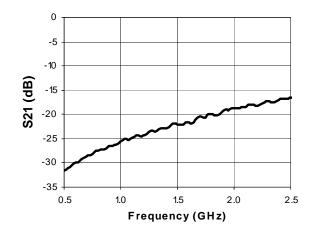
Insertion Loss, 25°C, 100 pF



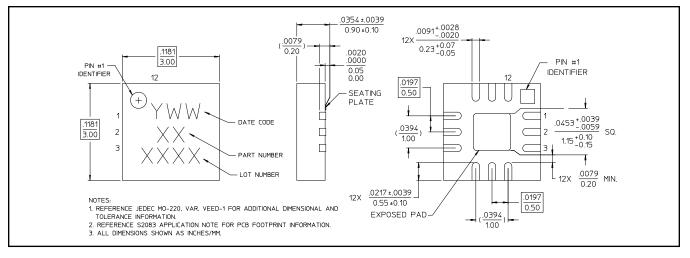
Harmonics, 25°C, 100 pF







3 mm 12-Lead PQFN



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