

Features

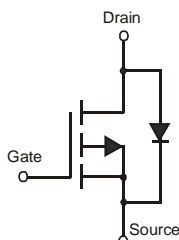
- Low On-Resistance
 - 60mΩ @ $V_{GS} = -4.5V$
 - 90mΩ @ $V_{GS} = -2.5V$
 - 113mΩ @ $V_{GS} = -1.8V$
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

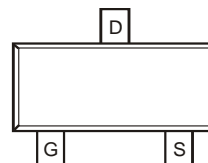
- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish — Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminals Connections: See Diagram Below
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.008 grams (approximate)



TOP VIEW



Internal Schematic



TOP VIEW

Maximum Ratings @ $T_A = 25^\circ C$ unless otherwise specified

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	-20	V
Gate-Source Voltage			V_{GSS}	±8	V
Continuous Drain Current (Note 3)	Steady State	$T_A = 25^{\circ}C$	I_D	-4.2	A
		$T_A = 70^{\circ}C$		-3.4	
Pulsed Drain Current (Note 4)			I_{DM}	-10	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3)	P_D	1.4	W
Thermal Resistance, Junction to Ambient @ $T_A = 25^\circ C$	$R_{\theta JA}$	90	$^\circ C/W$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ C$

Notes:

- No purposefully added lead.
- Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
- Device mounted on FR-4 PCB with 2oz. Copper and test pulse width $t \leq 10s$.
- Repetitive rating, pulse width limited by junction temperature.

Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)						
Drain-Source Breakdown Voltage	BV_{DSS}	-20	—	—	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	-1.0	μA	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	I_{GSS}	—	—	± 100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 5)						
Gate Threshold Voltage	$V_{GS(th)}$	-0.5	-	-0.9	V	$V_{DS} = V_{GS}, I_D = -250\mu A$
Static Drain-Source On-Resistance	$R_{DS(on)}$	—	45	60	m Ω	$V_{GS} = -4.5V, I_D = -4.2A$
			60	90		$V_{GS} = -2.5V, I_D = -3.4A$
			87	113		$V_{GS} = -1.8V, I_D = -2.0A$
Forward Transfer Admittance	$ Y_{fs} $	—	9	—	S	$V_{DS} = -5V, I_D = -4A$
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{iss}	—	727	—	pF	$V_{DS} = -20V, V_{GS} = 0V$ $f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	69	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	64	—	pF	
Gate Resistance	R_G	—	23	—	Ω	$V_{GS} = 0V, V_{DS} = 0V, f = 1.0\text{MHz}$
SWITCHING CHARACTERISTICS						
Total Gate Charge	Q_g	—	7.6	—	nC	$V_{GS} = -4.5V, V_{DS} = -4V, I_D = -3.5A$
Gate-Source Charge	Q_{gs}	—	1.4	—	nC	
Gate-Drain Charge	Q_{gd}	—	1.2	—	nC	
Turn-On Delay Time	$t_{D(on)}$	—	14.0	—	ns	$V_{DS} = -4V, V_{GS} = -4.5V,$ $R_L = 4\Omega, R_G = 6\Omega, I_D = -1A$
Turn-On Rise Time	t_r	—	13.0	—	ns	
Turn-Off Delay Time	$t_{D(off)}$	—	53.8	—	ns	
Turn-Off Fall Time	t_f	—	23.2	—	ns	

Notes: 5. Short duration pulse test used to minimize self-heating effect.

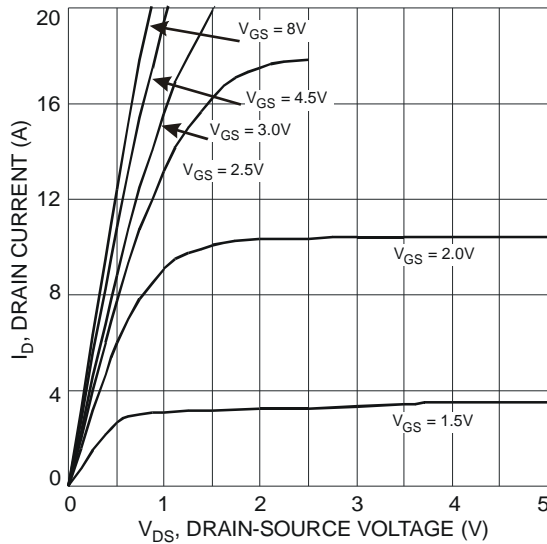


Fig. 1 Typical Output Characteristic

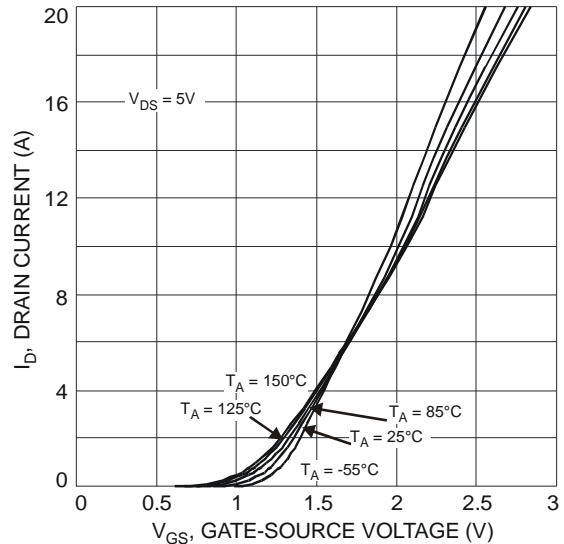


Fig. 2 Typical Transfer Characteristic

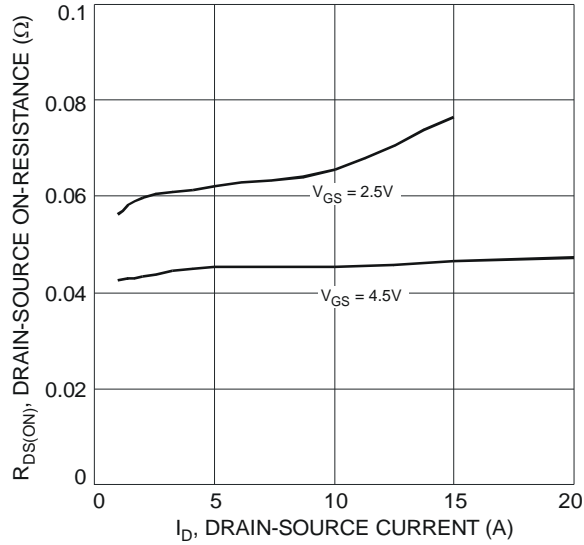


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

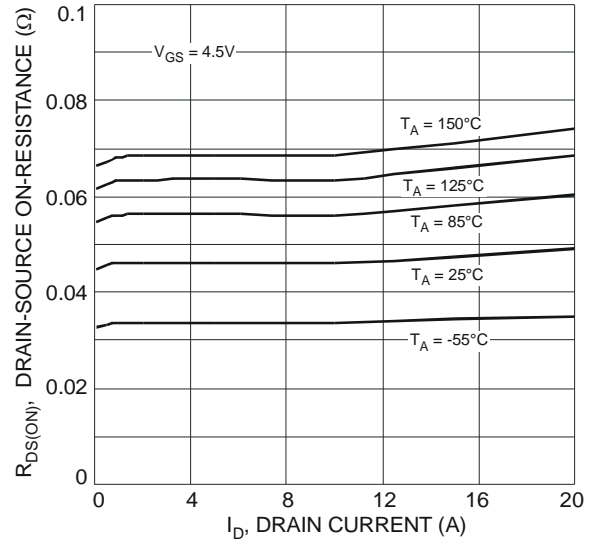


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

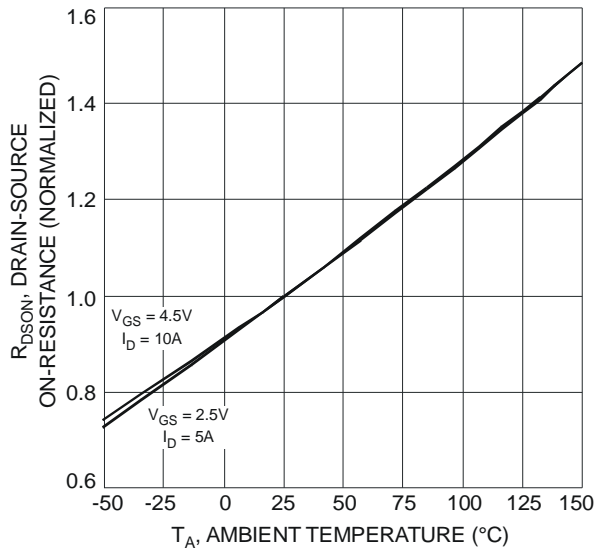


Fig. 5 On-Resistance Variation with Temperature

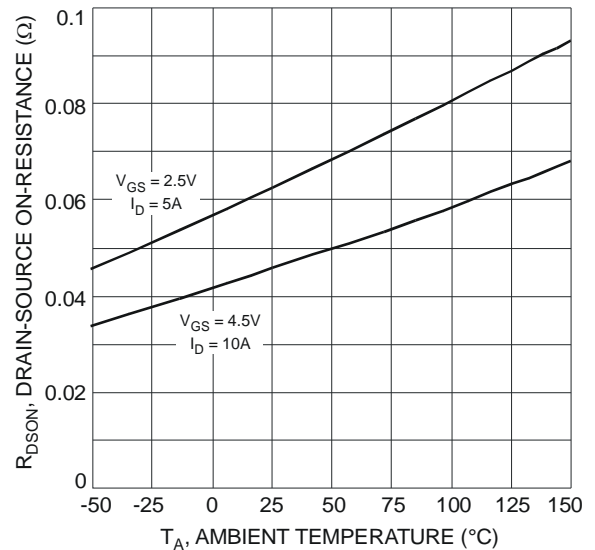


Fig. 6 On-Resistance Variation with Temperature

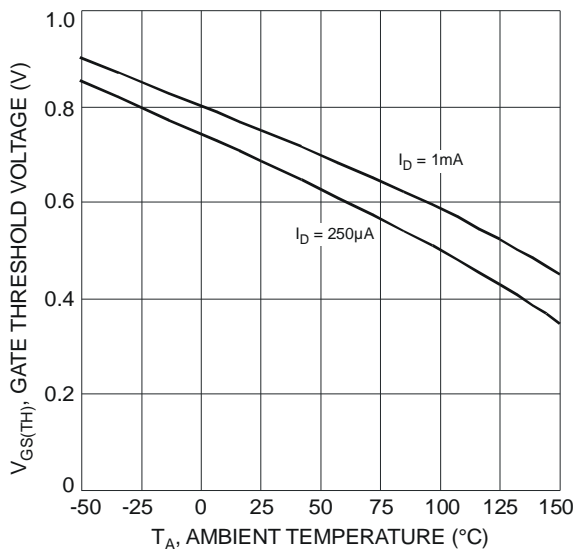


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

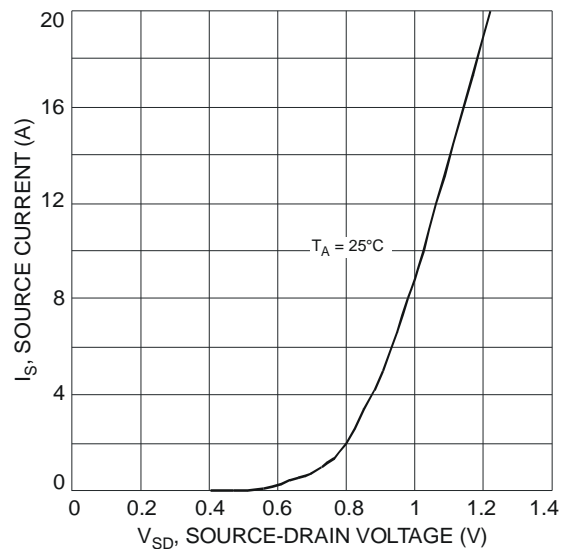


Fig. 8 Diode Forward Voltage vs. Current

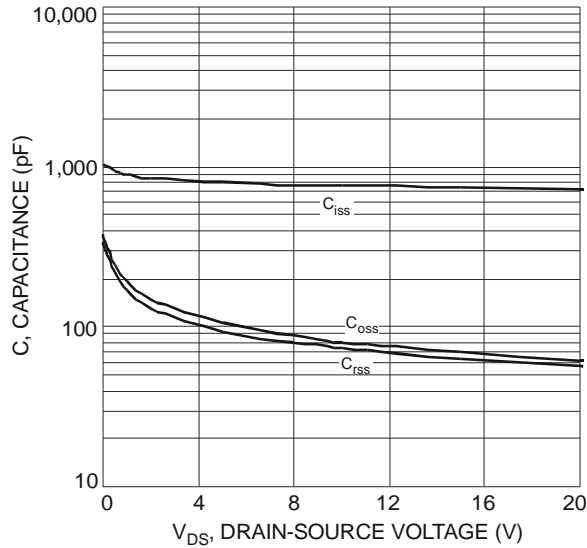


Fig. 9 Typical Total Capacitance

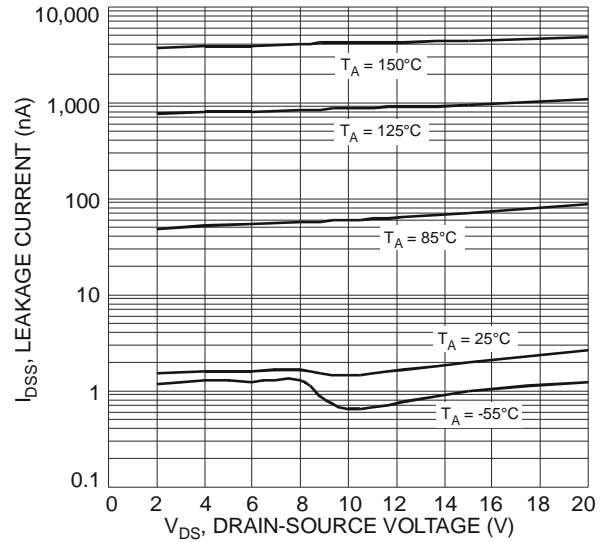


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

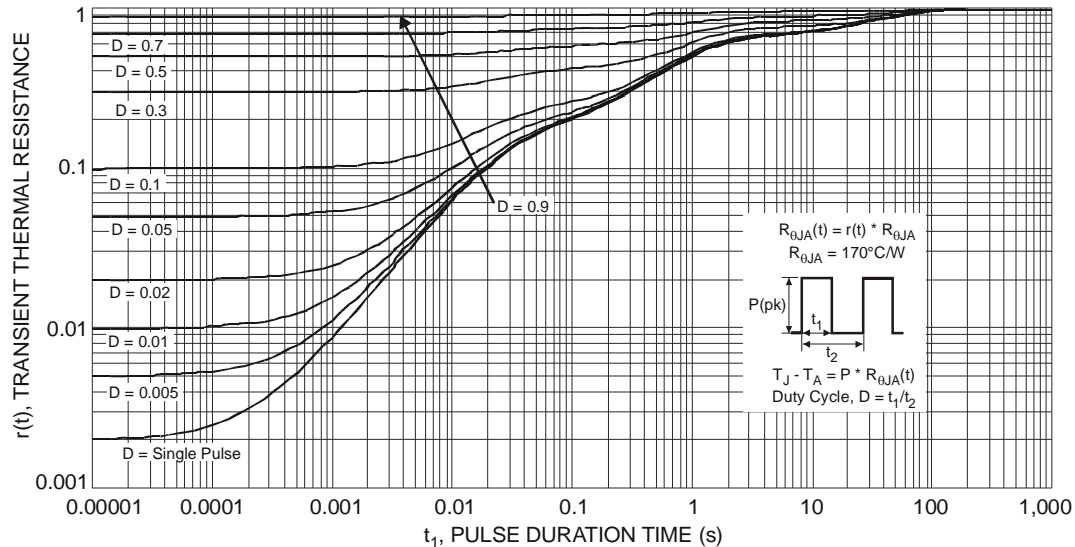


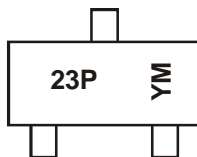
Fig. 11 Transient Thermal Response

Ordering Information (Note 6)

Part Number	Case	Packaging
DMP2305U-7	SOT-23	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information



23P = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: W = 2009)
M = Month (ex: 9 = September)

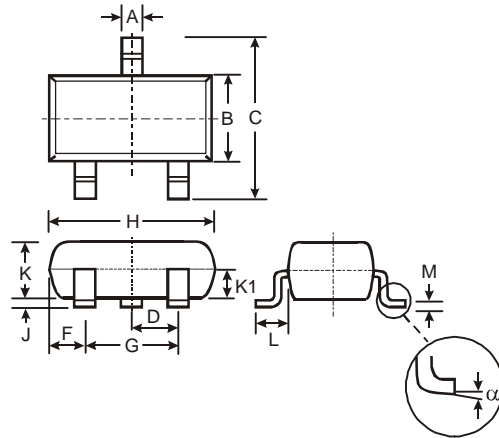
Date Code Key

Date Code Key

Year	2009	2010	2011	2012	2013	2014	2015
Code	W	X	Y	Z	A	B	C

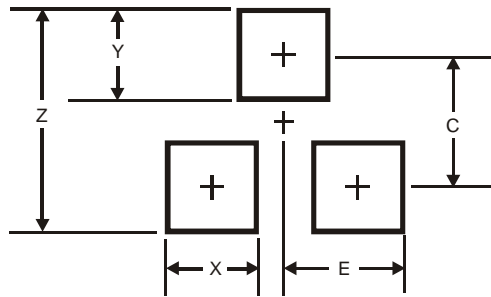
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Package Outline Dimensions



SOT-23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.903	1.10	1.00
K1	-	-	0.400
L	0.45	0.61	0.55
M	0.085	0.18	0.11
α	0°	8°	-
All Dimensions in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
X	0.8
Y	0.9
C	2.0
E	1.35

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