



JCS650

主要参数 MAIN CHARACTERISTICS

I_D	28.0 A
V_{DSS}	200 V
$R_{dson}(@V_{gs}=10V)$	0.085 Ω
Q_g	103 nC

用途

- 高频开关电源
- 电子镇流器
- UPS 电源

产品特性

- 低栅极电荷
- 低 C_{rss} (典型值 81pF)
- 开关速度快
- 产品全部经过雪崩测试
- 高抗 dv/dt 能力
- RoHS 产品

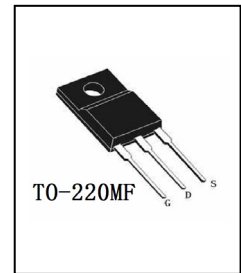
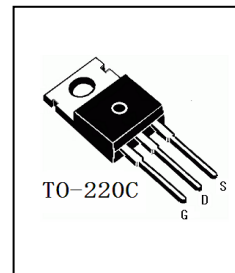
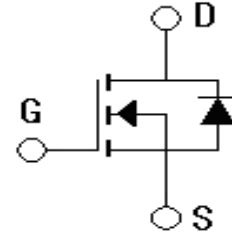
APPLICATIONS

- High efficiency switch mode power supplies
- Electronic lamp ballasts based on half bridge
- UPS

FEATURES

- Low gate charge
- Low C_{rss} (typical 81pF)
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability
- RoHS product

封装 Package



订货信息 ORDER MESSAGE

订货型号 Order codes	印记 Marking	封装 Package	无卤素 Halogen Free	包装 Packaging	器件重量 Device Weight
JCS650C-O-C-N-B	JCS650C	TO-220C	否 NO	条管 Tube	2.15 g(typ)
JCS650F-O-F-N-B	JCS650F	TO-220MF	否 NO	条管 Tube	2.20 g(typ)





绝对最大额定值 ABSOLUTE RATINGS (Tc=25℃)

项 目 Parameter	符 号 Symbol	数 值 Value		单 位 Unit
		JCS650C	JCS650F	
最高漏极-源极直流电压 Drain-Source Voltage	V _{DSS}	200		V
连续漏极电流 Drain Current -continuous	I _D T=25℃ T=100℃	28	28*	A
		17.7	17.7*	A
最大脉冲漏极电流 (注1) Drain Current - pulse (note 1)	I _{DM}	112	112*	A
最高栅源电压 Gate-Source Voltage	V _{GSS}	±30		V
单脉冲雪崩能量 (注2) Single Pulsed Avalanche Energy (note 2)	E _{AS}	575		mJ
雪崩电流 (注1) Avalanche Current (note 1)	I _{AR}	28		A
重复雪崩能量 (注1) Repetitive Avalanche Current (note 1)	E _{AR}	15.8		mJ
二极管反向恢复最大电压变化 速率 (注3) Peak Diode Recovery dv/dt (note 3)	dv/dt	5.5		V/ns
耗散功率 Power Dissipation	P _D T _C =25℃ -Derate above 25℃	158	50	W
		1.265	0.40	W/℃
最高结温及存储温度 Operating and Storage Temperature Range	T _J , T _{STG}	-55~+150		℃
引线最高焊接温度 Maximum Lead Temperature for Soldering Purposes	T _L	300		℃

*漏极电流由最高结温限制

*Drain current limited by maximum junction temperature





电特性 ELECTRICAL CHARACTERISTICS

项 目 Parameter	符 号 Symbol	测试条件 Tests conditions	最大 Min	典型 Typ	最大 Max	单 位 Units
关态特性 Off –Characteristics						
漏—源击穿电压 Drain-Source Voltage	BV_{DSS}	$I_D=250\mu A, V_{GS}=0V$	200	-	-	V
击穿电压温度特性 Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS}/\Delta T_J$	$I_D=250\mu A$, referenced to $25^\circ C$	-	0.19	-	V/ $^\circ C$
零栅压下漏极漏电流 Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=200V, V_{GS}=0V, T_C=25^\circ C$	-	-	1	μA
		$V_{DS}=160V, T_C=125^\circ C$	-	-	10	μA
正向栅极体漏电流 Gate-body leakage current, forward	I_{GSSF}	$V_{DS}=0V, V_{GS}=30V$	-	-	100	nA
反向栅极体漏电流 Gate-body leakage current, reverse	I_{GSSR}	$V_{DS}=0V, V_{GS}=-30V$	-	-	-100	nA
通态特性 On-Characteristics						
阈值电压 Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D=250\mu A$	2.0	-	4.0	V
静态导通电阻 Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS} = 10V, I_D=14A$	-	0.068	0.085	Ω
正向跨导 Forward Transconductance	g_{fs}	$V_{DS} = 40V, I_D=14A$ (note 4)	-	24	-	S
动态特性 Dynamic Characteristics						
输入电容 Input capacitance	C_{iss}	$V_{DS}=25V,$ $V_{GS}=0V,$ $f=1.0MHz$	-	2879	3742	pF
输出电容 Output capacitance	C_{oss}		-	362	470	pF
反向传输电容 Reverse transfer capacitance	C_{riss}		-	81	105	pF





电特性 ELECTRICAL CHARACTERISTICS

开关特性 Switching Characteristics						
延迟时间 Turn-On delay time	$t_{d(on)}$	$V_{DD}=100V, I_D=28A, R_G=25\Omega$ (note 4, 5)	-	28	69	ns
上升时间 Turn-On rise time	t_r		-	251	494	ns
延迟时间 Turn-Off delay time	$t_{d(off)}$		-	309	617	ns
下降时间 Turn-Off Fall time	t_f		-	220	412	ns
栅极电荷总量 Total Gate Charge	Q_g	$V_{DS}=160V,$ $I_D=28A$ $V_{GS}=10V$ (note 4, 5)	-	103	136	nC
栅-源电荷 Gate-Source charge	Q_{gs}		-	16	-	nC
栅-漏电荷 Gate-Drain charge	Q_{gd}		-	53	-	nC
漏-源二极管特性及最大额定值 Drain-Source Diode Characteristics and Maximum Ratings						
正向最大连续电流 Maximum Continuous Drain -Source Diode Forward Current		I_S	-	-	28	A
正向最大脉冲电流 Maximum Pulsed Drain-Source Diode Forward Current		I_{SM}	-	-	112	A
正向压降 Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS}=0V,$ $I_S=28.0A$	-	-	1.40	V
反向恢复时间 Reverse recovery time	t_{rr}	$V_{GS}=0V, I_S=28.0A$ $di_F/dt=100A/\mu s$ (note 4)	-	218	-	ns
反向恢复电荷 Reverse recovery charge	Q_{rr}		-	1.91	-	μC

热特性 THERMAL CHARACTERISTIC

项 目 Parameter	符 号 Symbol	最大 Max		单 位 Unit
		JCS650C	JCS650F	
结到管壳的热阻 Thermal Resistance, Junction to Case	$R_{th(j-c)}$	0.79	2.48	$^{\circ}C/W$
结到环境的热阻 Thermal Resistance, Junction to Ambient	$R_{th(j-A)}$	62.5	62.5	$^{\circ}C/W$

注释:

- 1: 脉冲宽度由最高结温限制
- 2: $L=1.1mH, I_{AS}=28A, V_{DD}=50V, R_G=25\Omega$, 起始结温 $T_J=25^{\circ}C$
- 3: $I_{SD} \leq 28A, di/dt \leq 200A/\mu s, V_{DD} \leq BV_{DSS}$, 起始结温 $T_J=25^{\circ}C$
- 4: 脉冲测试: 脉冲宽度 $\leq 300\mu s$, 占空比 $\leq 2\%$
- 5: 基本与工作温度无关

Notes:

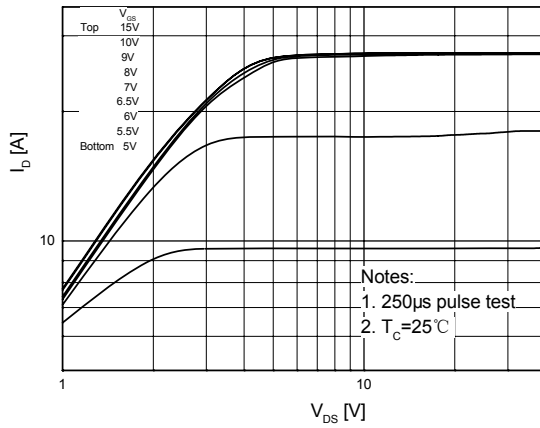
- 1: Pulse width limited by maximum junction temperature
- 2: $L=1.1mH, I_{AS}=28A, V_{DD}=50V, R_G=25\Omega$, Starting $T_J=25^{\circ}C$
- 3: $I_{SD} \leq 28A, di/dt \leq 200A/\mu s, V_{DD} \leq BV_{DSS}$, Starting $T_J=25^{\circ}C$
- 4: Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$
- 5: Essentially independent of operating temperature



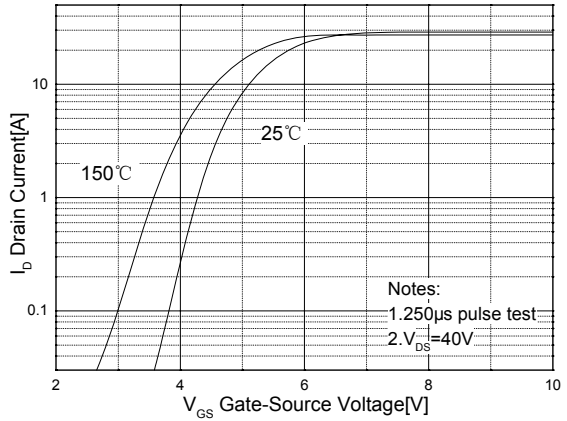


特征曲线 ELECTRICAL CHARACTERISTICS (curves)

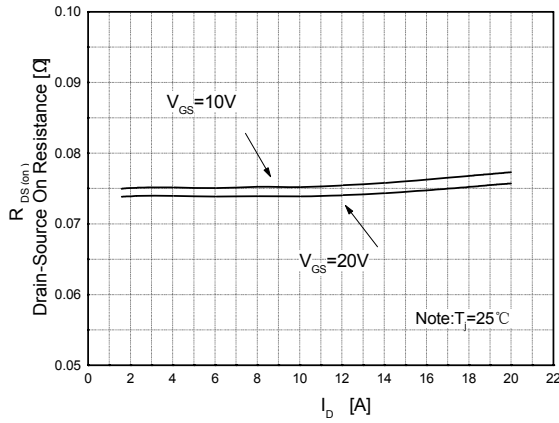
On-Region Characteristics



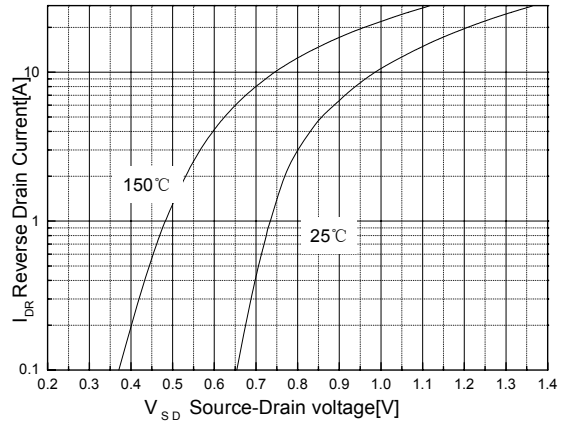
Transfer Characteristics



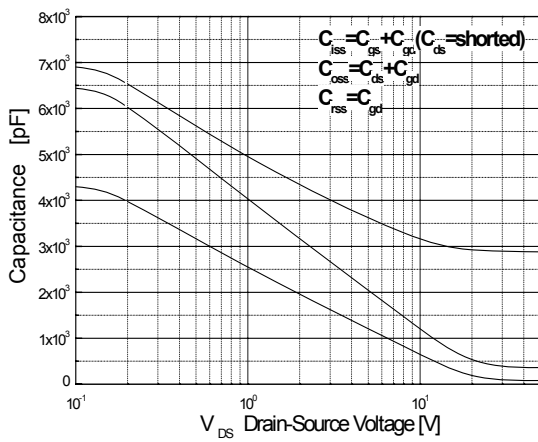
On-Resistance Variation vs. Drain Current and Gate Voltage



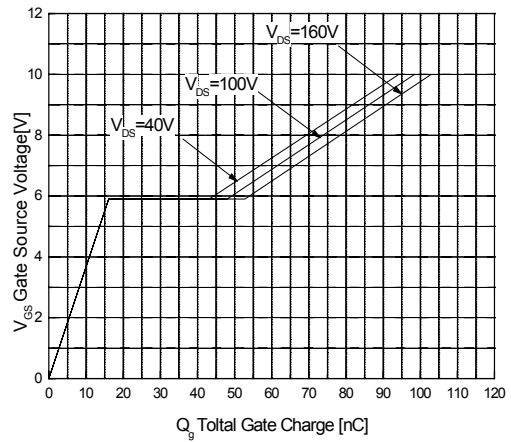
Body Diode Forward Voltage Variation vs. Source Current and Temperature



Capacitance Characteristics



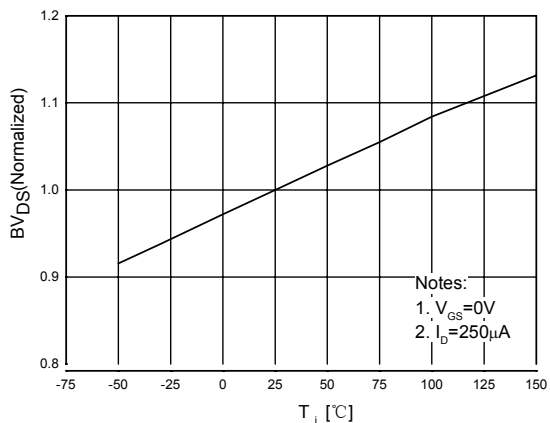
Gate Charge Characteristics



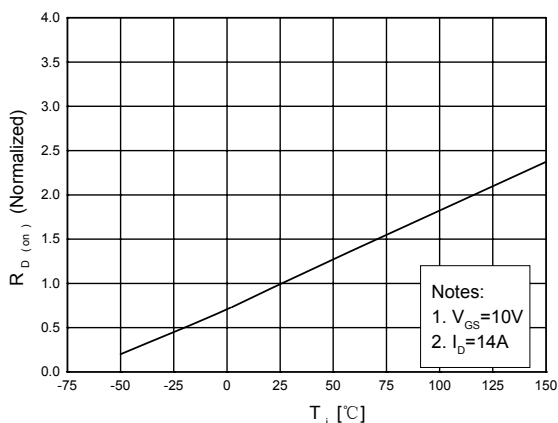


特征曲线 ELECTRICAL CHARACTERISTICS (curves)

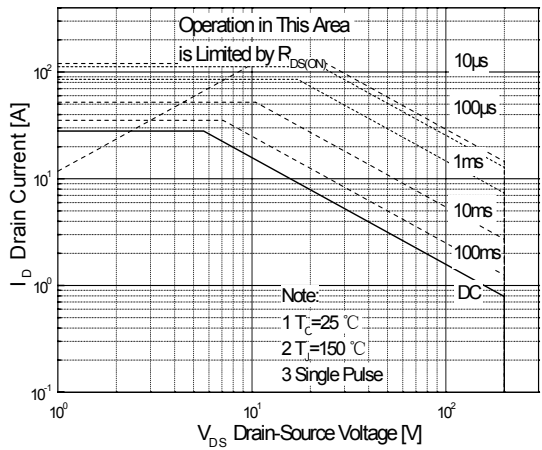
Breakdown Voltage Variation vs. Temperature



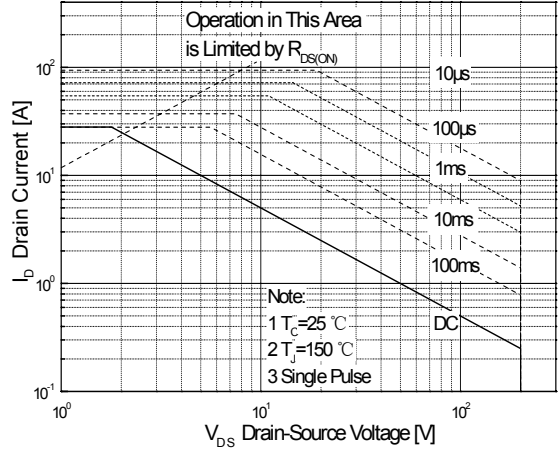
On-Resistance Variation vs. Temperature



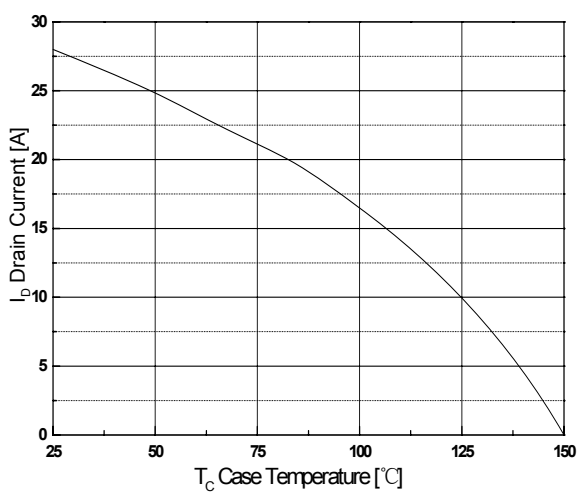
Maximum Safe Operating Area For JCS650C



Maximum Safe Operating Area For JCS650F

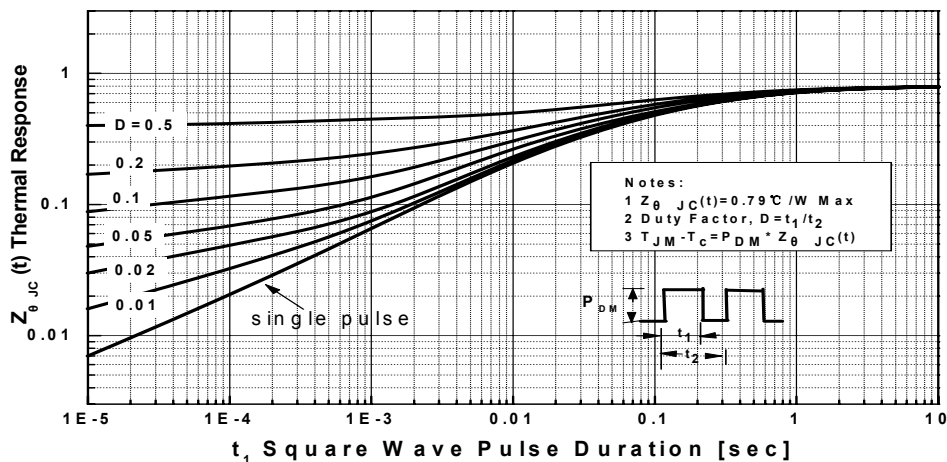


Maximum Drain Current vs. Case Temperature

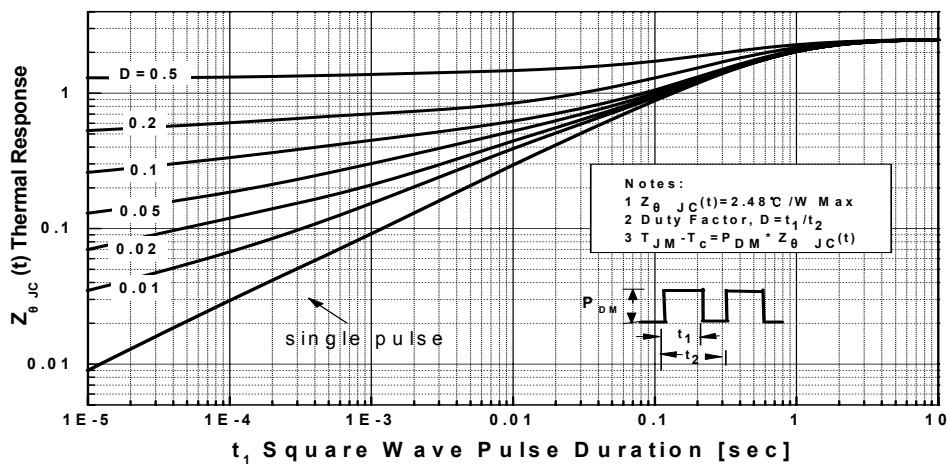




Transient Thermal Response Curve For JCS650C



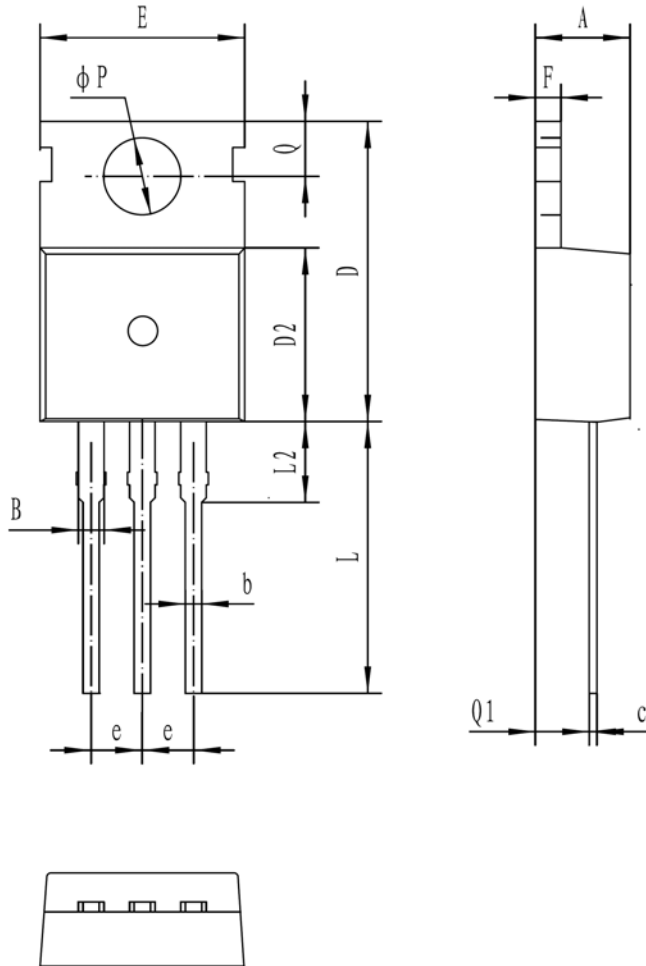
Transient Thermal Response Curve For JCS650F





TO-220C

单位 Unit: mm



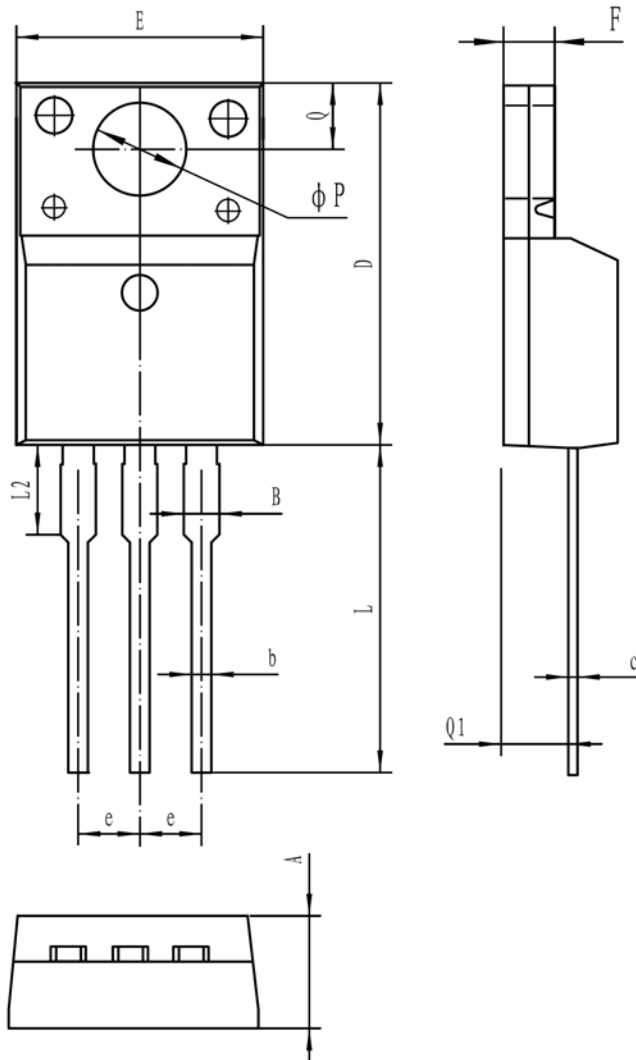
符号 symbol	MIN	MAX
A	4.30	4.70
B	1.22	1.47
b	0.70	0.95
c	0.40	0.65
D	15.20	16.20
D2	9.00	9.40
E	9.70	10.10
e	2.39	2.69
F	1.25	1.40
L	12.60	13.60
L2	2.80	3.20
Q	2.60	3.00
Q1	2.20	2.60
P	3.50	3.80





TO-220MF

单位 Unit: mm



符号 Symbol	MIN	MAX
A	4.5	4.9
B	-	1.47
b	0.7	0.9
c	0.45	0.6
D	15.67	16.07
E	9.96	10.36
e	2.54TYPE	
F	2.34	2.74
L	12.58	13.38
L2	3.13	3.33
ΦP	3.08	3.28
Q	3.2	3.4
Q1	2.56	2.96



**注意事项**

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- 3.在电路设计时请不要超过器件的绝对最大额定值，否则会影响整机的可靠性。
- 4.本说明书如有版本变更不另外告知

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2. We strongly recommend customers check carefully on the trademark when buying our product, if there is any question, please don't be hesitate to contact us.
3. Please do not exceed the absolute maximum ratings of the device when circuit designing.
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