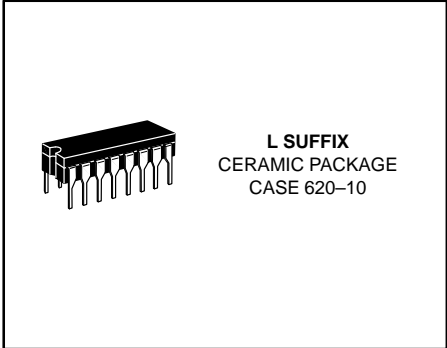


Dual 4-Input OR/NOR Gate

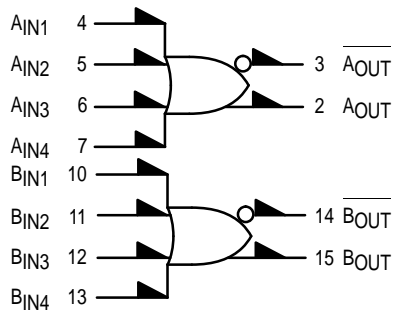
MC1660

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	-30°C		+25°C		+85°C		Unit
		Min	Max	Min	Max	Min	Max	
Power Supply Drain Current	I_E	—	—	—	28	—	—	mAdc
Input Current	I_{inH}	—	—	—	350	—	—	μ Adc
Switching Times								ns
Propagation Delay	t^{+-}	0.6	1.8	0.6	1.7	0.6	1.9	
	t^{-+}	0.6	1.6	0.6	1.5	0.6	1.7	
Rise Time, Fall Time (10% to 90%)	t^+, t^-	0.6	2.2	0.6	2.1	0.6	2.3	ns



LOGIC DIAGRAM



$$\overline{OUT} = \overline{IN1 + IN2 + IN3 + IN4}$$

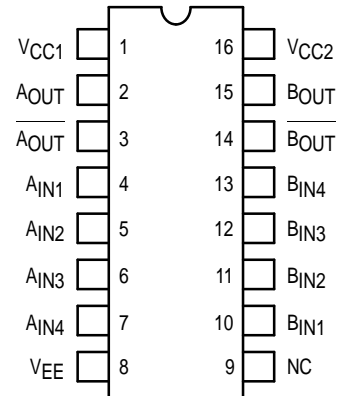
$$OUT = IN1 + IN2 + IN3 + IN4$$

$V_{CC1} = \text{PIN } 1$
 $V_{CC2} = \text{PIN } 16$
 $V_{EE} = \text{PIN } 8$

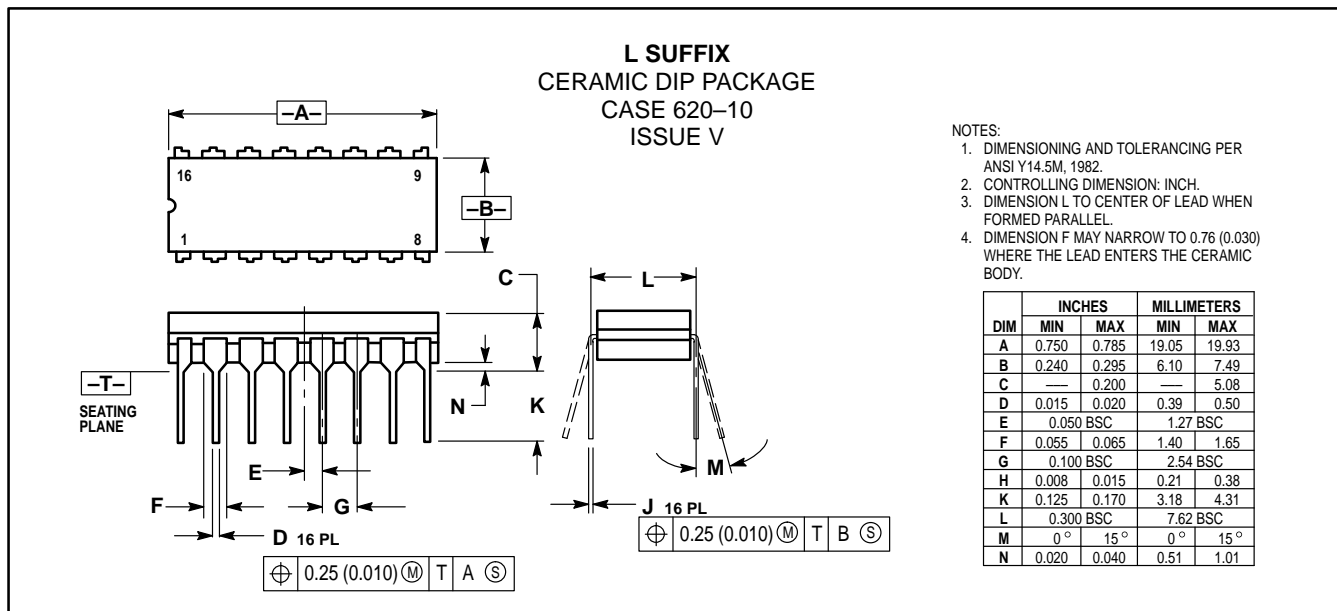
$t_{pd} = 0.9 \text{ ns typ (510 ohm load)}$
 $= 1.1 \text{ ns typ (50 ohm load)}$

$P_D = 120 \text{ mW typ/pkg (No load)}$
Full Load Current, $I_L = -25 \text{ mAdc max}$

PIN ASSIGNMENT



OUTLINE DIMENSIONS



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