

SERIES 610 5-TAPS 8 PIN SIP FAST LOGIC DELAY MODULES

- 8 Pin Single-In-Line Package • Minimum Board Space Required
- TTL and DTL Compatible
- 5 Equally Spaced Taps

Specifications:

- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Current : 100 μ A Max
- Logic 0 Input Current : -1.6mA Max
- Logic 1 Volt Out : 2.5V Min
- Logic 0 Volt Out : 0.5V Max
- Logic 1 Fan-Out : 25 Max
- Logic 0 Fan-Out : 12.5 Max
- Output Rise Time⁽²⁾ : 2NSEC TYP.
- Operating Temp Range : 0°C to 70°C
- Temperature Coefficient : 100 PPM/°C

**Input Test Conditions:**

- Input Pulse Voltage : 3.2V
- Input Rise Time : ≤ 2 NS
- Pulse Width : 40% of Total Delay
- Supply Current ICCL* : 32mA
- ICCH* : 7mA

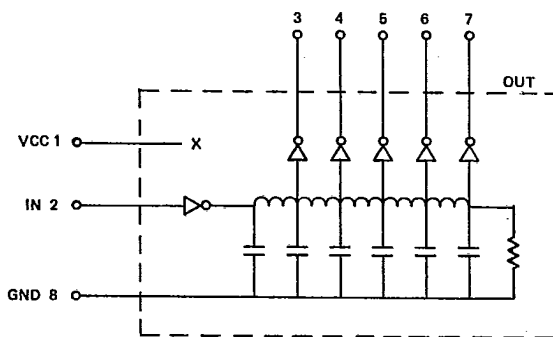
Electrical Specifications at 25°C (Measured with no Loads on Taps)

Part Number	Total Delay Nanosecond (1)	Tap To Tap Delay NSEC (1)
610-4*	4 $\pm$ 1	1 $\pm$ 0.5
610-5*	5 $\pm$ 1	1.2 $\pm$ 0.5
610-6*	6 $\pm$ 1	1.5 $\pm$ 0.5
610-8*	8 $\pm$ 2	2 $\pm$ 0.5
610-10*	10 $\pm$ 2	2.5 $\pm$ 0.5
610-12*	12 $\pm$ 2	3 $\pm$ 0.5
610-16*	16 $\pm$ 2	4 $\pm$ 0.6
610-25	25 $\pm$ 2	5 $\pm$ 0.8
610-30	30 $\pm$ 2	6 $\pm$ 1
610-35	35 $\pm$ 2	7 $\pm$ 1
610-40	40 $\pm$ 2	8 $\pm$ 1
610-45	45 $\pm$ 2.2	9 $\pm$ 1
610-50	50 $\pm$ 2.5	10 $\pm$ 2
610-60	60 $\pm$ 3	12 $\pm$ 2
610-75	75 $\pm$ 3.5	15 $\pm$ 2
610-100	100 $\pm$ 5.0	20 $\pm$ 2
610-125	125 $\pm$ 5.5	25 $\pm$ 2
610-150	150 $\pm$ 6.0	30 $\pm$ 3
610-175	175 $\pm$ 8.7	35 $\pm$ 3.5
610-200	200 $\pm$ 10	40 $\pm$ 4
610-250	250 $\pm$ 12.5	50 $\pm$ 5

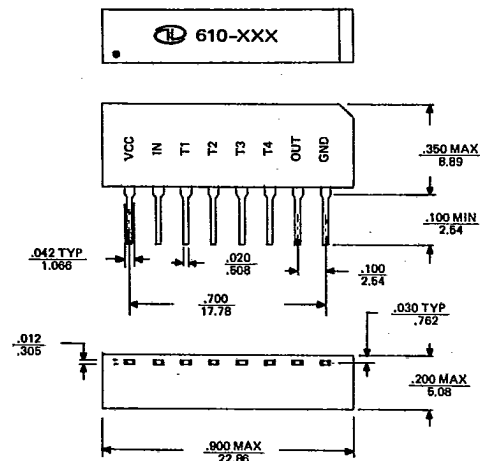
Note: (1) Measured at 1.5v level leading edge.

(2) Measured from 0.75v to 2.4v.

*Time Delay measured with respect to 1st Tap.



SCHEMATIC



INCHES .XXX $\pm$.010
MILLIMETERS .XX $\pm$.25