

□ MN101E01J, MN101E01K, MN101E01L, MN101E01M

Type	MN101E01J	MN101E01K	MN101E01L	MN101E01M
ROM (×8-bit) External memory can be expanded	192 K	256 K	320 K	384 K
RAM (×8-bit) External memory can be expanded	10 K	10 K	14 K	20 K
Package	QFP100-P-1818B *Lead-free	QFP100-P-1818B *Lead-free	QFP100-P-1818B *Lead-free LQFP100-P-1414 *Lead-free	QFP100-P-1818B *Lead-free LQFP100-P-1414 *Lead-free (under development)

Minimum Instruction Execution Time	Standard: 0.0625 μs (at 3.0 V to 3.6 V, 32 MHz) 0.1 μs (at 3.0 V to 3.6 V, 20 MHz) 62.5 μs (at 3.0 V to 3.6 V, 32 kHz) Double speed: 0.10 μs (at 3.0 V to 3.6 V, 10 MHz)
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Interrupts	• RESET • Watchdog • External 0 • External 1 • External 2 • External 3 • External 4 • External 5 • Timer 0 • Timer 1 • Timer 2 • Timer 3 • Timer 4 • Timer 5 • Timer 6 • Timer 7 (2 systems) • Time base • Serial 0 (2 systems) • Serial 1 (2 systems) • Serial 2 • Serial 3 • Serial 4 (2 systems) • Automatic transfer finish • A/D conversion finish • Key interrupts (8 lines)
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Timer Counter	<p>Timer counter 0 : 8-bit × 1 (square-wave/8-bit PWM output, event count, generation of remote control carrier, pulse width measurement, generation of real time)</p> <p>Clock source 1/2, 1/4 of system clock frequency; 1/1, 1/4, 1/16, 1/32, 1/64 of OSC oscillation clock frequency; 1/1 of XI oscillation clock frequency; external clock input</p> <p>Interrupt source coincidence with compare register 0</p> <p>Timer counter 1 : 8-bit × 1 (square-wave output, event count, synchronous output event)</p> <p>Clock source 1/2, 1/8 of system clock frequency; 1/1, 1/4, 1/16, 1/64, 1/128 of OSC oscillation clock frequency; 1/1 of XI oscillation clock frequency; external clock input</p> <p>Interrupt source coincidence with compare register 1</p> <p>Timer counter 0, 1 can be cascade-connected.</p> <p>Timer counter 2 : 8-bit × 1 (square-wave/8-bit PWM output, event count, synchronous output event, pulse width measurement generation of real time, serial baud rate timer)</p> <p>Clock source 1/2, 1/4 of system clock frequency; 1/1, 1/4, 1/16, 1/32, 1/64 of OSC oscillation clock frequency; 1/1 of XI oscillation clock frequency; external clock input</p> <p>Interrupt source coincidence with compare register 2</p> <p>Timer counter 3 : 8-bit × 1 (square-wave output, event count, generation of remote control carrier, serial baud rate timer)</p> <p>Clock source 1/2, 1/8 of system clock frequency; 1/1, 1/4, 1/16, 1/64, 1/128 of OSC oscillation clock frequency; 1/1 of XI oscillation clock frequency; external clock input</p> <p>Interrupt source coincidence with compare register 3</p> <p>Timer counter 2, 3 can be cascade-connected.</p> <p>Timer counter 4 : 8-bit × 1 (square-wave/8-bit PWM output, event count, pulse width measurement, serial baud rate timer)</p> <p>Clock source 1/2, 1/4 of system clock frequency; 1/1, 1/4, 1/16, 1/32, 1/64 of OSC oscillation clock frequency; 1/1 of XI oscillation clock frequency; external clock input frequency</p> <p>Interrupt source coincidence with compare register 4</p> <p>Timer counter 5 : 8-bit × 1 (square-wave output, event count, serial baud rate timer)</p> <p>Clock source 1/2, 1/4 of system clock frequency; 1/1, 1/4, 1/16, 1/64, 1/128 of OSC oscillation clock frequency; 1/1 of XI oscillation clock frequency; external clock input</p> <p>Interrupt source coincidence with compare register 5</p>
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Timer Counter (Continue)	Timer counter 4, 5 can be cascade-connected.
	<p>Timer counter 6 : 8-bit freerun timer</p> <p>Clock source 1/1 of system clock frequency; 1/1, 1/4096, 1/8192 of OSC oscillation clock frequency; 1/1, 1/4096, 1/8192 of XI oscillation clock frequency</p> <p>Interrupt source coincidence with compare register 6</p> <p>Timer counter 7 : 16-bit × 1 (square-wave/16-bit PWM output, cycle / duty continuous variable, event count, synchronous output event, pulse width measurement, input capture)</p> <p>Clock source 1/1, 1/2, 1/4, 1/16 of system clock frequency; 1/1, 1/2, 1/4, 1/16 of OSC oscillation clock frequency; 1/1, 1/2, 1/4, 1/16 of external clock input frequency</p> <p>Interrupt source coincidence with compare register 7 (2 lines)</p> <p>Time base timer (one-minute count setting)</p> <p>Clock source 1/1 of OSC oscillation clock frequency; 1/1 of XI oscillation clock frequency</p> <p>Interrupt source 1/128, 1/256, 1/512, 1/1024, 1/8192, 1/32768 of clock source frequency</p> <p>Watchdog timer</p> <p>Interrupt source 1/65536, 1/262144, 1/1048576, 1/4194304 of system clock frequency</p>

DMA Controller (Automatic Data Transfer)	<p>Max. Transfer cycles : 255</p> <p>Starting factor : external request, various types of interrupt, software</p> <p>Transfer mode : 1-byte transfer, word transfer, burst transfer</p>
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Serial Interface	<p>Serial 0 : synchronous type/UART (full-duplex) × 1</p> <p>Clock source 1/2, 1/4 of system clock frequency; pulse output of timer counter 2, 4; 1/2, 1/4, 1/16, 1/64 of OSC oscillation clock frequency</p>
	<p>Serial 1 : synchronous type/UART (full-duplex) × 1</p> <p>Clock source 1/2, 1/4 of system clock frequency; pulse output of timer counter 4, 5; 1/2, 1/4, 1/8, 1/16, 1/64 of OSC oscillation clock frequency</p>
	<p>Serial 2 : synchronous type/single-master I²C × 1</p> <p>Clock source 1/2, 1/4 of system clock frequency; pulse output of timer counter 2, 3; 1/2, 1/4, 1/8, 1/16, 1/32, 1/64, 1/128 of OSC oscillation clock frequency</p>
	<p>Serial 3 : synchronous type/single-master I²C × 1</p> <p>Clock source 1/2, 1/4 of system clock frequency; pulse output of timer counter 3, 5; 1/2, 1/4, 1/8, 1/16, 1/32, 1/64, 1/128 of OSC oscillation clock frequency</p>
	<p>Serial 4 : synchronous type/UART (full-duplex) × 1</p> <p>Clock source 1/2, 1/4 of system clock frequency; pulse output of timer counter 2, 5; 1/2, 1/4, 1/16, 1/64 of OSC oscillation clock frequency</p>

I/O Pins	I/O	34	• (5 V IF port) Common use • Specified pull-up resistor available • Input/output selectable (bit unit)
		50	• (3 V IF port) Common use • Specified pull-up resistor available • Input/output selectable (bit unit)

A/D Inputs	10-bit × 8-ch. (with S/H)
D/A Outputs	8-bit × 1-ch.
Special Ports	Buzzer output, remote control carrier signal output, high-current drive port
ROM Correction	Correcting address designation : up to 3 addresses possible

See the next page for electrical characteristics, pin assignment and support tool.

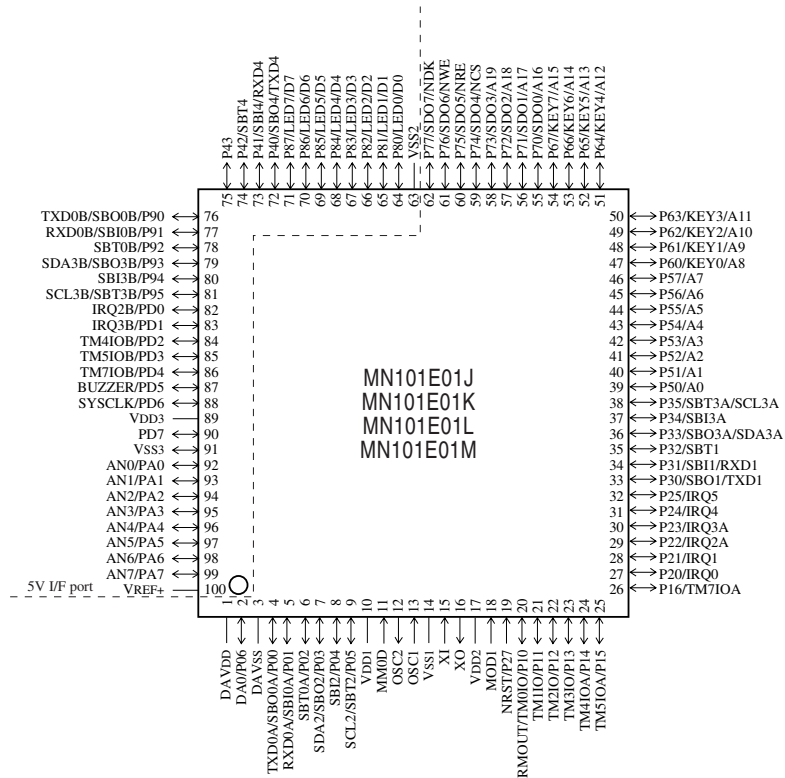
Electrical Characteristics

Supply current

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
Operating supply current	IDD1	fosc = 4 MHz, VDD = 3 V		11(48)	30(80)	mA
	IDD2	fx = 32 kHz, VDD = 3 V		8(43)	22(75)	mA
	IDD3	fx = 32 kHz, VDD = 3 V, Ta = 25°C		30(60)	120(180)	μA
Supply current at HALT	IDD4	fx = 32 kHz, VDD = 3 V, Ta = -40°C to +85°C		12	30	μA
Supply current at STOP	IDD5	VDD = 3 V, Ta = 25°C		0.3	3.0	μA
	IDD6	VDD = 3 V, Ta = -40°C to +85°C			80	μA

() : Flash memory built-in type.

Pin Assignment



QFP100-P-1818B *Lead-free

LQFP100-P-1414 *Lead-free

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Support Tool

■ In-circuit Emulator	PX-ICE101E9+PX-PRB101E01-QFP100-P-1818B		
	PX-ICE101E9+PX-PRB101E01-QFP100-P-1414		
■ Flash Memory Built-in Type	Type	MN101EF01M	
	ROM (× 8-bit)	384 K	
	RAM (× 8-bit)	24 K	
	Minimum instruction execution time	Standard:	0.0625 μs (at 3.0 V to 3.6 V, 32 MHz)
		Double speed:	0.10 μs (at 3.0 V to 3.6 V, 10 MHz)
Package	QFP100-P-1818B *Lead-free, LQFP100-P-1414 *Lead-free		

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