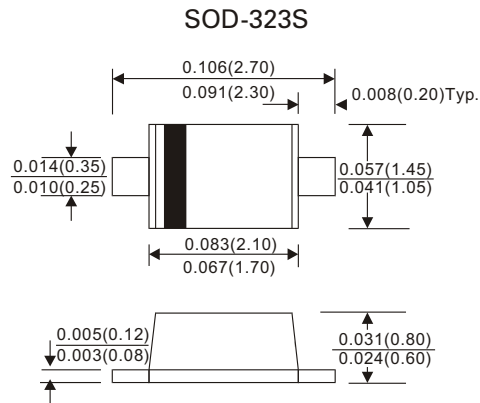


# PFM501V-NS

## SURFACE MOUNT SMALL SIGNAL TYPE



### FEATURES

- Extremely thin package
- Low stored charge
- Majority carrier conduction

### MECHANICAL DATA

Case : JEDEC SOD-323S molded plastic  
 Terminals : Solder plated, solderable per MIL-STD-750, Method 2026  
 Polarity : Indicated by cathode band  
 Mounting Position : Any  
 Weight : 0.0001482 ounce, 0.0042gram

### MAXIMUM RATING (at $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	CONDITIONS	SYMBOL	Min.	Typ.	Max.	UNITS
Rectified peak reverse voltage		$V_{RM}$			40	V
Continuous reverse voltage		$V_R$			40	V
Mean rectified current		$I_o$			100	mA
Forward surge current	8.3ms Single Half Sine-Wave Superimposed on Rated Load (JEDEC Method)	$I_{FSM}$			1000	mA
Capacitance between terminals	F=1MHz and applied 4vDC reverse voltage	$C_T$		20		pF
Storage temperature		$T_J$	-40		+125	$^\circ\text{C}$
Operating temperature		$T_{STG}$	-40		+125	$^\circ\text{C}$

### ELECTRICAL CHARACTERISTICS (at $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	CONDITIONS	SYMBOL	Min.	Typ.	Max.	UNITS
Forward voltage	$I_F = 100\text{mA DC}$	$V_F$			0.55	V
Reverse current	$V_R = 10\text{V DC}$	$I_R$			30	$\mu\text{A}$

# PFM501V-NS

## SURFACE MOUNT SMALL SIGNAL TYPE

### RATING AND CHARACTERISTICS CURVES PFM501V-NS

Fig. 1 Forward characteristics

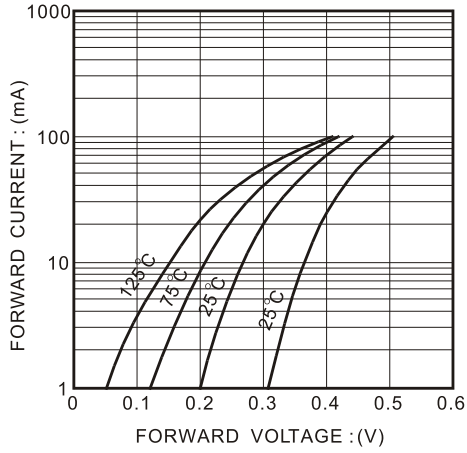


Fig. 2 Reverse characteristics

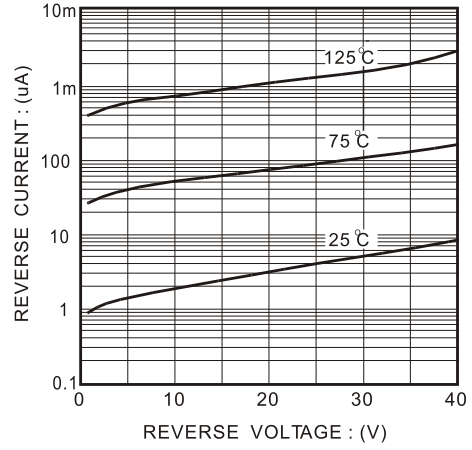


FIG.3-TYPICAL TERMINALS CAPACITANCE

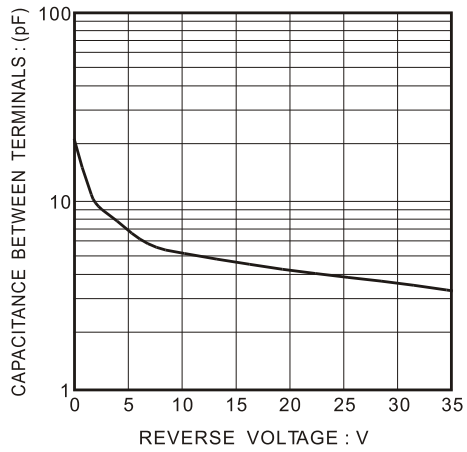


Fig. 4 Derating curve  
(mounting on glass epoxy PCBs)

