

Application Specific Discretes A.S.D.TM

FEATURES

- BIDIRECTIONAL TRIPLE CROWBAR
 PROTECTION.
- PEAK PULSE CURRENT : I_{PP} = 30 A , 10/1000 μs.
- BREAKDOWN VOLTAGE: TPI80xxN: 80V TPI120xxN: 120V.
- AVAILABLE IN SO-8 PACKAGES.
- LOW DYNAMIC BREAKOVER VOLTAGE : TPI80N : 150V TPI120 : 200V

DESCRIPTION

Dedicated devices for ISDN interface and high speed data telecom line protection. Equivalent to a triple TRISIL with low capacitance.

These devices provide :

- low capacitance from lines to ground, allowing high speed transmission without signal attenuation.
- good capacitance balance between lines in order to ensure longitudinal balance.
- fixed breakdown voltage in both common and differential modes.
- the same surge current capability in both common and differential modes.
- A particular attention has been given to the internal wire bonding. The "4-point" configuration ensures a reliable protection, eliminating overvoltages introduced by the parasitic inductances of the wiring (Ldi/dt), especially for very fast transient overvoltages.

COMPLIES WITH THE FOLLOWING STANDARDS :

CCITT K17 - K20	10/700	μs	1.5	kV
	5/310	μs	38	A
VDE 0433	10/700	μs	2	kV
	5/310	μs	50	A
VDE 0878	1.2/50	μs	1.5	kV
	1/20	μs	40	A
CNET	0.5/700	μs	1.5	kV
	0.2/310	μs	38	A

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TRIPOLAR PROTECTION FOR ISDN INTERFACES

TPI8011N

TPI12011N



SCHEMATIC DIAGRAM



ABSOLUTE MAXIMUM RATINGS (Tamb = 25 °C)

Symbol	Parameter	Value	Unit	
Ipp	Peak pulse current (see note 1)	10/1000 μs 5/320 μs 2/10 μs	30 40 90	A
I _{TSM}	Non repetitive surge peak on-state current (F = 50 Hz). $tp = 10 \text{ ms}$ $t = 1 \text{ s}$		8 3.5	A
T _{stg} Tj	Storage temperature range Maximum junction temperature	- 55 to + 150 150	°C	
TL	Maximum lead temperature for soldering	260	°C	



THERMAL RESISTANCES

Symbol	Parameter		Value	Unit
R _{th (j-a)}	Junction to ambient	SO-8	170	°C/W

ELECTRICAL CHARACTERISTICS (Tamb = 25 °C)		
Symbol	Parameter	
V_{RM}	Stand-off voltage	
I _{RM}	Leakage current	
V_{BR}	Breakdown voltage	
V_{BO}	Breakover voltage	
Ι _Η	Holding current	
I _{BO}	Breakover current	
I _{PP}	Peak pulse current	
VF	Forward Voltage Drop	
С	Capacitance	



	I _{RM} @	D V _{RM}	V _{BR}	@ I _R	V _{BO}	VBO dyn.	I _{BO}	Ι _Η
Types	max.		min.		max.	typ.	max.	min.
					note1	note2	note1	note3
	μΑ	V	V	mA	V	V	mA	mA
TPI8011N	10	70	80	1	120	150	800	150
TPI12011N	10	105	120	1	180	200	800	150

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Note 1 : See the reference test circuit 1. **Note 2 :** Surge test according to CCITT 1.5kV,10/700 μ s between Tip or Ring and ground. **Note 3 :** See functional holding current test circuit 2.

CAPACITANCES CHARACTERISTICS



CONFIGURATION	C _A (pF) max	C _B (pF) max	C _A - C _B (pF) max
V _A =1V V _B =56V	70	50	30
V _A = 56V V _B = 1V	50	70	30

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REFERENCE TEST CIRCUIT 1 :



FUNCTIONAL HOLDING CURRENT (IH) TEST CIRCUIT 2:



3) The D.U.T will come back off-state within 50 ms max.

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Fig. 1: Surge peak current versus overload duration.







1) Connect pins 2, 3, 6 and 7 to ground in order to guarantee a good surge current capability for long duration disturbances.

2) In order to take advantage of the "4-point" structure of the TPIxxxN, the Tip and Ring lines have to cross the device. In this case, the device will eliminate the overvoltages generated by the parasitic inductances of the wiring (Ldi/dt), especially for very fast transients.

2 - S INTERFACE PROTECTION

APPLICATION CIRCUITS :

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1 - U INTERFACE PROTECTION



This component uses an internal structure resulting in symetrical characteristics with a good balanced behaviour. Its topology ensures the same breakdown voltage level for positive and negative surges in differential and common mode.

ORDER CODE



MARKING

Package	Туре	Marking
SO-8	TPI8011N TPI12011N	TP80N TP120N

CONNECTION DIAGRAM

SO-8 Plastic





PACKAGE MECHANICAL DATA SO-8 Plastic



Packaging : Products supplied in antistatic tubes or tape and reel. **Weight :** 0.08g

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