L3 SERIES

1. PART NO. EXPRESSION:

L 3 - 4 7 N K - 1 0

(a) Series code

(d) □□: 10: RoHS Compliant

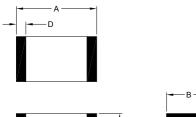
(a) (b) (c) (d)

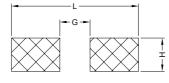
(b) Inductance code: 47N = 0.047uH

11 ~ 99 : Internal controlled number

(c) Tolerance code : $K = \pm 10\%$, $M = \pm 20\%$

2. CONFIGURATION & DIMENSIONS:









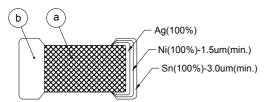
PCB Pattern

Unit:m/m

А	В	С		D	G	Н	L
2.00±0.20	1.25±0.20	0.85±0.20	1.25±0.20	0.50±0.30	1.00 Ref.	1.00 Ref.	3.00 Ref.

3. SCHEMATIC:

4. MATERIALS:



- (a) Body : Ferrite
- (b) Termination : Ag/Ni/Sn

5. GENERAL SPECIFICATION:

a) Temp. rise : 30°C Max.

b) Rated current: Base on temp. risec) Storage temp.: -40°C to +85°Cd) Operating temp.: -40°C to +85°C

e) Resistance to solder heat : 260°C.10secs



NOTE: Specifications subject to change without notice. Please check our website for latest information.



L3 SERIES

6. ELECTRICAL CHARACTERISTICS:

Part Number	EIA Size	Dim. C (mm)	Inductance (µH)	Q Min.	Test Frequency (MHz)	SRF (MHz) Min.	DC Resistance (Ω) Max.	Rated Current (mA)
L3-47N	0805	0.85±0.2	0.047	15	50	320	0.20	300
L3-68N□-□□	0805	0.85±0.2	0.068	15	50	280	0.20	300
L3-82N□-□□	0805	0.85±0.2	0.082	15	50	255	0.20	300
L3-R10□-□□	0805	0.85±0.2	0.100	20	25	235	0.30	250
L3-R12	0805	0.85±0.2	0.120	20	25	220	0.30	250
L3-R15□-□□	0805	0.85±0.2	0.150	20	25	200	0.40	250
L3-R18□-□□	0805	0.85±0.2	0.180	20	25	185	0.40	250
L3-R22	0805	0.85±0.2	0.220	20	25	170	0.50	250
L3-R27	0805	0.85±0.2	0.270	20	25	150	0.50	250
L3-R33□-□□	0805	0.85±0.2	0.330	20	25	145	0.55	250
L3-R39	0805	0.85±0.2	0.390	25	25	135	0.65	200
L3-R47	0805	1.25±0.2	0.470	25	25	125	0.65	200
L3-R56	0805	1.25±0.2	0.560	25	25	115	0.75	150
L3-R68	0805	1.25±0.2	0.680	25	25	105	0.80	150
L3-R82	0805	1.25±0.2	0.820	25	25	100	1.00	150
L3-1R0	0805	0.85±0.2	1.000	45	10	75	0.40	50
L3-1R2	0805	0.85±0.2	1.200	45	10	65	0.50	50
L3-1R5	0805	0.85±0.2	1.500	45	10	60	0.50	50
L3-1R8	0805	0.85±0.2	1.800	45	10	55	0.60	50
L3-2R2	0805	0.85±0.2	2.200	45	10	50	0.65	30
L3-2R7	0805	1.25±0.2	2.700	45	10	45	0.75	30
L3-3R3	0805	1.25±0.2	3.300	45	10	41	0.80	30
L3-3R9	0805	1.25±0.2	3.900	45	10	38	0.90	30
L3-4R7	0805	1.25±0.2	4.700	45	10	35	1.00	30
L3-5R6	0805	1.25±0.2	5.600	45	4	32	0.90	15
L3-6R8	0805	1.25±0.2	6.800	45	4	29	1.00	15
L3-8R2	0805	1.25±0.2	8.200	45	4	26	1.10	15
L3-100	0805	1.25±0.2	10.000	45	2	24	1.15	15

Packaging : Paper Carrier Tape Inductance tolerance : \square : K : $\pm 10\%$ M : $\pm 20\%$

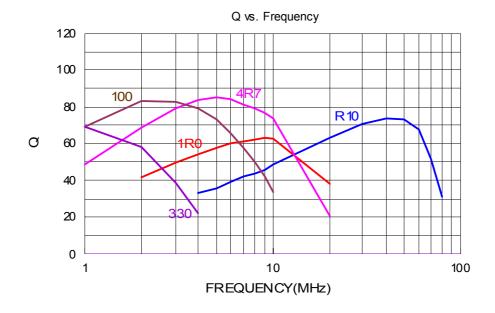


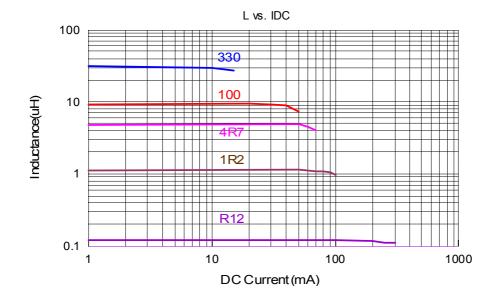
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L3 SERIES

7. IMPEDANCE VS. FREQUENCY CURVES:







NOTE: Specifications subject to change without notice. Please check our website for latest information.

L3 SERIES

8. RELIABILITY & TEST CONDITION:

ITEM	PERFORMANCE	TEST CONDITION			
Electrical Characteristics Tes	st				
Impedance		HP4291A, HP4287A+16092A			
DC Resistance	Refer to standard electrical characteristics list	HP4338B			
Rated Current					
Temperature Rise Test	30°C max. (Δt)	Applied the allowed DC current.			
Tomporataro raco roct	oo o max. (20)	Temperature measured by digital surface thermometer.			
Solder Heat Resistance	No mechanical damage Remaining terminal electrode: 70% min. Preheating Dipping Natural cooling 150°C 150°C 150°C 10±0.5 seconds	Preheat: 150°C, 60sec. Solder: Sn-Ag3.0-Cu0.5 Solder Temperature: 260±5°C Flux for lead free: rosin Dip Time: 10±0.5sec.			
Solderability	More than 90% of the terminal electrode should be covered with solder. Preheating Dipping Natural cooling 150°C 60	Preheat: 150°C, 60sec. Solder: Sn-Ag3.0-Cu0.5 Solder Temperature: 245±5°C Flux for lead free: rosin Dip Time: 4±1sec.			
Terminal Strength	The terminal electrode & the dielectric must	For Z / L Series :			
Tommar outlingur	not be damaged by the forces applied on the	Size Force (Kfg) Time (sec)			
	right conditions.	1 0.2			
		2 0.5			
	→ W	3 0.6			
		4 1.0 > 25			
		5 1.0			
		6 1.0			
	₩	7 1.5			
		8 2.0			
Flexture Strength	The terminal electrode & the dielectric must not be damaged by the forces applied on the right conditions. 20(.787) Bending 45(1.772) 45(1.772) 40(1.575)	Solder a chip on a test substrate, bend the substrate by 2mm (0.079in) and return.			



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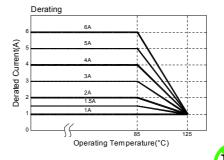
L3 SERIES

8. RELIABILITY & TEST CONDITION:

ITEM		PERFORMANC	E		TEST COND	DITION		
Bending Strength	The ferrite	e should not be damag	ged by forces	For Z / L Series	s :			
	applied o	n the right condition.		Series name	mm (inches)	P-Kgf		
	R0.5(0	.02) — 1.0(0.039)	2	0.80 (0.033)	0.3		
				3	1.40 (0.055)	1.0		
				4	0.00 (0.070)	0.5		
		∠	4	5	2.00 (0.079)	2.5		
				6	2.70 (0.400)	2.5		
		A		7	2.70 (0.106)	2.5		
				8	1			
							1	
Random Vibration Test	Appearan	ce : Cracking, shippin	g & any other	Frequency: 10	-55-10Hz for 1 m	in.		
	defects h	armful to the characte	ristics should	Amplitude : 1.5	2mm			
	not be all	owed.			nes : X, Y, Z dire			
				A period of 2 hours in each of 3 mutually perpendicular				
				directions (Tota	al 6 hours).			
Drop	Drop 10 t	imes on a concrete flo	or from a	No mechanical	No mechanical damage			
	height of	75cm.						
Loading at High	Appearan	ce : No damage.		Temperature : 8	85±5°C			
Temperature	Inductano	e: Within ±10% of ini	tial value.	Applied Current : rated current				
	Q : Withir	±30% of initial value.		Duration : 1008±12hrs				
				Measured at room temperature after placing for 2 to 3hrs.				
Humidity				Humidity: 90~9	95% RH.			
•				Temperature : 40±2°C Duration : 1008±12hrs				
				Measured at ro	om temperature	after placing fo	or 2 to 3hrs.	
Thermal Shock	Appearan	ce : No damage.		For L Series :				
	1	e: Within ±10% of init	tial value.	Condition for 1 cycle				
	Q : Withir	±30% of initial value.		Step1:-40±2°C 30±3 min.				
	Dhasa	Tomporatura (°C)	Times (min)	Step2: +85±5°C 30±3 min.				
	Phase 1	Temperature (°C) -40±2°C	Times (min.)	Number of cycl				
	2	-40±2 C +85±5°C	30±3	Measured at room temperature after placing for 2 to 3hrs.				
			3013					
Low temperature storage test	ivieasure	ed: 100 times		Temperature : -55±2°C				
				Duration : 1008				
				Measured at room temperature after placing for 2 to 3hrs.				
Drop	Drop 10 times on a concrete floor from a			No mechanical	damage			
	height of			-				

Derating Curve

For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over 85°C, the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.



RoHS Compliant

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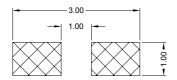
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PG. 5



9. SOLDERING AND MOUNTING:

9-1. Recommended PC Board Pattern



PC board should be designed so that products are not sufficient under mechanical stress as warping the board. Products shall be positioned in the sideway direction against the mechanical stress to prevent failure.

9-2. Soldering

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. The terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

9-2.1 Lead Free Solder Re-flow:

Recommended temperature profiles for re-flow soldering in Figure 1.

9-2.2 Solder Wave:

Wave soldering is perhaps the most rigorous of surface mount soldering processes due to the steep rise in temperature seen by the circuit when immersed in the molten solder wave, typical at 230°C. Due to the risk of thermal damage to products, wave soldering of large size products is discouraged. Recommended temperature profile for wave soldering is shown in Fig. 2

9-2.3 Soldering Iron (Figure 3):

Products attachment with soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. Note:

- a) Preheat circuit and products to 150°C.
- b) 350°C tip temperature (max)
- c) Never contact the ceramic with the iron tip
- d) 1.0mm tip diameter (max)
- e) Use a 20 watt soldering iron with tip diameter of 1.0mm
- f) Limit soldering time to 3 secs.

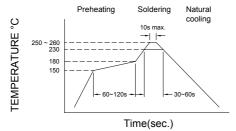


Figure 1. Re-flow Soldering

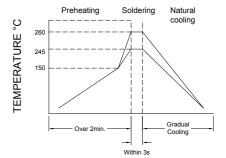


Figure 2. Wave Soldering

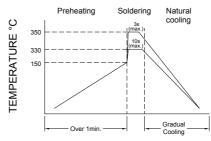


Figure 3. Hand Soldering

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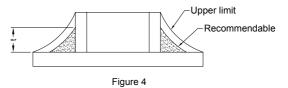
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9-3. Solder Volume

Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in Fig. 4.



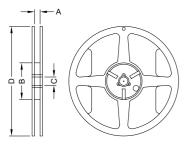


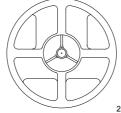
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L3 SERIES

10. PACKAGING INFORMATION:

10-1. Reel Dimension



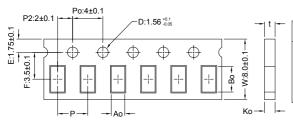


Туре	A(mm)	B(mm)	C(mm)	D(mm)
7" x 8mm	9.0±0.5	60.0±2.0	13.5±0.5	178.0±2.0
7" x 12mm	13.5±0.5	60.0±2.0	13.5±0.5	178.0±2.0

7" x 8mm

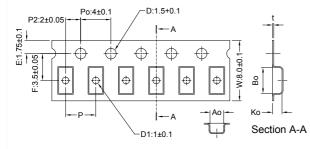
10-2 Tape Dimension / 8mm

Material: Paper



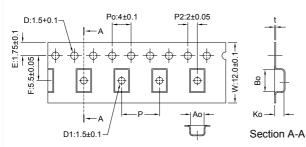
Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
	1	1.12±0.03	0.62±0.03	0.60±0.03	2.0±0.1	0.60±0.03	none
Z/L	2	1.85±0.05	1.05±0.05	0.95±0.05	4.0±0.1	0.95±0.05	none
	3(09)	2.30±0.05	1.50±0.05	0.95±0.05	4.0±0.1	0.95±0.05	none

Material: Plastic



	Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
		2	1.95±0.10	1.05±0.10	1.05±0.10	4.0±0.1	0.23±0.05	none
		3(09)	2.25±0.10	1.42±0.10	1.04±0.10	4.0±0.1	0.22±0.05	1.0±0.1
	Z/L	3(12)	2.35±0.10	1.50±0.10	1.45±0.10	4.0±0.1	0.22±0.05	1.0±0.1
		4	3.50±0.10	1.88±0.10	1.27±0.10	4.0±0.1	0.22±0.05	1.0±0.1
.		5	3.42±0.10	2.77±0.10	1.55±0.10	4.0±0.1	0.22±0.05	1.0±0.1

10-2.1 Tape Dimension / 12mm



Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
	6	4.95±0.1	1.93±0.1	1.93±0.1	4.0±0.1	0.24±0.05	1.5±0.1
Z/L	7	4.95±0.1	3.66±0.1	1.85±0.1	8.0±0.1	0.24±0.05	1.5±0.1
	8	6.10±0.1	5.40±0.1	2.00±0.1	8.0±0.1	0.30±0.05	1.5±0.1



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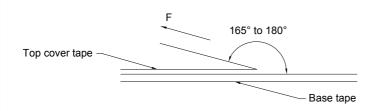
SUPERWORLD ELECTRONICS (S) PTE LTD

L3 SERIES

10-3. Packaging Quantity

Ohio Oine	0	7		_	4	3	3	0	4
Chip Size	8	7	6	5	4	C = 1.25mm	C = 0.85mm	2	'
Chip / Reel	1000	1000	2000	2500	3000	2000	4000	4000	10000
Inner Box	4000	4000	8000	12500	15000	10000	20000	20000	50000
Middle Box	20000	20000	40000	62500	75000	50000	100000	100000	250000
Carton	40000	40000	80000	125000	150000	100000	200000	200000	500000
Bulk (Bags)	7000	12000	20000	30000	50000	100000	150000	200000	300000

10-4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed (mm/min)
5~35	45~85	860~1060	300

Application Notice

1. Storage Conditions :

To maintain the solderability of terminal electrodes :

- a) Temperature and humidity conditions : $-10 \sim 40^{\circ}$ C and $30 \sim 70\%$ RH.
- b) Recommended products should be used within 6 months from the time of delivery.
- c) The packaging material should be kept where no chlorine or sulfur exists in the air.

2. Transportation :

- a) Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- b) The use of tweezers or vacuum pick up is strongly recommended for individual components.
- c) Bulk handling should ensure that abrasion and mechanical shock are minimized.



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