



# Accutec Microcircuit Corporation

## AK68512D 524,288 x 8 Bit CMOS Static Random Access Memory

### DESCRIPTION

The Accutec AK68512D high density memory module is a static random access memory organized in 512K x 8 bit words. The assembly consists of two medium speed 128K x 8 SRAMs in thin TSOP packages and a CMOS decoder logic IC mounted on the top side and two medium speed 128K x 8 SRAMs in thin TSOP packages mounted on the bottom surfaces of a printed circuit board. The module is supplied in a 600 mil wide, 32 pin DIP (Dual In-Line Package) configuration. This pinout is completely compatible with forthcoming industry standard monolithic designs. These modules are intended for use in applications where limited board space dictates compact module designs.

The operation of the AK68512D is identical to standard monolithic 8 bit word wide SRAMs.

The AK68512D offers the features of low power and medium speed by using CMOS devices and makes high density mounting possible with no surface mount technology.

### FEATURES

- 524,288 x 8 bit organization
- Fast access time: 85 - 120 nSEC
- Completely static RAM, no clock or timing strobe required
- Inputs and outputs TTL compatible
- Conventional 600 mil wide SIP package with industry compatible pinout
- Single +5 Volt (+/- 10%) power supply
- Operating free air temperature 0° to 70°C

### ELECTRICAL SPECIFICATIONS

Timing diagrams and basic electrical characteristics are those of the standard 128K x 8 SRAMs used to construct these modules. Accutec's module design allows the flexibility of selecting industry-compatible 128K x 8 SRAMs from any of a number of semiconductor manufacturers.

### PIN NOMENCLATURE

DQ <sub>1</sub> - DQ <sub>8</sub>	Data In/Data Out
A <sub>0</sub> - A <sub>18</sub>	Address Inputs
$\overline{\text{CE}}$	Chip Enable
$\overline{\text{WE}}$	Write Enable
V <sub>cc</sub>	5v Supply
V <sub>ss</sub>	Ground
$\overline{\text{OE}}$	Output Enable

### TIMING OPTIONS

70 nSEC Access Time
85 nSEC Access Time
100 nSEC Access Time
120 nSEC Access Time

### PIN ASSIGNMENT

