

# XThin® LEDs

## CxxxXT420-Sxx00-A

Cree's XThin LEDs are the next generation of solid-state LEDs that combine highly efficient InGaN materials with Cree's proprietary G•SiC® substrate to deliver superior price/performance for high-intensity LEDs. These LED chips have a geometrically enhanced Epi-down design to maximize light extraction efficiency and require only a single wire bond connection. These vertically structured LED chips are approximately 175 microns in height and require a low forward voltage. Cree's XT™ chips are tested for conformity to optical and electrical specifications and the ability to withstand 1000 V ESD. Applications for XThin LEDs include next-generation mobile appliances for use in their LCD backlights and digital camera flash where brightness, sub-miniaturization, and low power consumption are required.

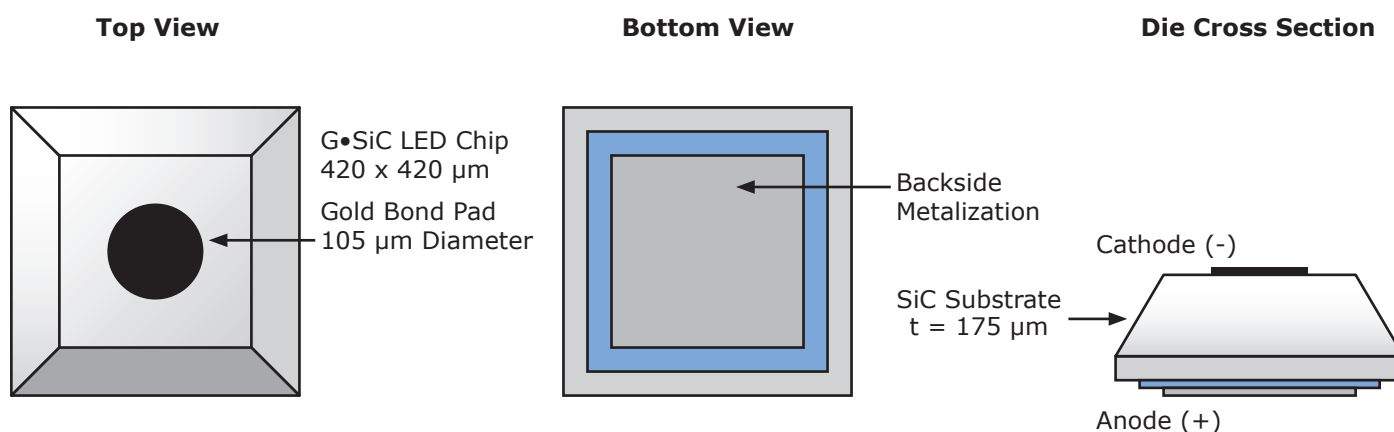
### FEATURES

- XThin LED Performance
  - 450 nm – 24 mW min.
- Low Forward Voltage
- Single Wire Bond Structure
- Class 2 ESD Rating

### APPLICATIONS

- LCD Backlighting
- Digital Camera Flash for Mobile Appliance
- LED Video Displays
- Audio Product Display Lighting

### CxxxXT420-Sxx00-A Chip Diagram



Maximum Ratings at $T_A = 25^\circ\text{C}$ <small>Notes 1&amp;3</small>		CxxxXT420-Sxx00-A
DC Forward Current		60 mA
Peak Forward Current (1/10 duty cycle @ 1 kHz)		200 mA
LED Junction Temperature		125°C
Reverse Voltage		5 V
Operating Temperature Range		-40°C to +100°C
Storage Temperature Range		-40°C to +100°C
Electrostatic Discharge Threshold (HBM) <small>Note 2</small>		1000 V
Electrostatic Discharge Classification (MIL-STD-883E) <small>Note 2</small>		Class 2

Typical Electrical/Optical Characteristics at $T_A = 25^\circ\text{C}$ , $I_f = 20\text{ mA}$ <small>Note 3</small>					
Part Number	Forward Voltage ( $V_f$ , V)			Reverse Current [ $I(V_r=5V)$ , $\mu\text{A}$ ]	Full Width Half Max ( $\lambda_{DF}$ , nm)
	Min.	Typ.	Max.	Max.	Typ.
C450XT420-Sxx00-A	2.6	3.1	3.6	2	20

Mechanical Specifications		CxxxXT420-Sxx00-A	
Description		Dimension	Tolerance
P-N Junction Area ( $\mu\text{m}$ )		368 x 368	$\pm 25$
Top Area ( $\mu\text{m}$ )		250 x 250	$\pm 25$
Bottom Area ( $\mu\text{m}$ )		420 x 420	$\pm 25$
Chip Thickness ( $\mu\text{m}$ )		175	$\pm 15$
Au Bond Pad Diameter ( $\mu\text{m}$ )		105	-5, +15
Au Bond Pad Thickness ( $\mu\text{m}$ )		1.2	$\pm 0.5$
Back Contact Metal Area ( $\mu\text{m}$ )		330 x 330	$\pm 25$
Back Contact Metal Thickness ( $\mu\text{m}$ )	(Au/Sn) <small>Note 4</small>	1.7	$\pm 0.3$

### Notes:

- Maximum ratings are package-dependent. The above ratings were determined using a T-1 3/4 package (with Hysol OS4000 epoxy) for characterization. Ratings for other packages may differ. The forward currents (DC and Peak) are not limited by the die but by the effect of the LED junction temperature on the package. The junction temperature limit of 125°C is a limit of the T-1 3/4 package; junction temperature should be characterized in a specific package to determine limitations. Assembly processing temperature must not exceed 325°C (< 5 seconds). See Cree XThin Applications Note for more assembly process information.
- Product resistance to electrostatic discharge (ESD) according to the HBM is measured by simulating ESD using a rapid avalanche energy test (RAET). The RAET procedures are designed to approximate the maximum ESD ratings shown. The RAET procedure is performed on each die. The ESD classification of Class 2 is based on sample testing according to MIL-STD-883E.
- All products conform to the listed minimum and maximum specifications for electrical and optical characteristics when assembled and operated at 20 mA within the maximum ratings shown above. Efficiency decreases at higher currents. Typical values given are within the range of average values expected by manufacturer in large quantities and are provided for information only. All measurements were made using lamps in T-1 3/4 packages (with Hysol OS4000 epoxy). Optical characteristics measured in an integrating sphere using Illuminance E.
- Back contact metal is 80%/20% Au/Sn by weight, with target eutectic melting temperature of approximately 282°C. See XBright® Applications Note for detailed packaging recommendations.
- Caution: To avoid leakage currents and achieve maximum output efficiency, die attach material must not contact the side of the chip. See Cree XBright Applications Note for more information.
- XThin chips are shipped with the junction side down, not requiring a die transfer prior to die attach.
- Specifications are subject to change without notice.



## Standard Bins for CxxxXT420-Sxx00-A

LED chips are sorted to the **radiant flux** and **dominant wavelength** bins shown. A sorted die sheet contains die from only one bin. Sorted die kit (CxxxXT420-Sxx00-A) orders may be filled with any or all bins (CxxxXT420-01xx-A) contained in the kit. All radiant flux and dominant wavelength values shown and specified are at  $I_f = 20 \text{ mA}$ .

### XT-24

### C450XT420-S2400-A

Radiant Flux	30.0 mW	C450XT420-0125-A	C450XT420-0126-A	C450XT420-0127-A	C450XT420-0128-A	
	27.0 mW	C450XT420-0121-A	C450XT420-0122-A	C450XT420-0123-A	C450XT420-0124-A	
	24.0 mW	C450XT420-0117-A	C450XT420-0118-A	C450XT420-0119-A	C450XT420-0120-A	
		445 nm	447.5 nm	450 nm	452.5 nm	455 nm
		<b>Dominant Wavelength</b>				

## Characteristic Curves

These are representative measurements for the XThin product. Actual curves will vary slightly for the various radiant flux and dominant wavelength bins.

