

Type Number	Case			Applications	ROM (Bytes)	RAM (Bits)	Stacks *(RAM)	Cycle Time (μ s)	I _{DD} (mA)	Port		Piggyback for Evaluation	Remarks
	Package	Pins & Variation	Package No.							Max. Voltage (V)	Max. Current (mA)		
1	LC66516B	DIP	64S	3071	16K	512×4	64*	0.92	4.5	15	20	LC66PG5XX LC66E516	With 3-level input, comparator input
2	LC66E516	DIP with window	64S	3126	16K	512×4	64*	0.92	4.5	15	20	-	Windowed EPROM version of LC66506B/508B/512B/516B
3	LC66P516	DIP	64S	3071	16K	512×4	64*	0.92	4.5	15	20	-	OTP PROM version of LC66506B/508B/512B/516B
4	LC67216A	DIP	64S	3071	16K	608×4	64*	0.65	6	V _{DD} -45	-30	-	AD input (4 bits×8ch), 3 timers, on-chip FLT automatic display controller

● 4 BITS, SINGLE CHIP LCD DRIVE TYPE (CMOS integrated circuit)

Type Number	Package	Pins & Variation	Package No.	Applications	ROM (Bytes)	RAM (Bits)	Stacks *(RAM)	Cycle Time (μ s)	I _{DD} (A)	Remarks
5	LC5732	Chip, QIP	64A	3057	2K	48×4	1	122	3 μ	LCD 81 segments (27 pins 1/3 duty)
6	LC5732H	QIP	64B	3026B	2K	48×4	1	10	300 μ	LCD 81 segments (27 pins 1/3 duty)
7	LC5733	Chip, QIP	64A	3057	2K×8	128×4	1	122	3 μ	LCD 81 segments (27 pins 1/3 duty)
8	LC5733H	QIP	64B	3026B	2K×8	128×4	1	17.6	300 μ	LCD 81 segments (27 pins 1/3 duty)
9	LC5734	Chip, QIP	64A	3057	2K×8	128×4	1	122	3 μ	LCD 81 segments (27 pins 1/3 duty)
10	LC5734H	QIP	64B	3026B	2K×8	128×4	1	17.6	300 μ	On-chip remote control carrier synthesizer
11	LC5738	Chip, QIP	64A	3057	4K×8	48×4	1	122	3 μ	LCD 128 segments (32 pins 1/4 duty)
12	LC5851N	QIP	64B	3026B	1K×15	64×4	4	120 to 40	3 μ /300 μ	LCD 75 segments (25 pins 1/3 duty)
13	LC5862H	Chip, QIP	80A	3044B	2K×16	256×4	8	2/122*	20 μ /1.7m	LCD 140 segments (35 pins 1/4 duty)
14	LC5863H	Chip, QIP	80A	3044B	3K×16	256×4	8	2/122*	20 μ /1.7m	LCD 140 segments (35 pins 1/4 duty)
15	LC5864H	Chip, QIP	80A	3044B	4K×16	256×4	8	2/122*	20 μ /1.7m	LCD 140 segments (35 pins 1/4 duty)
16	LC5866H	Chip, QIP	80A	3044B	6K×16	256×4	8	2/122*	20 μ /1.7m	LCD 140 segments (35 pins 1/4 duty)
17	LC5868H	Chip, QIP	80A	3044B	8K×16	256×4	8	2/122*	20 μ /1.7m	LCD 140 segments (35 pins 1/4 duty)

* : Low-speed mode

AUDIO-USE DSP (CMOS integrated circuit)

DSP : Digital Signal Processor

Type Number	Package	Pins & Variation	Package No.	Circuit Functions & Applications	Main Specifications	
18	LC93010	DIP	64S	3071	Audio-use DSP (Program RAM on chip)	Dual processor type, 32-bit ALU, 24-bit×16-bit multiplier, 320×32-bit program memory (RAM), sync serial I/O

MEMORIES (monolithic integrated circuit)

Type Number	Package	Pins & Variation	Package No.	Circuit Functions & Applications	Main Specifications	
19	LC3514A	DIP	18	3007A	CMOS 4K Static RAM (1K×4 Bits)	Address access time=150,200ns
20	LC3514AL	DIP	18	3007A	CMOS 4K Static RAM (1K×4 Bits)	Address access time=150,200ns, low standby current (I _{CCS} =1 μ A)
21	LC3514AD	DIP	18	3007A	CMOS 4K Static RAM (1K×4 Bits)	Wide operating voltage range (V _{CC} =3.0 to 6.0V), low standby current (I _{CCS} =1 μ A)
22	LC3517AD	DIP	24	3072	CMOS 16K Static RAM (2K×8 Bits), \overline{OE} , \overline{CE} Pins	Wide operating voltage range (V _{CC} =2.6 to 5.5V)
23	LC3517AMD	MFP	24	3045B	CMOS 16K Static RAM (2K×8 Bits), \overline{OE} , \overline{CE} Pins	Miniflat package version of LC3517AD
24	LC3517ASD	DIP	24W	3092	CMOS 16K Static RAM (2K×8 Bits), \overline{OE} , \overline{CE} Pins	Slim package version of LC3517AD
25	LC3517B	DIP	24	3072	CMOS 16K Static RAM (2K×8 Bits), \overline{OE} , \overline{CE} Pins	Address access time=120,150ns, low operating current : 9mA (f=1MHz)
26	LC3517BM	MFP	24	3045B	CMOS 16K Static RAM (2K×8 Bits), \overline{OE} , \overline{CE} Pins	Miniflat package version of LC3517B
27	LC3517BS	DIP	24	3114	CMOS 16K Static RAM (2K×8 Bits), \overline{OE} , \overline{CE} Pins	Slim package version of LC3517B
28	LC3517BL	DIP	24	3072	CMOS 16K Static RAM (2K×8 Bits), \overline{OE} , \overline{CE} Pins	Address access time=120,150ns, low standby current, low operating current : 9mA (f=1MHz)
29	LC3517BML	MFP	24	3045B	CMOS 16K Static RAM (2K×8 Bits), \overline{OE} , \overline{CE} Pins	Miniflat package version of LC3517BL
30	LC3517BSL	DIP	24	3114	CMOS 16K Static RAM (2K×8 Bits), \overline{OE} , \overline{CE} Pins	Slim package version of LC3517BL