

# HZM6.8ZMWA

Silicon Planar Zener Diode for Surge Absorb

## HITACHI

ADE-208-822A (Z)

Rev.1  
Nov. 2001

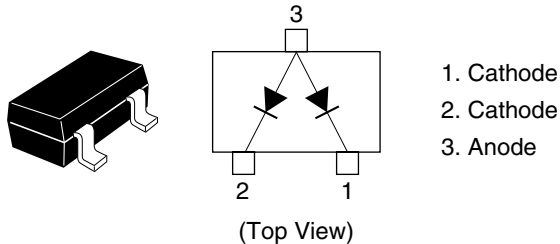
### Features

- HZM6.8ZMWA has two devices in a monolithic, and can absorb surge.
- Low capacitance ( $C = 25 \text{ pF max}$ ) and can protect ESD of signal line.
- MPAK Package is suitable for high density surface mounting and high speed assembly.

### Ordering Information

Type No.	Laser Mark	Package Code
HZM6.8ZMWA	68N	MPAK

### Pin Arrangement



## Absolute Maximum Ratings

( $T_a = 25^\circ\text{C}$ )

Item	Symbol	Value	Unit
Power dissipation	$P_d$ *	200	mW
Junction temperature	$T_j$	150	$^\circ\text{C}$
Storage temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

Note: Two device total, See Fig.2.

## Electrical Characteristics\*<sup>1</sup>

( $T_a = 25^\circ\text{C}$ )

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Zener voltage	$V_z$	6.47	—	7.00	V	$I_z = 5\text{ mA}$ , 40 ms pulse
Reverse current	$I_R$	—	—	2	$\mu\text{A}$	$V_R = 3.5\text{ V}$
Capacitance	C	—	—	25	pF	$V_R = 0\text{ V}$ , $f = 1\text{ MHz}$
Dynamic resistance	$r_d$	—	—	30	$\Omega$	$I_z = 5\text{ mA}$
ESD-Capability * <sup>2</sup> , * <sup>3</sup>	—	20	—	—	kV	C = 150 pF, R = 330 $\Omega$ , Both forward and reverse direction 10 pulse

Notes: 1. Per one device.

2. Failure criterion ;  $I_R > 2\text{ }\mu\text{A}$  at  $V_R = 3.5\text{ V}$ .

3. Between cathode and anode.

Main Characteristic

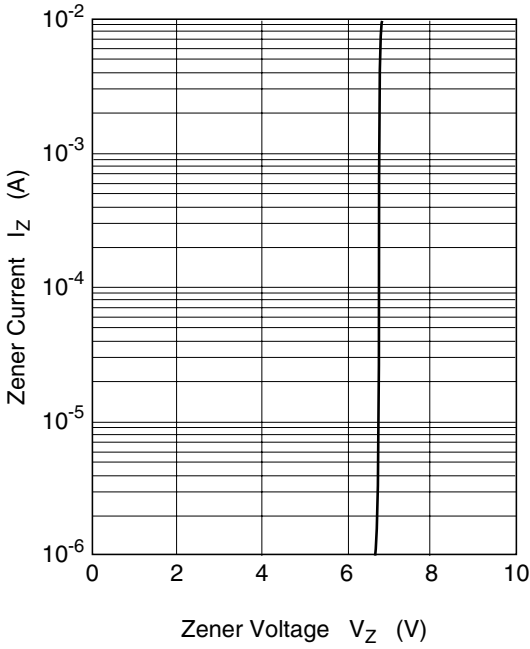


Fig.1 Zener current vs. Zener voltage

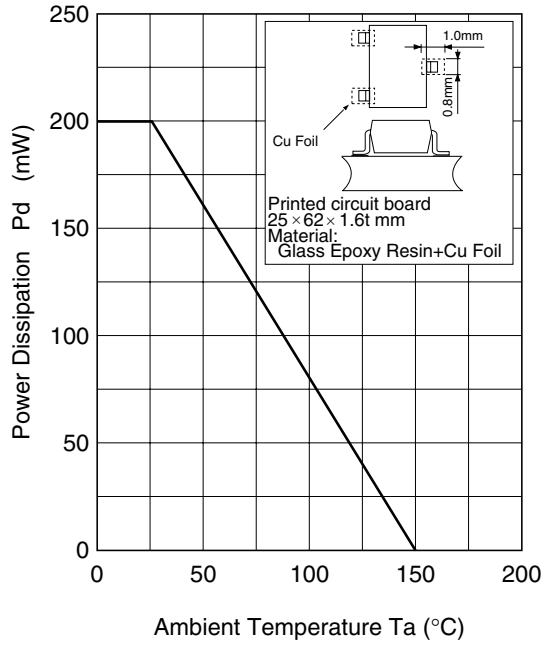


Fig.2 Power Dissipation vs. Ambient Temperature

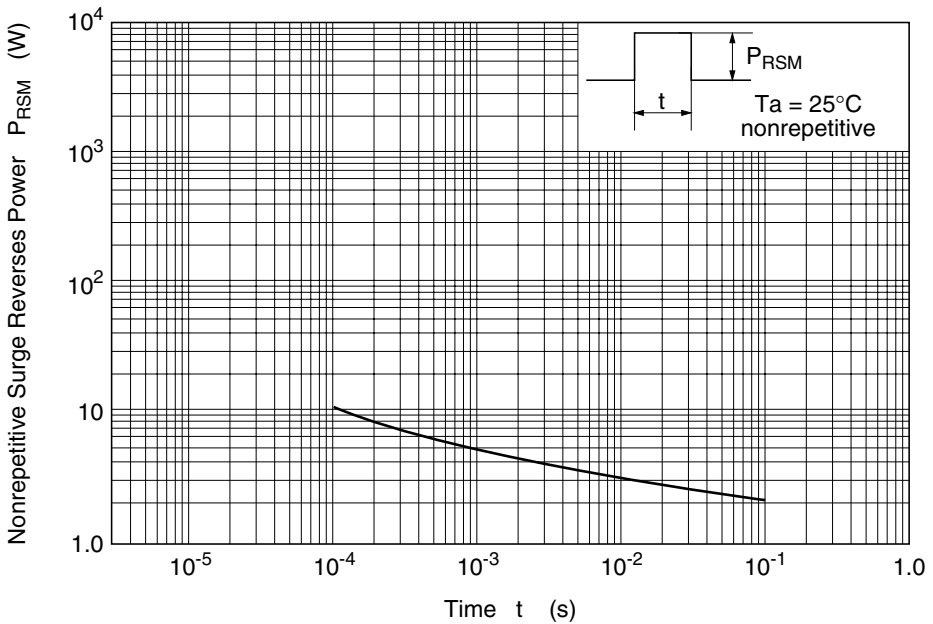


Fig.3 Surge Reverse Power Ratings

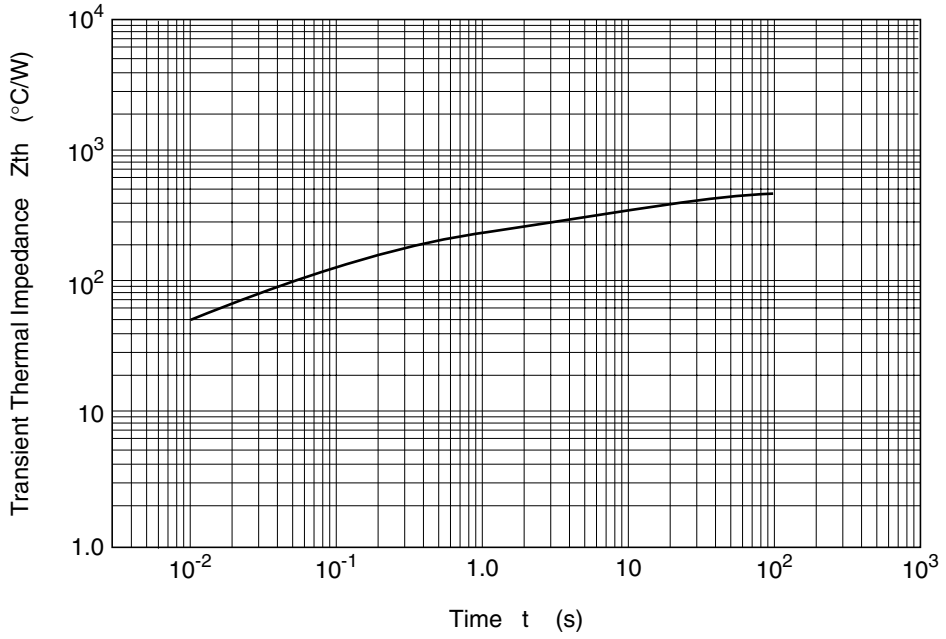
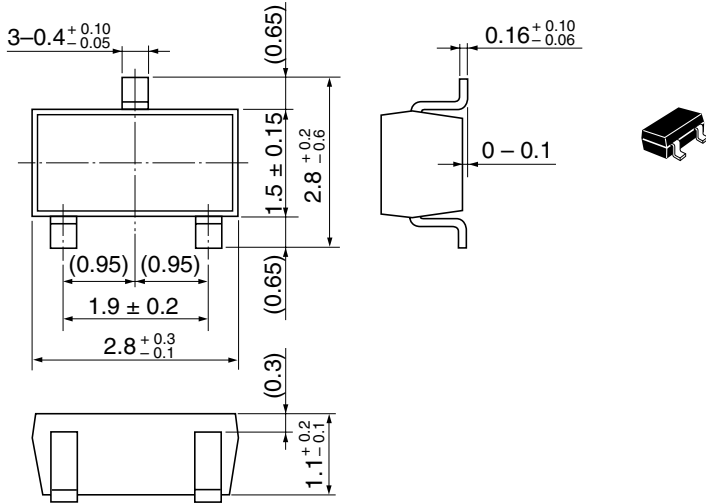


Fig.4 Transient Thermal Impedance

Package Dimensions

As of July, 2001  
Unit: mm



Hitachi Code	MPAK
JEDEC	—
JEITA	Conforms
Mass (reference value)	0.011 g

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## Sales Offices

# HITACHI

### Hitachi, Ltd.

Semiconductor & Integrated Circuits  
Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan  
Tel: (03) 3270-2111 Fax: (03) 3270-5109

URL <http://www.hitachisemiconductor.com/>

### For further information write to:

Hitachi Semiconductor (America) Inc.  
179 East Tasman Drive  
San Jose, CA 95134  
Tel: <1> (408) 433-1990  
Fax: <1> (408) 433-0223

Hitachi Europe Ltd.  
Electronic Components Group  
Whitebrook Park  
Lower Cookham Road  
Maidenhead  
Berkshire SL6 8YA, United Kingdom  
Tel: <44> (1628) 585000  
Fax: <44> (1628) 585200

Hitachi Europe GmbH  
Electronic Components Group  
Dornacher Straße 3  
D-85622 Feldkirchen  
Postfach 201, D-85619 Feldkirchen  
Germany  
Tel: <49> (89) 9 9180-0  
Fax: <49> (89) 9 29 30 00

Hitachi Asia Ltd.  
Hitachi Tower  
16 Collyer Quay #20-00  
Singapore 049318  
Tel: <65>-538-6533/538-8577  
Fax: <65>-538-6933/538-3877  
URL: <http://semiconductor.hitachi.com.sg>

Hitachi Asia Ltd.  
(Taipei Branch Office)  
4/F, No. 167, Tun Hwa North Road  
Hung-Kuo Building  
Taipei (105), Taiwan  
Tel: <886>-(2)-2718-3666  
Fax: <886>-(2)-2718-8180  
Telex: 23222 HAS-TP  
URL: <http://www.hitachi.com.tw>

Hitachi Asia (Hong Kong) Ltd.  
Group III (Electronic Components)  
7/F., North Tower  
World Finance Centre,  
Harbour City, Canton Road  
Tsim Sha Tsui, Kowloon Hong Kong  
Tel: <852>-(2)-735-9218  
Fax: <852>-(2)-730-0281  
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