

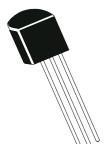
Continental Device India Limited

An IS/ISO 9002 and IECQ Certified Manufacturer





NPN/PNP SILICON PLANAR EPITAXIAL TRANSISTORS



BC635, 37, 39 (NPN) BC636, 38, 40 (PNP) TO 92 BCE

Driver Stages of Audio Amplifier Application.

ABSOLUTE MAXIMUM RATINGS(Ta=25 deg C)

DESCRIPTION	SYMBOL	BC635	BC637	BC639	UNITS
		BC636	BC638	BC640	
Collector -Base Voltage	VCBO	45	60	80	V
Collector -Emitter Voltage	VCEO	45	60	80	V
Emitter -Base Voltage	VEBO		5.0		V
Collector Current Continuous	IC		1.0		Α
Power Dissipation@ Ta=25 deg C	PD		800		mW
Derate Above 25 deg C			6.4		mW/deg C
Power Dissipation@ Tc=25 deg C			2.75		W
Derate Above 25 deg C			22		mW/deg C
Operating & Storage Junction	Tj, Tstg		-55 to +150		deg C
Temperature Range					
THERMAL RESISTANCE					
From Junction to Case	Rth(j-c)		45		deg C/W
From Junction to Ambient	Rth(j-a)		156		deg C/W

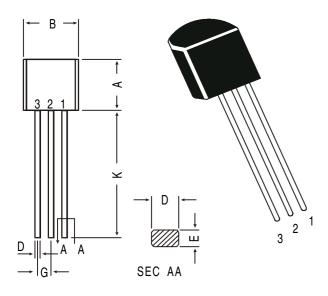
DESCRIPTION	SYMBOL	SYMBOL TEST CONDITION		BC637	BC639	UNITS
			BC636	BC638	BC640	
Collector -Emitter Voltage	VCEO *	IC=10mA,IB=0	>45	>60	>80	V
Collector -Base Voltage	VCBO	IC=100uA.IE=0	>45	>60	>80	V
Emitter-Base Voltage	VEBO	IE=10uA, IC=0		>5.0		V
Collector-Cut off Current	ICBO	VCB=30V, IE=0		<10	nA	
		Ta=125 deg C				
		VCB=30V, IE=0		<10		uA
Base Emitter on Voltage	VBE(on) *	IC=500mA,VCE=2V		<1.	<1.0	
Collector Emitter Saturation Voltage	VCE(Sat) *	Sat) * IC=500mA, IB=50mA <0.5			V	
DC Current Gain	hFE*	IC=5mA, VCE=2V		>25		
		IC=150mA,VCE=2V	40-250	40-160	40-160	
		Group-10		63 -160 100 -250		
		Group-16				
		IC=500mA,VCE=2V		>2	5	

ELECTRICAL CHARACTERISTICS (Ta=25 deg C Unless Otherwise Specified)

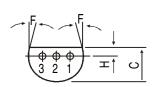
DESCRIPTION	SYMBOL TEST CONDITION			Value	UNITS
DYNAMIC CHARACTERISTICS					
Transistors Frequency	ft	IC=50mA,VCE=2V	NPN	Typ 200	MHz
		f=100MHz	PNP	Typ 150	MHz
Out-Put Capacitance	Cob	VCB=10V, f=1MHz	NPN	Typ 7.0	pF
			PNP	Typ 9.0	pF
In-Put Capacitance	Cib	VBE=0.5V, IC=0	NPN	Typ 50	pF
		f=1MHz	PNP	Typ 110	pF

^{*}Pulse Test: Pulse Width =300us, Duty CYCLE=2%

TO-92 Plastic Package



All diminsions in mm.

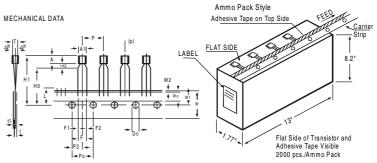


PIN CONFIGURATION

- 1. BASE
- 2. COLLECTOR
- 3. EMITTER

DIM	MIN.	MAX.
Α	4.32	5.33
В	4.45	5.20
O	3.18	4.19
D	0.41	0.55
Е	0.35	0.50
F	5 D	EG
G	1.14	1.40
Н	1.14	1.53
K	12.70	_

TO-92 Transistors on Tape and Ammo Pack



All dimensions in mm unless specified otherwise

ITEM		SPECIFICATION				D5144BW0	
IIIEM	SYMBOL	MIN.	MIN. NOM. MAX.		TOL.	REMARKS	
BODY WIDTH BODY HEIGHT	A1 <u>A</u>	4.0 4.8		4.8 5.2			
BODY THICKNESS PITCH OF COMPONENT	T P Po	3.9	12.7 12.7	4.2	±1	OUMUL ATIVE DITOU	
FEED HOLE PITCH FEED HOLE CENTRE TO	Ρ0		12.7		±0.3	CUMULATIVE PITCH ERROR 1.0 mm/20 PITCH	
COMPONENT CENTRE	P2		6.35		±0.4	TO BE MEASURED AT BOTTOM OF CLINCH	
DISTANCE BETWEEN OUTER LEADS	F		5.08		+0.6 -0.2		
COMPONENT ALIGNMENT TAPE WIDTH	∆h W		0 18 6	1	±0.5 ±0.2	AT TOP OF BODY	
HOLD-DOWN TAPE WIDTH HOLE POSITION	Wo W1		9		±0.2 +0.7 -0.5		
HOLD-DOWN TAPE POSITION LEAD WIRE CLINCH HEIGHT	W2 Ho		0.5 16		±0.2 ±0.5		
COMPONENT HEIGHT LENGTH OF SNIPPED LEADS	H1 L			23.25 11.0			
FEED HOLE DIAMETER TOTAL TAPE THICKNESS	Do t		4	1.2	±0.2	t1 0.3 - 0.6	
LEAD - TO - LEAD DISTANCEF1,	F2		2.54		+0.4 -0.1		
CLINCH HEIGHT PULL - OUT FORCE	H2 (P)	6N		3			

- 1. MAXIMUM ALIGNMENT DEVIATION BETWEEN LEADS NOT TO BE GREATER THAN 0.2 mm.
 2. MAXIMUM NON-CUMULATIVE VARIATION BETWEEN TAPE FEED HOLES SHALL NOT EXCEED 1 mm IN 20 PITCHES.
- PITCHES.

 3. HOLDDOWN TAPE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO EXPOSURE OF ADHESIVE.

 4. NO MORE THAN 3 CONSECUTIVE MISSING COMPONENTS ARE PERMITTED.

 5. A TAPE TRAILER, HAVING AT LEAST THREE FEED HOLES ARE REQUIRED AFTER THE LAST COMPONENT.

 6. SPLICES SHALL NOT INTERFERE WITH THE SPROCKET FEED HOLES.

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5.0K	17" x 15" x 13.5"	80.0K	23 kgs
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2.0K	17" x 15" x 13.5"	32.0K	12.5 kgs

Notes

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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