UF200 THRU UF2010

ULTRAFAST SWITCHING RECTIFIER VOLTAGE - 50 to 1000 Volts CURRENT - 2.0 Amperes

FEATURES

- Plastic package has Underwriters Laboratory
 Flammability Classification 94V-O utilizing
 Flame Retardant Epoxy Molding Compound
- Void-free Plastic in DO-15 package
- 2.0 ampere operation at T_A=55 **¢J** with no thermal runaway
- Exceeds environmental standards of MIL-S-19500/228
- Ultra fast switching for high efficiency

MECHANICAL DATA

Case: Molded plastic, DO-15

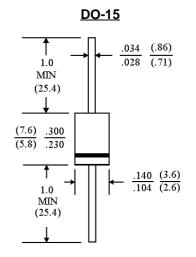
Terminals: Axial leads, solderable per MIL-STD-202,

Method 208

Polarity: Band denotes cathode

Mounting Position: Any

Weight: 0.015 ounce, 0.4 gram



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25 () ambient temperature unless otherwise specified.

Single phase, half wave, 60 Hz, resistive or inductive load.

	UF200	UF201	UF202	UF204	UF206	UF208	UF2010	UNITS
Peak Reverse Voltage, Pepetitive; V _{RM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	35	70	140	280	420	560	700	V
DC Blocking Voltage; VR	50	100	200	400	600	800	1000	V
Average Forward Current, Io @T _A =55 ¢J 3.8"				2.0				Α
lead length, 60Hz, resistive or inductive load								
Peak Forward Surge Current I _{FM} (surge)	60							Α
8.3msec. single half sine-wave								
superimposed on rated load (JEDEC method)								
Maximum Forward Voltage V _F @2.0A, 25 ¢J		1.00		1.10		1.70		V
Maximum Reverse Current, @ Rated T _J =25 ¢ J	10.0							£g A
Reverse Voltage T _J =100 ¢J	500							£g A
Typical Junction capacitance (Note 1) CJ	35							₽F
Typical Junction Resistance (Note 2) R £K JA	45							¢J/W
Reverse Recovery Time	50	50	50	50	75	75	75	ns
I _F =.5A, I _R =1A, Irr=.25A								
Operating and Storage Temperature Range	-55 TO +150							¢J

NOTES:

- 1. Measured at 1 MHz and applied reverse voltage of 4.0 VDC
- 2. Thermal resistance from junction to ambient and from junction to lead length 0.375"(9.5mm) P.C.B. mounted



RATING AND CHARACTERISTIC CURVES UF200 THRU UF2010

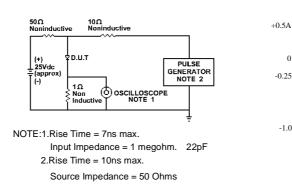


Fig. 1-REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

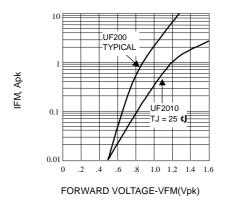


Fig. 2-FORWARD CHARACTERISTICS

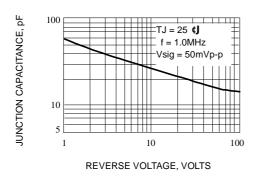
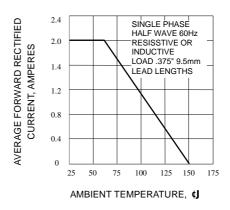


Fig. 4-TYPICAL JUNCTION CAPACITANCE



SET TIME

BASE FOR

50 ns/cm

Fig. 3-FORWARD CURRENT DERATING CURVE

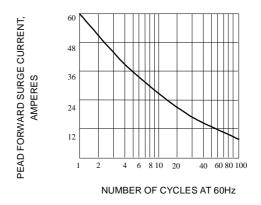


Fig. 5-PEAK FORWARD SURGE CURRENT

