TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSII<sup>-5</sup>)

# 2SK1365

#### **Switching Power Supply Applications**

Unit: mm

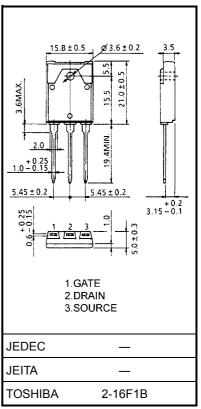
 $\begin{array}{ll} \bullet & Low\ drain-source\ ON\ resistance & \vdots\ R_{DS}\ (oN)=1.5\ \Omega\ (typ.) \\ \bullet & High\ forward\ transfer\ admittance & \vdots\ |Y_{fs}|=4.0\ S\ (typ.) \\ \bullet & Low\ leakage\ current & \vdots\ I_{DSS}=300\ \mu A\ (max)\ (V_{DS}=800\ V) \\ \bullet & Enhancement-mode & \vdots\ V_{th}=1.5{\sim}3.5\ V\ (V_{DS}=10\ V,\ I_{D}=1\ mA) \end{array}$ 

### **Maximum Ratings (Ta = 25°C)**

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	1000	V	
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		$V_{DGR}$	1000	V	
Gate-source voltage		$V_{GSS}$	±20	V	
Drain current	DC (Note 1)	I <sub>D</sub>	7	Α	
	Pulse (Note 1)	I <sub>DP</sub>	21	A	
Drain power dissipation (Tc = 25°C)		$P_{D}$	90	W	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55~150	°C	

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.39	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	41.6	°C/W



Weight: 5.8 g (typ.)

Note 1: Please use devices on condition that the channel temperature is below 150°C.

This transistor is an electrostatic sensitive device.

Please handle with caution.

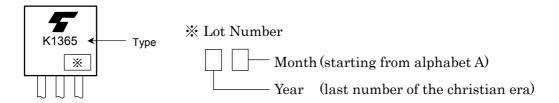
## **Electrical Characteristics (Ta = 25°C)**

Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V	_	_	±50	nA
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 800 V, V <sub>GS</sub> = 0 V	_	_	300	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	1000	_	_	V
Gate threshold v	/oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.5		3.5	V
Drain-source O	N resistance	R <sub>DS (ON)</sub>	I <sub>D</sub> = 4 A, V <sub>GS</sub> = 10 V	_	1.5	1.8	Ω
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 4 A	2.0	4.0	_	S
Input capacitano	e	C <sub>iss</sub>		_	1300	_	
Reverse transfe	r capacitance	C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	100	_	pF
Output capacitance		Coss		_	180	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS} = 100 \Omega$ $V_{GS} = 100 \Omega$ $V_{GS} = 100 \Omega$	_	25	_	- ns
	Turn-on time	t <sub>on</sub>		_	40	_	
	Fall time	t <sub>f</sub>		_	20	_	
	Turn-off time	t <sub>off</sub>	$V_{DD} = 400V$ Duty $\leq 1\%$ , $t_{W} = 10 \mu s$	_	100	_	
Total gate charg plus gate-drain)	e (Gate-source	Q <sub>g</sub>		_	120	_	nC
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 7 \text{ A}$	_	70		
Gate-drain ("miller") charge		$Q_{gd}$	]		50	_	

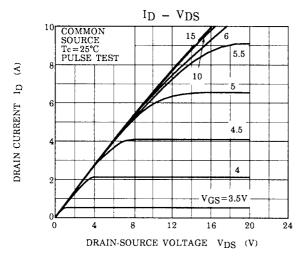
# Source-Drain Ratings and Characteristics (Ta = 25°C)

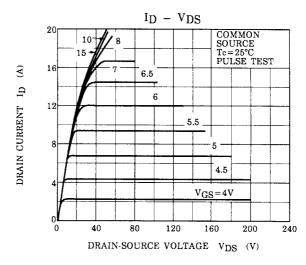
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	-	_	_	7	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	-	_	_	21	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 7 A, V <sub>GS</sub> = 0 V	_	_	-1.9	V

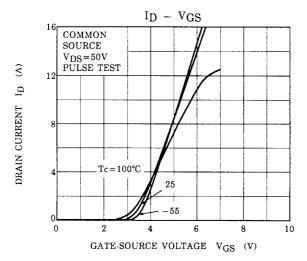
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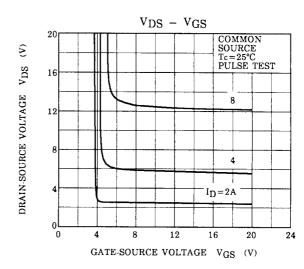


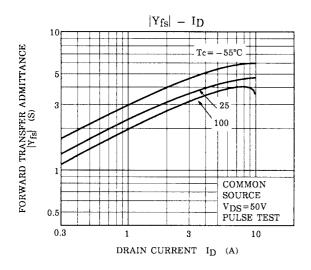
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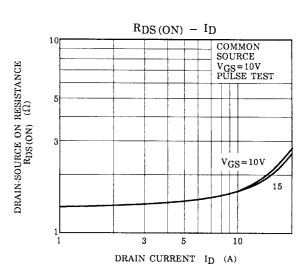




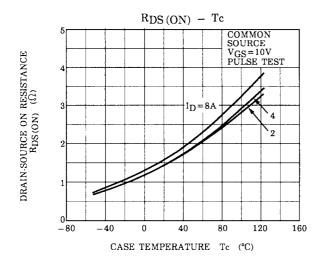


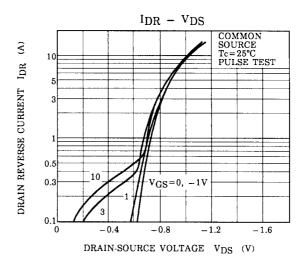


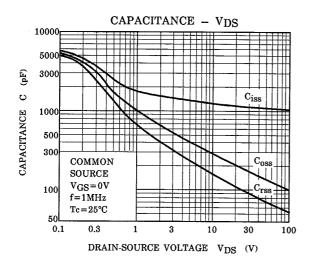


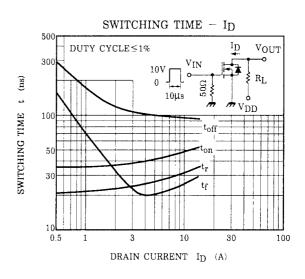


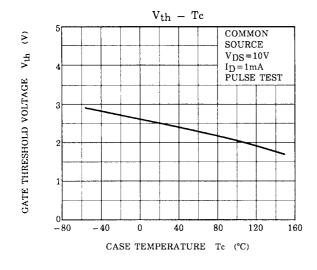
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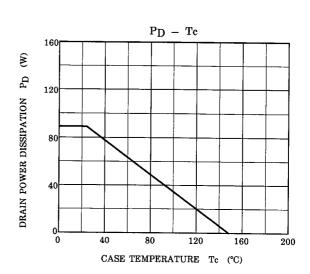




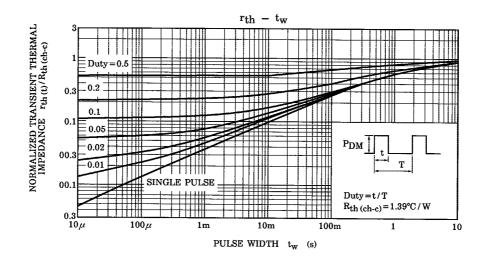


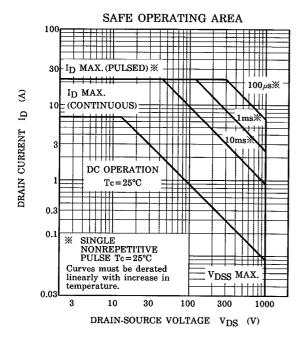






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