



# **BC847BS**

### **DUAL NPN SURFACE MOUNT SMALL SIGNAL TRANSISTOR**

### **Features**

Ideally Suited for Automatic Insertion

For Switching and AF Amplifier Applications

Ultra-Small Surface Mount Package

Lead Free/RoHS Compliant (Note 2)

Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

Case: SOT-363

Case Material: Molded Plastic. UL Flammability

Classification Rating 94V-0

Moisture Sensitivity: Level 1 per J-STD-020C

Terminals: Solderable per MIL-STD-202, Method 208

Lead Free Plating (Matte Tin Finish annealed over Alloy 42

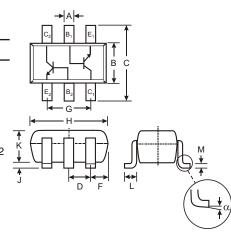
leadframe).

Terminal Connections: See Diagram

Marking: K1F (See Page 2)

Ordering & Date Code Information: See Page 2

Weight: 0.006 grams



	SOT-363										
Dim	Min	Max									
Α	0.10	0.30									
В	1.15	1.35									
С	2.00	2.20									
D	0.65 N	ominal									
F	0.30	0.40									
Н	1.80	2.20									
J		0.10									
K	0.90	1.00									
L	0.25	0.40									
М	0.10	0.25									
	8°										
All Din	nensions	in mm									

### Maximum Ratings @ T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	50	V
Collector-Emitter Voltage	V <sub>CEO</sub>	45	V
Emitter-Base Voltage	V <sub>EBO</sub>	5.0	V
Collector Current	I <sub>C</sub>	100	mA
Peak Collector Current	Ісм	200	mA
Peak Base Current	I <sub>BM</sub>	200	mA
Power Dissipation (Note 1)	P <sub>d</sub>	200	mW
Thermal Resistance, Junction to Ambient (Note 1)	R JA	500	°C/W
Operating and Storage Temperature Range	T <sub>j</sub> , T <sub>STG</sub>	-65 to +150	°C

Notes:

Device mounted on FR-4 PCB, 1 inch x 0.85 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

No purposefully added lead.



## Electrical Characteristics @ T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
DC Current Gain (Note 3)	h <sub>FE</sub>	200	_	450	_	$V_{CE} = 5.0V, I_{C} = 2.0mA$
Collector-Emitter Saturation Voltage (Note 3)	V <sub>CE(SAT)</sub>	_	-	100 400	mV	I <sub>C</sub> = 10mA, I <sub>B</sub> = 0.5mA I <sub>C</sub> = 100mA, I <sub>B</sub> = 5.0mA
Base-Emitter Saturation Voltage (Note 3)	V <sub>BE(SAT)</sub>	_	755	_	mV	$I_C = 10mA, I_B = 0.5mA$
Base-Emitter Voltage (Note 3)	V <sub>BE</sub>	580	665	700	mV	$V_{CE} = 5.0V, I_{C} = 2.0mA$
Collector Cutoff Current (Note 3)	I <sub>CBO</sub>	_	_	15 5.0	nΑ μΑ	V <sub>CB</sub> = 30V, I <sub>E</sub> = 0 V <sub>CB</sub> = 30V, T <sub>j</sub> = 125°C
Emitter Cutoff Current (Note 3)	I <sub>EBO</sub>	_	_	100	nA	$V_{EB} = 5.0V, I_{C} = 0$
Gain Bandwidth Product	f <sub>T</sub>	100	_	_	MHz	$V_{CE} = 5.0V, I_{C} = 10mA, f = 100MHz$
Collector-Base Capacitance	Ссво	_	_	1.5	pF	V <sub>CB</sub> = 10V, f = 1.0MHz
Emitter-Base Capacitance	C <sub>EBO</sub>	_	11	_	pF	V <sub>EB</sub> = 0.5V, f = 1.0MHz

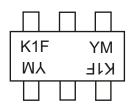
# **Ordering Information** (Note 4)

Device	Packaging	Shipping		
BC847BS-7-F	SOT-363	3000/Tape & Reel		

Notes: 3. Short duration pulse test used to minimize self-heating effect.

4. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

# **Marking Information**



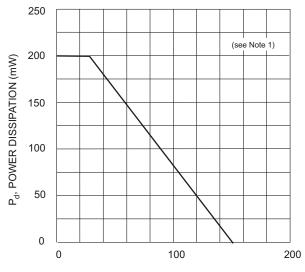
K1F = Product Type Marking Code YM = Date Code Marking Y = Year ex: N = 2002 M = Month ex: 9 = September

#### Date Code Key

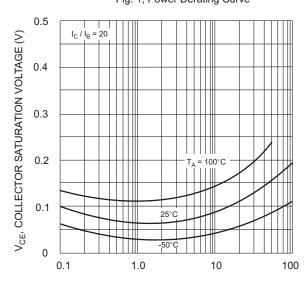
Year	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	J	K	L	М	Ν	Р	R	S	Т	U	V	W	X	Υ	Z

Month	Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



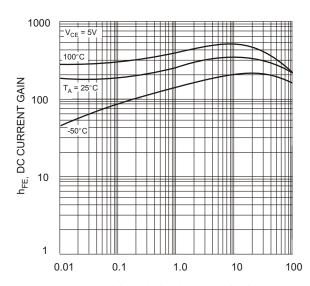


T<sub>A</sub>, AMBIENT TEMPERATURE (°C) Fig. 1, Power Derating Curve

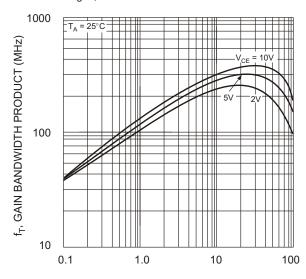


I<sub>C</sub>, COLLECTOR CURRENT (mA)
Fig. 3, Collector Saturation Voltage vs Collector Current

Notes: 1. Device mounted on FR4 printed circuit board.



I<sub>C</sub>, COLLECTOR CURRENT (mA) Fig. 2, DC Current Gain vs Collector Current



 $I_{\rm C}$ , COLLECTOR CURRENT (mA) Fig. 4, Gain Bandwidth Product vs Collector Current

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