

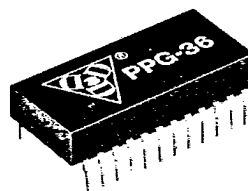
Programmable Pulse Generator

SERIES: PPG-36
(6-Bit) TTL Interfaced

data delay devices, inc.

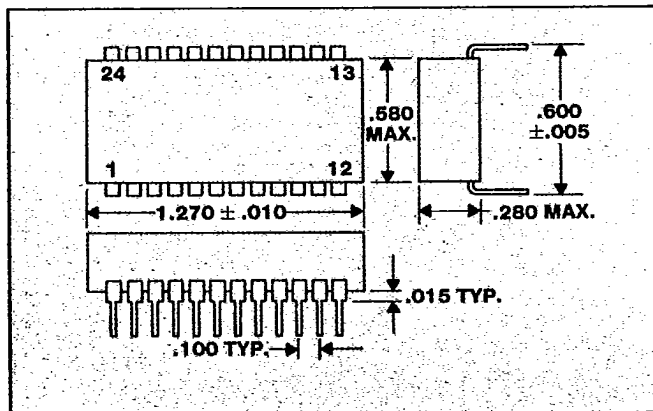
Features:

- Precise pulse width.
- Inverted & non-inverted outputs.
- 6-BIT address.
- 1 ns to 10 ns incremental steps.
- Positive pulse triggered.
- 24 pins DIP package.
- Low profile.



Specifications:

- Input signal requirement: TTL rising-edge.
- Output fan-out: TTL Schottky loads.
- Trigger inherent delay (T_{00}): 6 ns Typ.
- Inherent pulse-width (PW_0): 11 ns Typ.
- Pulse-width variation: monotonic in one direction.
- Programmed pulse-width tolerance: $\pm 5\%$ or 1 ns whichever is greater.
- Supply voltage: 5 Vdc $\pm 5\%$.
- Operating temperature: 0°C to 70°C .
- Temperature coefficient: 100 PPM/ $^\circ\text{C}$.
- Power dissipation: 900 mw Typ.



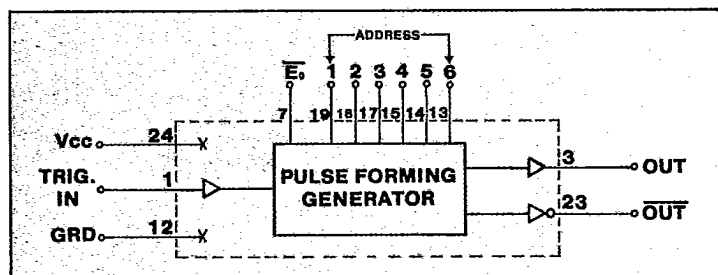
TRUTH TABLE

Address (Bit No.)						Enable (E_0)	Pulse-Width Out
8	5	4	3	2	1		
0	0	0	0	0	0	0	T_0
0	0	0	0	0	1	0	T_1
0	0	0	0	1	0	0	T_2
0	0	0	0	1	1	0	T_3
0	0	0	1	0	0	0	T_4
0	0	0	1	1	1	0	T_5
0	0	1	0	0	0	0	T_6
0	0	1	1	1	1	0	T_7
0	1	0	0	0	0	0	T_8
0	1	1	1	1	1	0	T_9
1	0	0	0	0	0	0	T_{10}
1	1	1	1	1	1	0	T_{11}
0	0	0	0	0	0	1	0

0 = Logic 0 1 = Logic 1 ϕ = Don't care.

T_0 = Reference or Inherent pulse-width of unit.

$T_1 - T_{11}$ = Multiplier of Incremental pulse-width.



Part Number	Incremental Pulse-Width (ns)	Total Programmed Pulse-Width (ns)
PPG-36-.5	.5 $\pm$.3	31.5
PPG-36-1	1 $\pm$.5	63
PPG-36-2	2 $\pm$.5	126
PPG-36-3	3 $\pm$ 1.0	189
PPG-36-4	4 $\pm$ 1.0	252
PPG-36-5	5 $\pm$ 1.5	315
PPG-36-6	6 $\pm$ 1.5	378
PPG-36-7	7 $\pm$ 1.5	441
PPG-36-8	8 $\pm$ 2.0	504
PPG-36-9	9 $\pm$ 2.0	567
PPG-36-10	10 $\pm$ 2.0	630

Other customized units available.