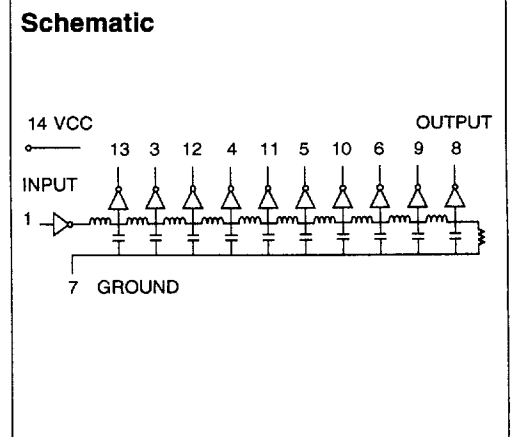


# 14 Pin DIP 10 Tap High Speed CMOS(HCT) Compatible Active Delay Lines

TAP DELAYS ±5% or ±2 nS†	TOTAL DELAYS ±5% or ±2 nS†	PART NUMBER	TAP DELAYS ±5% or ±2 nS†	TOTAL DELAYS ±5% or ±2 nS†	PART NUMBER
*4.0 ±1	48	EPA540-48	44	440	EPA540-440
*5.0 ±1	57	EPA540-57	45	450	EPA540-450
*6.0 ±1	66	EPA540-66	47	470	EPA540-470
*7.5 ±1	79.5	EPA540-79.5	50	500	EPA540-500
*10.0	102	EPA540-102	55	550	EPA540-550
12.5	125	EPA540-125	60	600	EPA540-600
15.0	150	EPA540-150	65	650	EPA540-650
17.5	175	EPA540-175	70	700	EPA540-700
20.0	200	EPA540-200	75	750	EPA540-750
22.5	225	EPA540-225	80	800	EPA540-800
25.0	250	EPA540-250	85	850	EPA540-850
30.0	300	EPA540-300	90	900	EPA540-900
35.0	350	EPA540-350	95	950	EPA540-950
40.0	400	EPA540-400	100	1000	EPA540-1000
42.0	420	EPA540-420			

†Whichever is greater. Delay times referenced from input to leading edges at 25°C, 5.0V. \* 1st tap is 12 ± 2 nS.

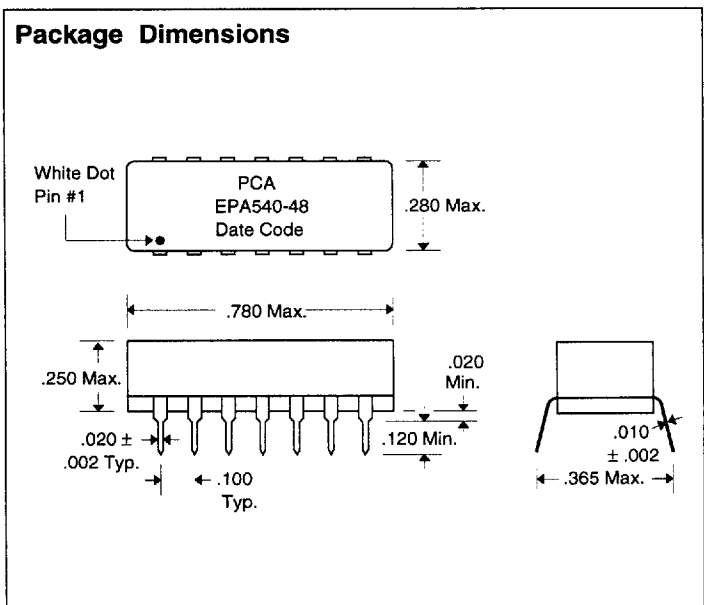
DC Electrical Characteristics		Test Conditions	Min	Max	Unit
Parameter					
V <sub>IH</sub>	High Level Input Voltage	V <sub>CC</sub> =4.5 to 5.5	2.0		Volt
V <sub>IL</sub>	Low Level Input Voltage	V <sub>CC</sub> =4.5 to 5.5		0.8	Volt
V <sub>OH</sub>	High Level Output Voltage	V <sub>CC</sub> =4.5V, I <sub>O</sub> =-4.0mA @V <sub>IH</sub> or V <sub>IL</sub>	4.0		Volt
V <sub>OL</sub>	Low Level Output Voltage	V <sub>CC</sub> =4.5V, I <sub>O</sub> = 4.0mA @V <sub>IH</sub> or V <sub>IL</sub>		0.3	Volt
I <sub>L</sub>	Input Leakage Current	V <sub>CC</sub> =5.5V @V <sub>IH</sub> or V <sub>IL</sub>		±1.0	µA
I <sub>CC</sub>	Supply Current	V <sub>CC</sub> =5.5V, V <sub>IN</sub> =0		15	mA
T <sub>RO</sub>	Output Rise Time	≤550 nS (.75 - 2.4 Volts) ≥550 nS (.75 - 2.4 Volts)	4		nS
I <sub>OH</sub>	High-Level Output Voltage			-4	nS
I <sub>OL</sub>	Low-Level Output Voltage			4	nS



Recommended Operating Conditions		Min	Max	Unit
V <sub>CC</sub>	DC Supply Voltage	4.5	5.5	Volt
V <sub>I</sub>	DC Input Voltage Range	0	V <sub>CC</sub>	Volt
V <sub>O</sub>	DC Output Voltage Range	0	V <sub>CC</sub>	Volt
P <sub>W</sub> *	Pulse Width % of Total Delay	40		%
D*	Duty Cycle		40	%
T <sub>A</sub>	Operating Free Air Temperature	0	70	°C

\*These two values are inter-dependent.

Input Pulse Test Conditions @ 25°C		Unit	
E <sub>IN</sub>	Pulse Input Voltage	3.2	Volts
P <sub>W</sub>	Pulse Width % of Total Delay	150	%
T <sub>RI</sub>	Input Rise Time (0.75 - 2.4 Volts)	2.0	nS
PRR	Pulse Repetition Rate @ PW ≤ 500nS	1.0	MHz
	Pulse Repetition Rate @ PW > 500nS	100	KHz
V <sub>CC</sub>	Supply Voltage	5.0	Volts



DSA540 Rev. A 2/5/96

6852109 0000596 496

QAF-CS01 Rev. B 8/25/94

Unless Otherwise Noted Dimensions in Inches

Tolerances:

Fractional = ± 1/32

.XX = ± .030 .XXX = ± .010



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