

HA17904A Series

Dual Operational Amplifier

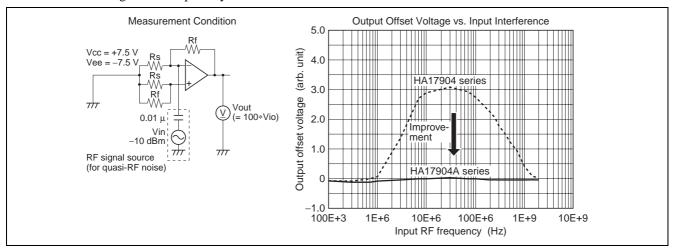
REJ03D0689-0200 Rev.2.00 Mar 10, 2006

Description

HA17904A series are dual operational amplifier that provide high gain and internal phase compensation, with single power supply. They can be widely applied to control equipments and to general use.

Features

- Wide range of supply voltage, and single power supply used
- Wide range of common mode voltage, and possible to operate with an input about 0 V, and output around 0 V is available
- Frequency characteristics and input bias current are temperature compensated
- Low electro-magnetic susceptibility level

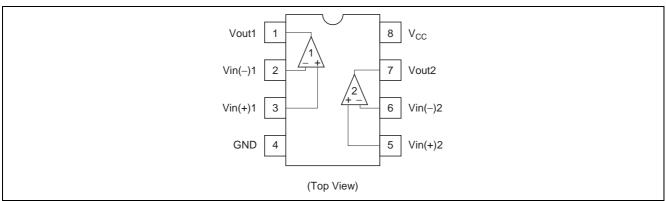


Ordering Information

Type No.	Application	Package Name	Package Code
HA17904APS	Industrial use	DIP-8 pin	PRDP0008AF-B
HA17904AFP		SOP-8 pin (JEITA)	PRSP0008DE-B
HA17904ARP		SOP-8 pin (JEDEC)	PRSP0008DD-C
HA17904AT		TSSOP-8 pin	PTSP0008JC-B

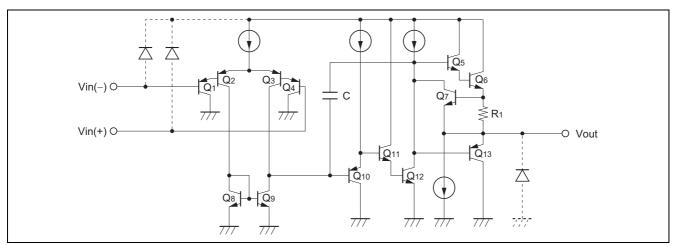


Pin Arrangement



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Circuit Schematic (1/2)



Note: If Input/Output terminals voltage over the absolute maximum ratings, there is possibility of mis-operation, characteristics deterioration and destruction, because of the current's flowing to parasitic diode in IC.

The Input/Output terminals are recommended to be protected with the clamp circuit which using the diode with low forward voltage (like schottky barrier diode) when there is a possibility for the Input/Output terminals voltage exceeds the absolute maximum ratings.

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item		Symbol	Ratings	Unit
Power supply voltage		V _{CC}	32	V
Output sink current		Iosink	50	mA
Common mode input voltage		V _{CM}	−0.3 to +V _{CC}	V
Differential input voltage		Vin(diff)	±V _{CC}	V
Output voltage		Vout	−0.3 to +V _{CC}	V
Allowable power dissipation DIP		P _T	570 * ¹	mW
	SOP		385 * ²]
	TSSOP		192 * ³]
Operating temperature		Topr	-40 to +85	°C
Storage temperature		Tstg	-55 to +125	°C

Notes: 1. HA17904APS:

This is the allowable values up to $Ta = 50^{\circ}C$. Derate by 8.3 mW/°C.

2. HA17904AFP/ARP:

These are the allowable values up to $Ta = 25^{\circ}C$ mounting in air.

When it is mounted on glass epoxy board of 40 mm \times 40 mm \times 1.5 mmt with 30% wiring density, the allowable value is 570 mW up to Ta = 45°C. If Ta > 45°C, derate by 7.14 mW/°C.

3. HA17904AT:

These are the allowable values up to $Ta = 25^{\circ}C$. Derate by 1.92 mW/°C above that temperature.

Electrical Characteristics

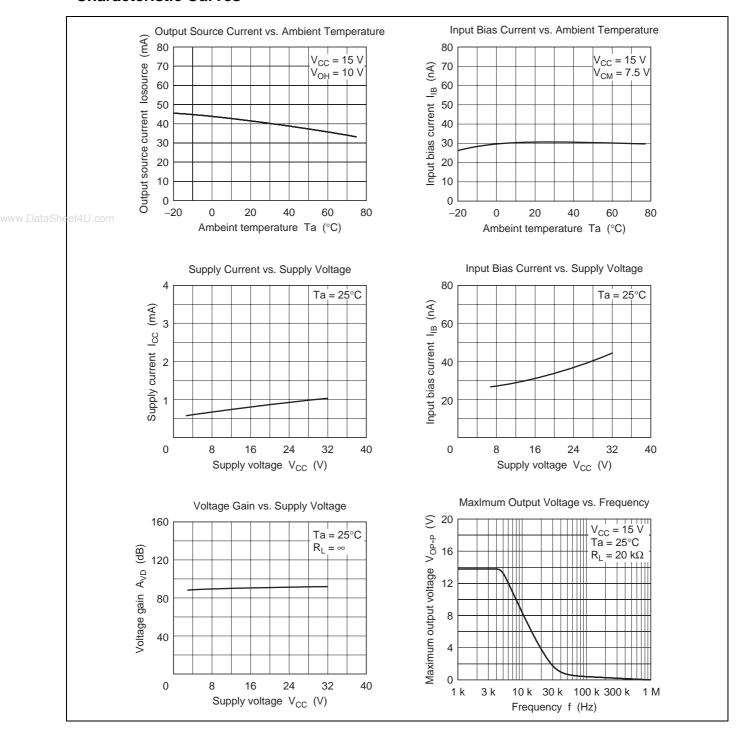
 $(V_{CC} = +15 \text{ V}, \text{ Ta} = 25^{\circ}\text{C})$

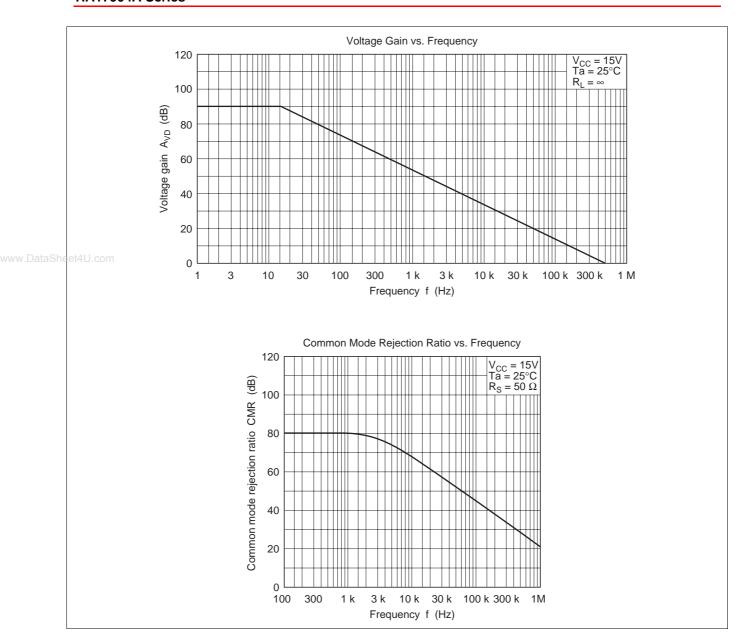
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Input offset voltage	V _{IO}	_	3	7	mV	$V_{CM} = 7.5V$, $R_S = 50\Omega$, $Rf = 50k\Omega$
Input offset current	I _{IO}	_	5	50	nA	$V_{CM} = 7.5V, I_{IO} = I_{I(+)} - I_{I(-)} $
Input bias current	I _{IB}	_	30	250	nA	$V_{CM} = 7.5V$
Power source rejection ratio	PSRR	_	93	_	dB	$R_S = 1k\Omega$, $Rf = 100k\Omega$
Voltage gain	A _{VD}	75	90	_	dB	$R_L = \infty$, $R_S = 1k\Omega$, $Rf = 100k\Omega$
Common mode rejection ratio	CMR	_	80	_	dB	$R_S = 50\Omega$, $Rf = 5k\Omega$
Common mode input voltage range	V _{CM (+)}	13.5	_	_	V	$R_S = 1k\Omega$, $Rf = 100k\Omega$
	V _{CM (-)}	_	_	-0.3	V	$R_S = 1k\Omega$, $Rf = 100k\Omega$
Peak-to-peak output voltage	V _{OP-P}	_	13.6	_	V	$\begin{split} f &= 100 \text{Hz}, \ R_L = 20 \text{k}\Omega, \ R_S = 1 \text{k}\Omega, \\ Rf &= 100 \text{k}\Omega \end{split}$
Output source current	Iosource	20	40	_	mA	$V_{IN}^{+} = 1V, V_{IN}^{-} = 0V, V_{OH} = 10V$
Output sink current	Iosink	10	20	_	mA	$V_{IN}^- = 1V, V_{IN}^+ = 0V, V_{OL} = 2.5V$
		15	50	_	μΑ	$V_{IN}^- = 1V$, $V_{IN}^+ = 0V$, $V_{OU} = 200 \text{mV}$
Supply current	Icc		0.8	2	mA	$V_{IN} = GND, R_L = \infty$
Slew rate	SR	_	0.2	_	V/μs	$R_L = \infty$, $V_{CM} = 7.5V$, $f = 1.5kHz$
Channel separation *1	CS	_	(120)	_	dB	f = 1kHz

Note: 1. Design spec.

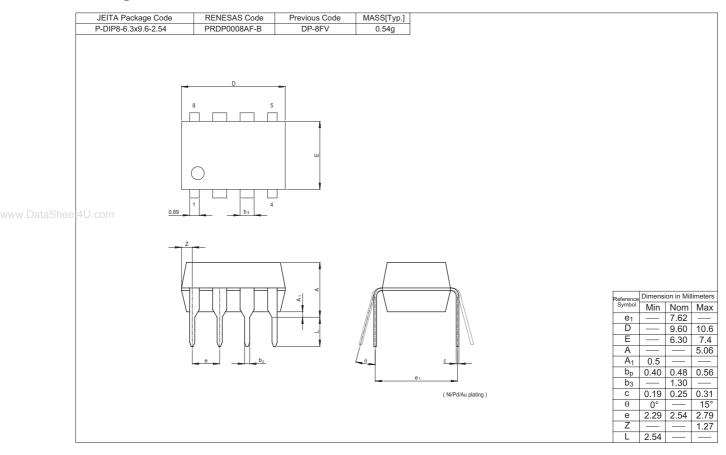


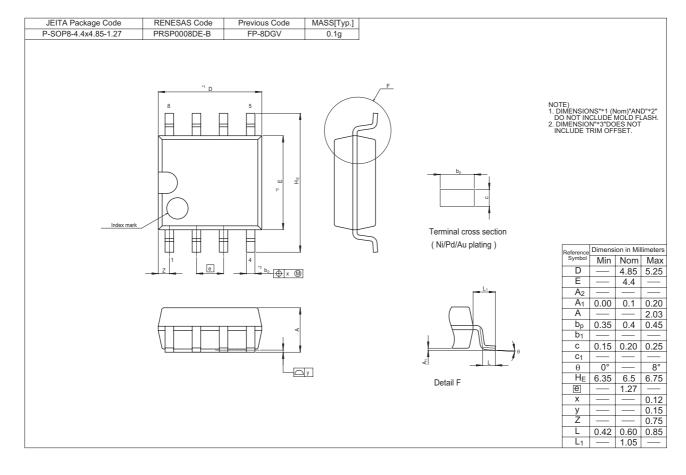
Characteristic Curves

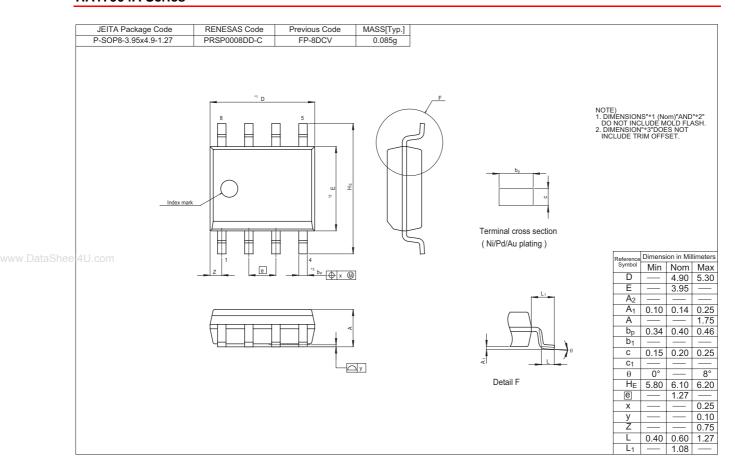


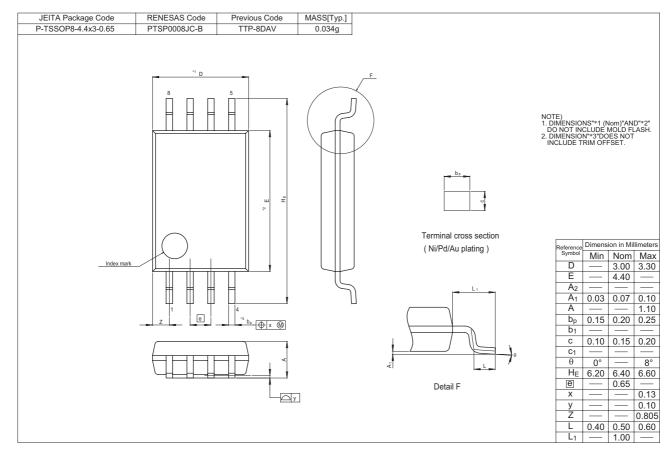


Package Dimensions









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