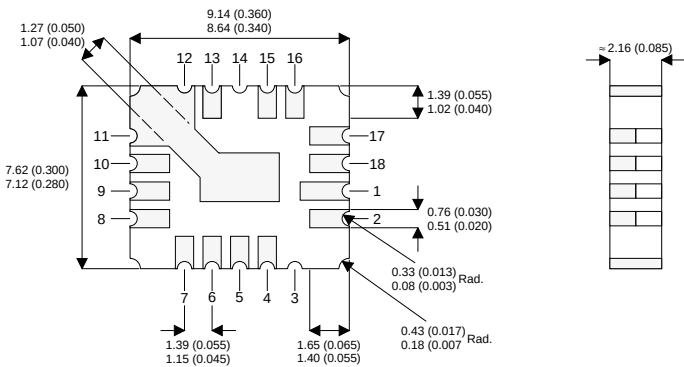


MECHANICAL DATA

Dimensions in mm



LCC4

TRANSISTOR	PINS
BASE	4,5
COLLECTOR	1,2,15,16,17,18
EMITTER	6,7,8,9,10,11,12,13

ADVANCED DISTRIBUTED BASE DESIGN HIGH VOLTAGE HIGH SPEED NPN SILICON POWER TRANSISTOR

- SEMEFAB DESIGNED AND DIFFUSED DIE
- HIGH VOLTAGE
- FAST SWITCHING
- HIGH ENERGY RATING

FEATURES

- Multi-base for efficient energy distribution across the chip resulting in significantly improved switching and energy ratings across full temperature range.
- Ion implant and high accuracy masking for tight control of characteristics from batch to batch.
- Triple Guard Rings for improved control of high voltages.

ABSOLUTE MAXIMUM RATINGS ($T_{case} = 25^{\circ}C$ unless otherwise stated)

V_{CBO}	Collector – Base Voltage ($I_E = 0$)	200V
V_{CEO}	Collector – Emitter Voltage ($I_B = 0$)	100V
V_{EBO}	Emitter – Base Voltage ($I_C = 0$)	10V
I_C	Continuous Collector Current	8A
$I_{C(PK)}$	Peak Collector Current	12A
I_B	Base Current	2A
P_{tot}	Total Dissipation at $T_{case} = 25^{\circ}C$	5W
$R_{\theta j-c}$	Thermal Resistance Junction to Case	$25^{\circ}C/W$
T_{stg}	Operating and Storage Temperature Range	-55 to $+150^{\circ}C$

ELECTRICAL CHARACTERISTICS ($T_{\text{case}} = 25^{\circ}\text{C}$ unless otherwise stated)

Parameter		Test Conditions		Min.	Typ.	Max.	Unit
ELECTRICAL CHARACTERISTICS							
V _{CEO(sus)}	Collector – Emitter Sustaining Voltage	I _C = 10mA		100			V
V _{(BR)CBO}	Collector – Base Breakdown Voltage	I _C = 1mA		250			
V _{(BR)EBO}	Emitter – Base Breakdown Voltage	I _E = 1mA		10			
I _{CBO}	Collector – Base Cut–Off Current	V _{CB} = 250V T _C = 125°C				10 100	μA
I _{CEO}	Collector – Emitter Cut–Off Current	I _B = 0	V _{CE} = 100V			100	
I _{EBO}	Emitter Cut–Off Current	V _{EB} = 9V I _C = 0 T _C = 125°C				10 100	μA
h _{FE} *	DC Current Gain	I _C = 0.3A	V _{CE} = 4V	30		80	
		I _C = 3A	V _{CE} = 4V	25		60	
		I _C = 5A	V _{CE} = 4V	20		50	
		T _C = 125°C					
V _{CE(sat)} *	Collector – Emitter Saturation Voltage	I _C = 1A	I _B = 0.1A			0.2	V
		I _C = 3A	I _B = 0.3A			0.5	
		I _C = 5A	I _B = 0.5A			0.8	
V _{BE(sat)} *	Base – Emitter Saturation Voltage	I _C = 3A	I _B = 0.3A			1.1	V
		I _C = 5A	I _B = 0.5A			1.3	
DYNAMIC CHARACTERISTICS							
f _t	Transition Frequency	I _C = 0.2A	V _{CE} = 4V		20		MHz
C _{ob}	Output Capacitance	V _{CB} = 10V	f = 1MHz		44		pF

* Pulse test $t_{\text{p}} = 300\mu\text{s}$, $\delta < 2\%$