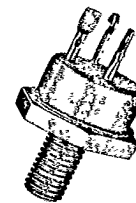


20 AMP/NPN**Pirgo Silicon Planar
Power Transistors**

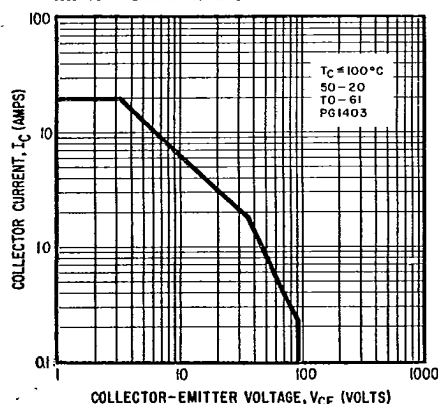
**20 AMP
90 VOLT
65 WATT
40 MHz
TO-61**



TO-61

- Linear h_{FE} from 50 mA to 20 amps
- Low saturation voltage at maximum collector current
- High frequency $f_t = 40$ MHz (typical)
- High voltage, $BV_{CEO(sus)}$ to 90 volts

NPN Type	Package	BV_{CBO}	$BV_{CEO(sus)}$	BV_{EBO}	h_{FE}			h_{FE}		$V_{CE(sat)}$		C_{obMax} pf	P_{DW} @ 100° C
					Min	Max	@ I_{CA}	Min	@ I_{CA}	Max	@ I_{CA}		
PG1400	TO-61	60	30	8	40	120	10	10	20	1.5	20	700	65
PG1401	TO-61	80	50	8	40	120	10	10	20	1.5	20	700	65
PG1402	TO-61	100	70	8	40	120	10	10	20	1.5	20	700	65
PG1403	TO-61	100	90	8	40	120	10	10	20	1.5	20	700	65
PG1404	TO-61	60	30	8	20	60	10	5	20	2.0	20	700	65
PG1405	TO-61	80	50	8	20	60	10	5	20	2.0	20	700	65
PG1406	TO-61	100	70	8	20	60	10	5	20	2.0	20	700	65
PG1407	TO-61	100	90	8	20	60	10	5	20	2.0	20	700	65
PG1408	TO-61	60	30	8	100	300	10	15	20	1.5	20	700	65
PG1409	TO-61	80	50	8	100	300	10	15	20	1.5	20	700	65
PG1410	TO-61	100	70	8	100	300	10	15	20	1.5	20	700	65
PG1411	TO-61	100	90	8	100	300	10	15	20	1.5	20	700	65
PG1412	TO-61	60	30	6	20	60	10			.7	10	700	65
PG1413	TO-61	80	50	6	20	60	10			.7	10	700	65
PG1414	TO-61	100	70	6	20	60	10			.7	10	700	65
PG1415	TO-61	100	90	6	20	60	10			.7	10	700	65
PG1416	TO-61	60	30	6	40	120	10			.7	10	700	65
PG1417	TO-61	80	50	6	40	120	10			.7	10	700	65
PG1418	TO-61	100	70	6	40	120	10			.7	10	700	65
PG1419	TO-61	100	90	6	40	120	10			.7	10	700	65
PG1420	TO-61	60	30	6	100	300	10			.7	10	700	65
PG1421	TO-61	80	50	6	100	300	10			.7	10	700	65
PG1422	TO-61	100	70	6	100	300	10			.7	10	700	65
PG1423	TO-61	100	90	6	100	300	10			.7	10	700	65
2N5313	TO-61	80	80	6	30	90	10	5	20	1.5	10	500	50
2N5315	TO-61	100	100	6	30	90	10	5	20	1.5	10	500	50

MAXIMUM SAFE OPERATING AREA**NORMALIZED h_{FE} Vs I_C** 