TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSⅢ)

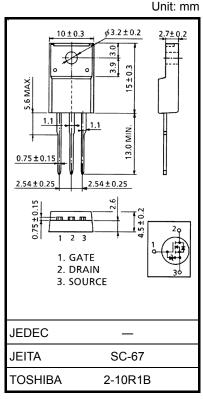
2SK3844

Switching Regulator, DC-DC Converter Applications Motor Drive Applications

- Low drain-source ON resistance: RDS (ON) = 4.1 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 63 \text{ S (typ.)}$
- Low leakage current: $IDSS = 100 \mu A (max)(VDS = 60 V)$
- Enhancement mode: $V_{th} = 2.0 \text{ to } 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	60	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	60	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	45	Α	
	Pulse (Note 1)	I _{DP}	180	A	
Drain power dissipation (Tc=25°C)		PD	45	W	
Single pulse avalanche energy (Note 2)		E _{AS}	527	mJ	
Avalanche current		I _{AR}	45	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	4.0	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: VDD = 25 V, Tch = 25°C (initial), L = 353 μ H, IAR = 45 A, RG = 25 Ω

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.



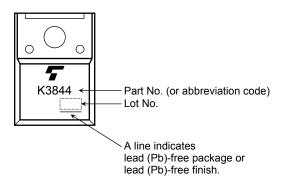
Electrical Characteristics (Ta = 25°C)

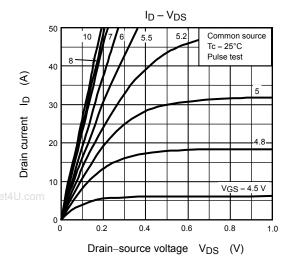
Cha	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Drain cut-off current		I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	60	_	_	V
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	35	_	_	V
Gate threshold voltage		V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source ON	resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 23 A	_	4.1	5.8	mΩ
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 23 A	32	63		S
Input capacitance		C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	12400		pF
Reverse transfer capacitance		C _{rss}		_	700		
Output capacitance		Coss		_	1100		
Switching time	Rise time	t _r	VGS $\frac{10 \text{ V}}{0 \text{ V}}$ $\frac{\text{ID} = 23 \text{ A}}{\text{CE}}$ $\frac{\text{CV}}{\text{CE}}$ $\frac{\text{CE}}{\text{V}}$ $\frac{\text{CE}}{\text{CE}}$ $\frac{\text{CE}}{$		18		ns
	Turn-on time	t _{on}			45		
	Fall time	t _f			35		
	Turn-off time	t _{off}	Duty ≦ 1%, t _w = 10 μs		200	_	
Total gate charge (gate-source plus gate-drain)		Qg			196	_	nC
Gate-source charge		Q _{gs}	$V_{DD} \simeq 48 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 45 \text{ A}$	_	148	_	
Gate-drain ("miller") charge		Q _{gd}		_	48	_	

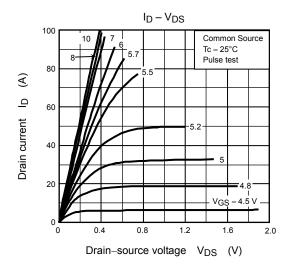
Source-Drain Ratings and Characteristics (Ta = 25°C)

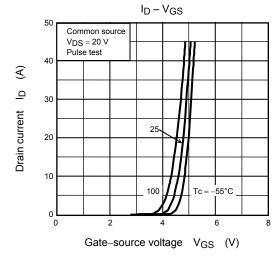
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1	I _{DR} 1	_	_	_	45	Α
Pulse drain reverse current (Note 1) I _{DRP}	_	_	_	180	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 45 A, V _{GS} = 0 V	_	_	-1.5	V
Reverse recovery time	t _{rr}	I _{DR} = 45 A, V _{GS} = 0 V,	_	67	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 50 A/μs	_	70	_	nC

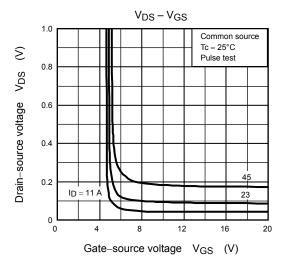
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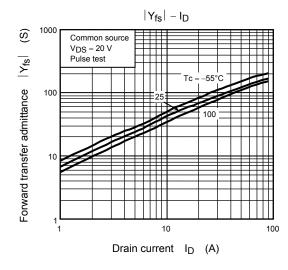


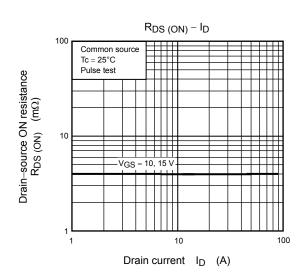


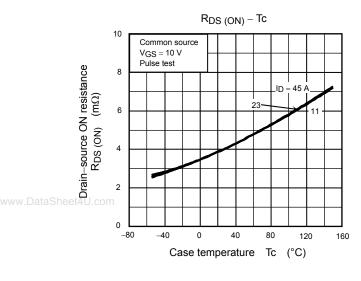


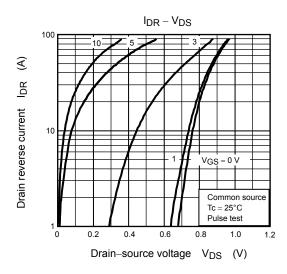


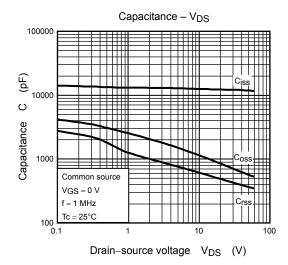


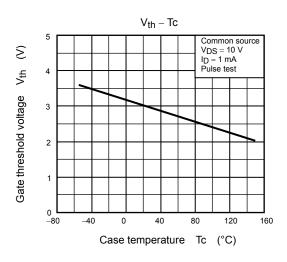


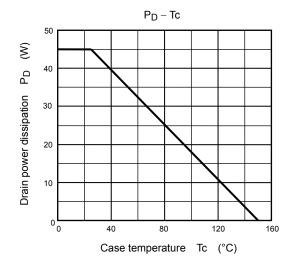


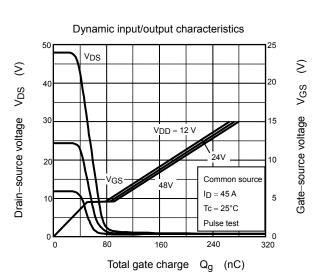


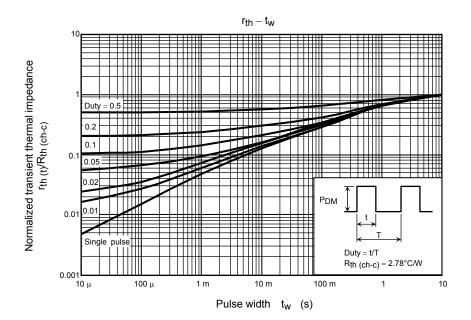




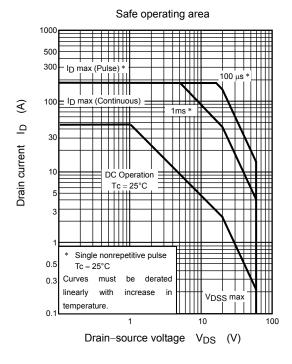


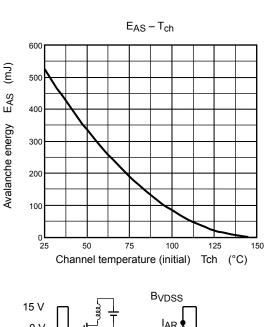


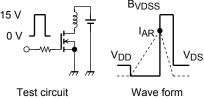




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$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 25~V,~L = 353~\mu H \end{aligned} \qquad E_{AS} &= \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - VDD} \right) \end{aligned}$$

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