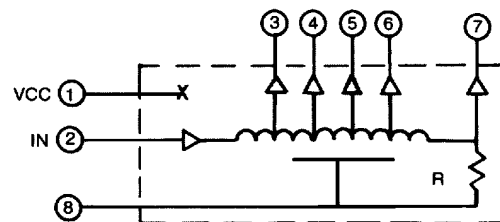
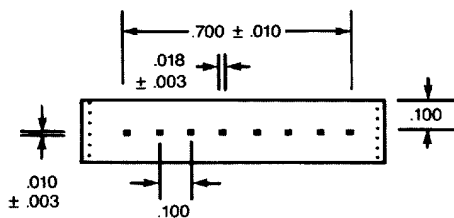
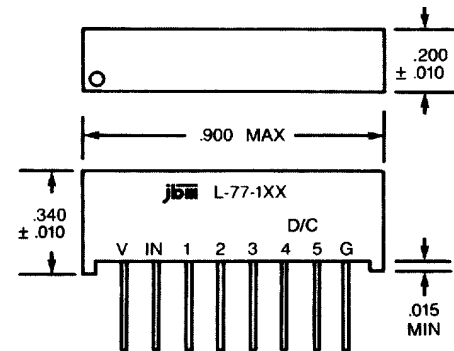


T-47-13

L-77-1XX SERIES

5 Tap Active SIP

**INPUT CONDITIONS**

(See page 2)

Operating Temperature 0° to + 70°C

Storage Temperature - 55°C to + 125°C

(All lines internally terminated)

 I_{IH} Logic "1" Input Current 50 μ A Max. I_{IL} Logic "0" Input Current - 2mA Max. V_{OH} Logic "1" Voltage Out 2.7 V Min. V_{OL} Logic "0" Voltage Out 0.5V Max.**DRIVE CAPABILITIES
(TTL LOADS)**

Logic "0" { 10 Loads/Tap Max.
20 Loads/Unit Max.

Logic "1" { 20 Loads/Unit Max.

Supply Current 75mA Typ.

Voltage Range 4.75V to 5.25V

JBM P/N	ALL DELAY TIMES MEASURED IN NANOSECONDS (ns) @ 1.5V LEVEL, @ 25°C.					TRO .8/2.0V
	TAP 1	TAP 2	TAP 3	TAP 4	OUTPUT	
L-77-100	4 ± 2	8 ± 2	12 ± 2	16 ± 2	20 ± 2	2ns MAX
L-77-101	6 ± 2	10 ± 2	15 ± 2	20 ± 2	25 ± 2	2ns MAX
L-77-102	6 ± 2	12 ± 2	18 ± 2	24 ± 2	30 ± 2	2ns MAX
L-77-103	7 ± 2	14 ± 2	21 ± 2	28 ± 2	35 ± 2	2ns MAX
L-77-104	8 ± 2	16 ± 2	24 ± 2	32 ± 2	40 ± 2	2ns MAX
L-77-105	9 ± 2	18 ± 2	27 ± 2	36 ± 2	45 ± 2.3	2ns MAX
L-77-106	10 ± 2	20 ± 2	30 ± 2	40 ± 2	50 ± 2.5	2ns MAX
L-77-107	15 ± 2	30 ± 2	45 ± 2.5	60 ± 3	75 ± 3.7	2ns MAX
L-77-108	20 ± 2	40 ± 2	60 ± 3	80 ± 4	100 ± 5	2ns MAX
L-77-109	25 ± 2	50 ± 2.5	75 ± 4	100 ± 5	125 ± 6.3	2.5ns MAX
L-77-110	30 ± 2	60 ± 3	90 ± 4.5	120 ± 6	150 ± 7.5	2.5ns MAX
L-77-111	40 ± 2	80 ± 4	120 ± 6	160 ± 8	200 ± 10	3ns MAX
L-77-112	50 ± 2.5	100 ± 5	150 ± 7.5	200 ± 10	250 ± 12.5	3ns MAX
L-77-113	60 ± 3	120 ± 6	180 ± 9	240 ± 12	300 ± 15	3.5ns MAX
L-77-114	80 ± 4	160 ± 8	240 ± 12	320 ± 16	400 ± 20	4ns MAX
L-77-115	100 ± 5	200 ± 10	300 ± 15	400 ± 20	500 ± 25	4ns MAX