

May 1990

Operational Amplifiers

For Commercial, Industrial, and Military Applications

Features:

- Short-circuit protection and latch-free operation
- Unity-gain phase compensation with a single 30-pF capacitor
- Replacement for industry types 101, 201, 301A
- CA301A Slew Rate (Summing ampl.) 10 V/μs

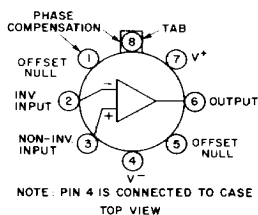
Applications:

- Long-interval integrator
- Timers
- Sample-and-hold circuits
- Summing amplifiers
- Multivibrators
- Comparators
- Instrumentation
- AC/DC converters
- Inverting amplifiers
- Sine- & square-wave generators
- Capacitance multipliers & simulated inductors

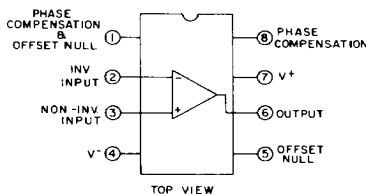
The CA101, CA201, and CA301A are general-purpose, high-gain operational amplifiers for use in military, industrial, and commercial applications.

These types, which are externally phase compensated, permit a choice of operation for optimum high-frequency performance at a selected gain; unity-gain compensation can be obtained with a single 30-pF capacitor.

All types are available in 8-lead TO-5 style packages with standard leads (T suffix), and with dual-in-line formed leads ("DIL-CAN", S suffix). The CA301A is also available in the 8-lead dual-in-line plastic package ("MINI-DIP", E suffix), and in chip form (H suffix).



(a) TO-5 Style package for all types
T-Suffix
S-Suffix



(b) Plastic package for CA301A
E-Suffix

Figure 1 - Functional diagrams.

*Technical Data on LM Branded types is identical to the corresponding CA Branded types.

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File Number **786**

Maximum Ratings, Absolute Maximum Values at $T_A = 25^\circ\text{C}$:

CA101, CA201	44 V
CA301A	36 V

DC INPUT VOLTAGE ± 15 V
(For supply voltages less than ± 15 V, the Input Voltage rating is equal to the DC Supply Voltage)

DIFFERENTIAL INPUT VOLTAGE	±30 V
OUTPUT SHORT-CIRCUIT DURATION	Indefinite*

DEVICE DISSIPATION:

UP TO $T_A = 75^\circ\text{C}$ 500 mW

Above $T_A = 75^\circ\text{C}$ Derate linearly at 6.67 mW/ $^\circ\text{C}$

AMBIENT TEMPERATURE RANGE:

Operating —

CA101 -55 to +125° C

CA201, CA301A..... 0 to +70°C

Storage (All types) -65 to +150° C

LEAD TEMPERATURE (During Soldering):

At a distance $1/16'' \pm 1/32''$ (1.59 $\pm$ 0.79 mm) from case for 10 seconds max. +265°C

* At $T_A \leq 70^\circ\text{C}$ and $T_C \leq 125^\circ\text{C}$ (CA101); $T_A \leq 55^\circ\text{C}$ and $T_C \leq 70^\circ\text{C}$ (CA201, CA301A).

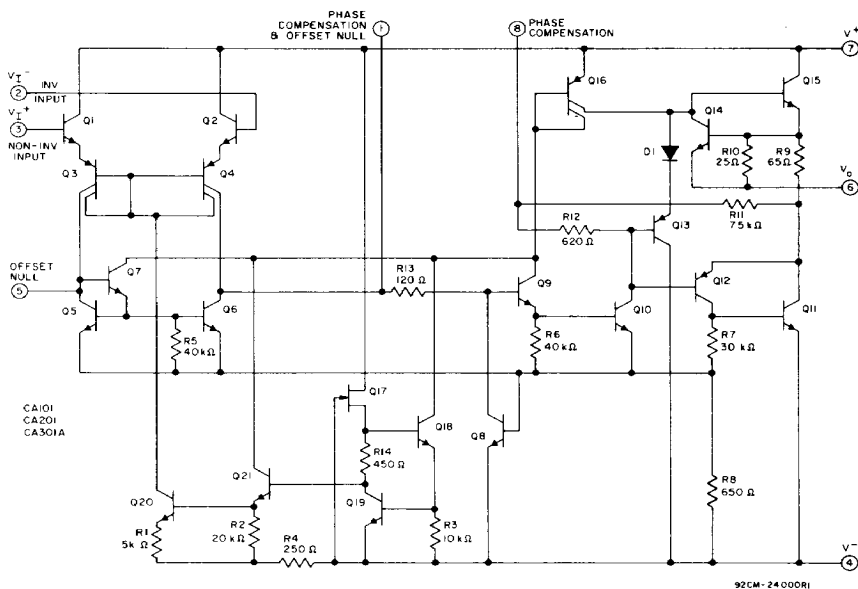


Fig. 2 - Schematic diagram.

CA101, CA201, CA301A, LM201, LM301A

ELECTRICAL CHARACTERISTICS

CHARACTER- ISTICS	TEST CONDITIONS Δ		LIMITS									UNITS
	Supply Voltage (V±) = 5 to 15 V	CA101			CA201			CA301A				
		Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.		
Input Offset Voltage <i>V_{IO}</i>	<i>T_A</i> =25° C <i>R_S</i> ≤10kΩ	—	1	5	—	2	7.5	—	—	—	mV	
	<i>R_S</i> ≤50kΩ	—	—	—	—	—	—	—	2	7.5		
	<i>R_S</i> ≤10kΩ	—	—	6	—	—	10	—	—	—		
	<i>R_S</i> ≤50kΩ	—	—	—	—	—	—	—	—	10		
Average Temper- ature Coefficient of Input Offset Voltage <i>αV_{IO}</i>	<i>R_S</i> ≤10kΩ	—	6	—	—	10	—	—	—	—	μV/° C	
	<i>R_S</i> ≤50Ω	—	3	—	—	6	—	—	—	—		
		—	—	—	—	—	—	—	6	30		
Average Temper- ature Coefficient of Input Offset Current <i>αI_{IO}</i>	-55° C to +25° C	—	—	—	—	—	—	—	—	—	nA/° C	
	0° C to +25° C	—	—	—	—	—	—	—	0.02	0.6		
	+25° C to +70° C	—	—	—	—	—	—	—	0.01	0.3		
	+25° C to +125° C	—	—	—	—	—	—	—	—	—		
Input Offset Current <i>I_{IO}</i>	<i>T_A</i> = 0° C	—	—	—	—	150	750	—	—	—	nA	
	<i>T_A</i> = 25° C	—	40	200	—	100	500	—	3	50		
	<i>T_A</i> = 70° C	—	—	—	—	50	400	—	—	—		
	<i>T_A</i> = 125° C	—	10	200	—	—	—	—	—	—		
		—	—	—	—	—	—	—	—	70		
	<i>T_A</i> = -55° C	—	100	500	—	—	—	—	—	—		
Input Bias Current <i>I_{IB}</i>	<i>T_A</i> = -55° C	—	0.28	1.5	—	—	—	—	—	—	μA	
	<i>T_A</i> = 0° C	—	—	—	—	0.32	2	—	—	—		
	<i>T_A</i> = 25° C	—	0.12	0.5	—	0.25	1.5	—	0.07	0.25		
		—	—	—	—	—	—	—	—	0.3		
Supply Current <i>I_±</i>	<i>T_A</i> =25° C <i>V_±</i> =15V	—	—	—	—	—	—	—	1.8	3	mA	
	<i>V_±</i> =20V	—	1.8	3	—	1.8	3	—	—	—		
	<i>T_A</i> =125° C <i>V_±</i> =20V	—	1.2	2.5	—	—	—	—	—	—		
Open-Loop Differential Voltage Gain <i>A_{OL}</i>	<i>T_A</i> =25° C <i>V_±</i> =15V <i>V_O</i> =±10V <i>R_L</i> ≥2kΩ	50	160	—	20	150	—	25	160	—	V/mV	
	<i>V_±</i> =15V <i>V_O</i> =±10V <i>R_L</i> ≥2kΩ	25	—	—	15	—	—	15	—	—		
Input Resis- tance <i>R_I</i>	<i>T_A</i> =25° C	0.3	0.8	—	0.1	0.4	—	0.5	2	—	MΩ	
Output Voltage Swing <i>V_{OPP}</i>	<i>V_±</i> =15V <i>R_L</i> =10kΩ	±12	±14	—	±12	±14	—	±12	±14	—	V	
	<i>V_±</i> =15V <i>R_L</i> =2kΩ	±10	±13	—	±10	±13	—	±10	±13	—		
Common-Mode Input-Voltage Range <i>V_{ICR}</i>	<i>V_±</i> =15V	±12	—	—	±12	—	—	±12	—	—	V	
	<i>V_±</i> =20V	—	—	—	—	—	—	—	—	—		
Common-Mode Rejection Ratio <i>CMRR</i>	<i>R_S</i> ≤10kΩ	70	90	—	65	90	—	—	—	—	dB	
	<i>R_S</i> ≤50kΩ	—	—	—	—	—	—	70	90	—		
Supply-Voltage Rejection Ratio <i>PSRR</i>	<i>R_S</i> ≤10kΩ	70	90	—	70	90	—	—	—	—	dB	
	<i>R_S</i> ≤50kΩ	—	—	—	—	—	—	70	90	—		

Δ Characteristics applicable over operating temperature range (T_A) as shown below, unless otherwise specified:

CA101: -55 to $+125^\circ\text{C}$; CA201, CA301A: 0 to 70°C

TYPICAL STATIC CHARACTERISTICS
TYPE CA101

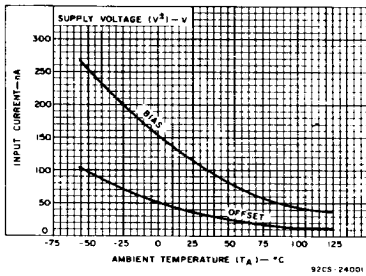


Fig. 3 - Input current (I_{iO} , I_{iB}) vs. temperature.

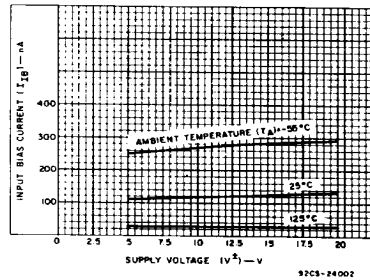


Fig. 4 - Input bias current vs. supply voltage.

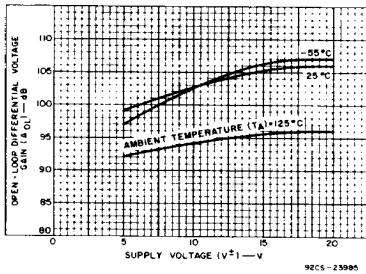


Fig. 5 - Voltage gain vs. supply voltage.

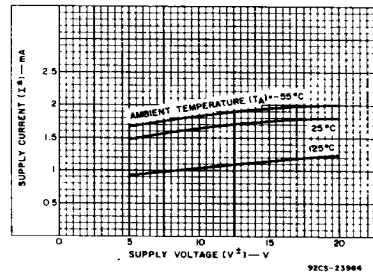


Fig. 6 - Supply characteristics.

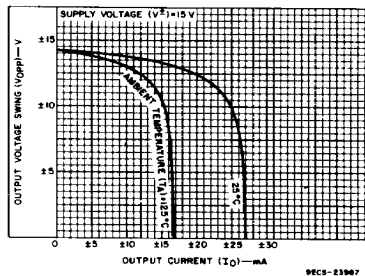


Fig. 7 - Output characteristics.

TYPE CA201

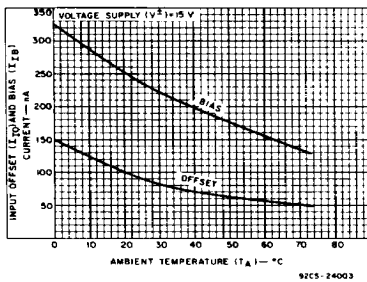


Fig. 8 - Input current (I_{iO} , I_{iB}) vs. temperature.

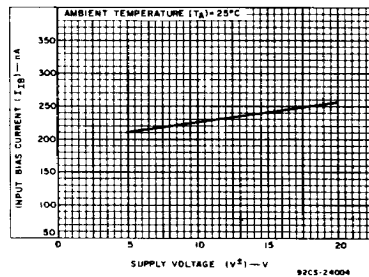


Fig. 9 - Input bias current (I_{iB}) vs. supply voltage.

CA101, CA201, CA301A, LM201, LM301A

TYPICAL STATIC CHARACTERISTICS (Cont'd)

TYPE CA201

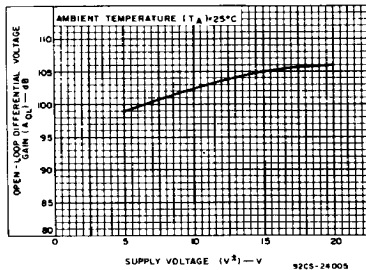


Fig. 10 - Voltage gain vs. supply voltage.

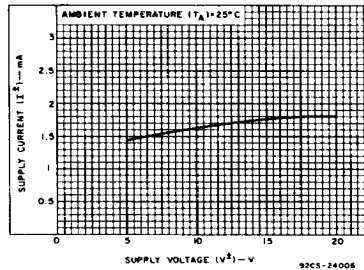


Fig. 11 - Supply characteristics.

TYPE CA301A

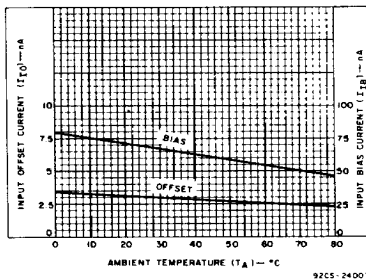


Fig. 12 - Input current (I_{IO} , I_{IB}) vs. temperature.

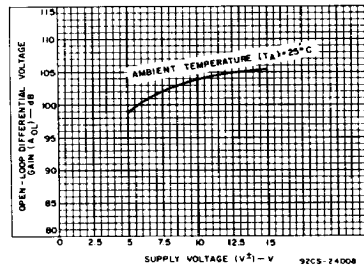


Fig. 13 - Voltage gain vs. supply voltage.

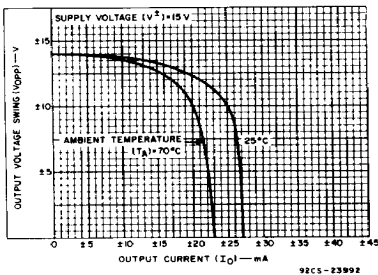


Fig. 14 - Output characteristics.

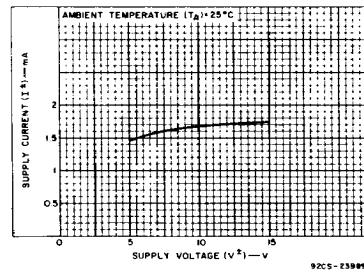


Fig. 15 - Supply characteristics.

TYPICAL DYNAMIC CHARACTERISTICS

TYPES CA101, CA201, CA301A

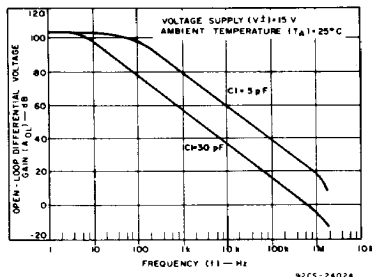


Fig. 16 - Voltage gain vs. frequency.

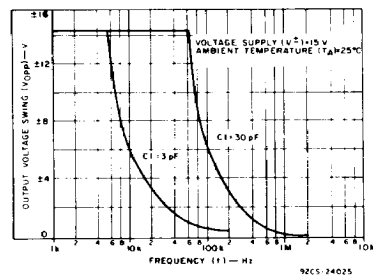


Fig. 17 - Output voltage swing vs. frequency.

TYPICAL DYNAMIC CHARACTERISTICS (Cont'd)
FOR TYPES CA101, CA201 AND CA301A

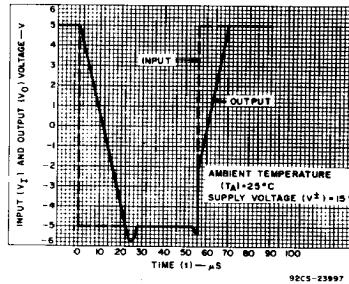


Fig. 18 - Voltage follower pulse response.

TYPE CA301A

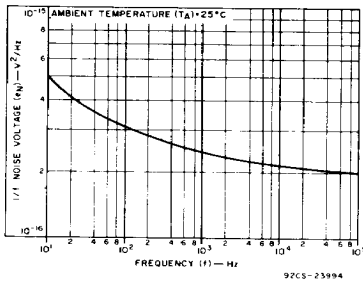


Fig. 19 - $1/f$ noise voltage vs. frequency.

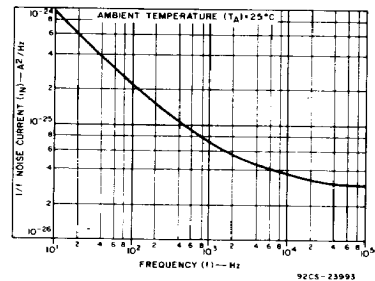
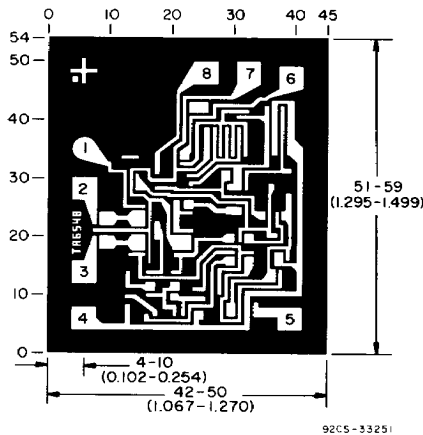


Fig. 20 - $1/f$ noise current vs. frequency.



Dimensions and pad layout for CA301H.

Dimensions in parentheses are in millimeters and are derived from the basic inch dimensions as indicated. Grid graduations are in mils (10^{-3} inch).