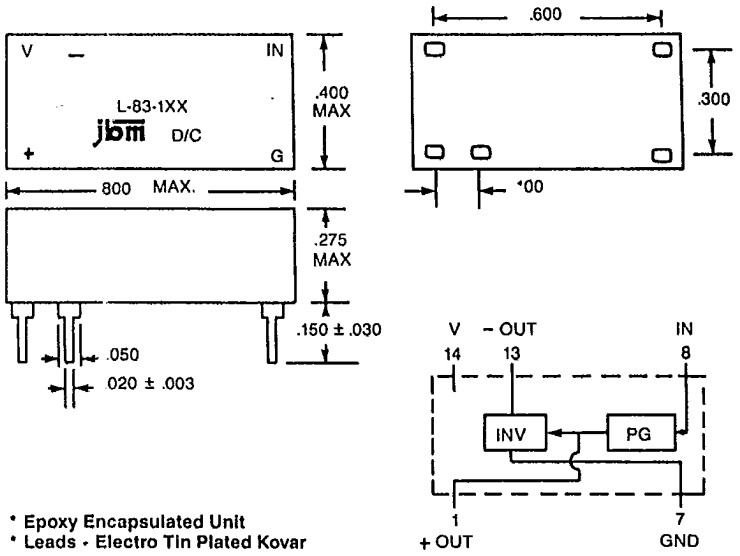
T-51-19

L-83-100 SERIES

TTL Pulse Generator



* Epoxy Encapsulated Unit
* Leads - Electro Tin Plated Kovar

FEATURES

- 14 pin outline
- TTL compatible
- Pulse widths from 5 to 100ns

Input Characteristics:

V_{IH} Logic "1" Input Voltage .... 2.0V Min.
V_{IL} Logic "0" Input Voltage .... .8V Max.
I_{IH} Logic "1" Input Current .. 50 μA Max.
I_{IL} Logic "0" Input Current .. −2mA Max.

Output Characteristics:

V_{OH} Logic "1" Output Voltage .. 2.4V Min.
V_{OL} Logic "0" Output Voltage .. .5V Max.

Environment:

Operating temperature ... 0°C to +70°C
Storage temperature . −55°C to +125°C
Supply voltage ..... 4.75V to 5.25V

DELAY SPECIFICATIONS IN NANOSECONDS (NS)					
JBM P/N	PULSE WIDTH @ 1.5V LEVEL	PULSE RATE (in Mhz) MAX	JBM P/N	PULSE WIDTH @ 1.5V LEVEL	PULSE RATE (in Mhz) MAX
L-83-101	5.0 ± 1ns	98	L-83-111	35 ± 1.5ns	13
L-83-102	6.0 ± 1ns	80	L-83-112	40 ± 1.5ns	11
L-83-103	7.0 ± 1ns	70	L-83-113	45 ± 1.5ns	10
L-83-104	8.0 ± 1ns	60	L-83-114	50 ± 1.5ns	9
L-83-105	9.0 ± 1ns	54	L-83-115	60 ± 1.5ns	8
L-83-106	10 ± 1ns	49	L-83-116	70 ± 2.0ns	7
L-83-107	15 ± 1ns	32	L-83-117	75 ± 2.0ns	6
L-83-108	20 ± 1ns	24	L-83-118	80 ± 2.0ns	6
L-83-109	25 ± 1ns	19	L-83-119	90 ± 3.0ns	5
L-83-110	30 ± 1ns	15	L-83-120	100 ± 3.0ns	4

Measurements @ 25°C, VCC = 5.0VDC, Input Pulse Width 10ns.