

XT[™] Rectangular LEDs CxxxXR230-Sxx00-x

Cree's XT Rectangular LEDs are the next generation of solid-state LEDs that combine highly efficient InGaN materials with Cree's proprietary G•SiC® substrate to deliver superior price/performance for high-intensity LEDs. These LED chips have a geometrically enhanced Epi-down design to maximize light extraction efficiency and require only a single wire bond connection. These vertically structured LED chips are approximately 150 microns in height and require a low forward voltage. Cree's XT Rectangular chips are tested for conformity to optical and electrical specifications and the ability to withstand 1000 V ESD. Applications for XT Rectangular LEDs include next-generation mobile appliances for use in their LCD backlights and digital camera flash where brightness, sub-miniaturization, and low power consumption are required.

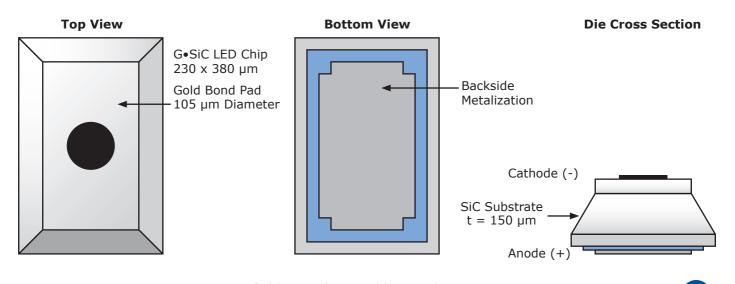
FEATURES

- XT Rectangular LED Performance
 - 460 nm & 470 nm
 - XR-18™ 18.0 mW min.
 - XR-21[™] 21.0 mW min.
- Thin 150 µm Chip
- Low Forward Voltage 3.2 Typical at 20 mA
- Single Wire Bond Structure
- Class 2 ESD Rating
- Sn Contact for Low-Temp. Die Attach Methods
- Die Attach Options:
 - Flux Eutectic; Flux & Temperature (-C)
 - Eutectic; Temperature & Pressure (-D)

APPLICATIONS

- Mobile Appliance LCD Backlighting
- Digital Camera Flash

CxxxXR230-Sxx00-x Chip Diagram





Maximum Ratings at T _A = 25°C Notes 28.4	CxxxXR230-Sxx00- <i>x</i>
DC Forward Current	30 mA
Peak Forward Current (1/10 duty cycle @ 1 kHz)	100 mA
LED Junction Temperature	125°C
Reverse Voltage	5 V
Operating Temperature Range	-40°C to +100°C
Storage Temperature Range	-40°C to +100°C
Electrostatic Discharge Threshold (HBM) Note 3	1000 V
Electrostatic Discharge Classification (MIL-STD-883E) Note 3	Class 2

Typical Electrical/Optical Characteristics at T _A = 25°C, If = 20 mA Note 4								
Part Number	Forwa	Forward Voltage (V _f , V)		Reverse Current [I(Vr=5V), μA]	Full Width Half Max $(\lambda_{ m p},{ m nm})$			
	Min.	Тур.	Max.	Max.	Тур.			
C460XR260-Sxx00-x	2.7	3.2	3.7	2	21			
C470XR260-Sxx00-x	2.7	3.2	3.7	2	22			

Mechanical Specifications	CxxxXR230-Sxx00- <i>x</i>		
Description	Dimension	Tolerance	
P-N Junction Area (µm)		180 x 340	± 25
Top Area (μm)	130 x 280	± 25	
Bottom Area (µm)		230 x 380	± 25
Chip Thickness (µm)		150	± 15
Au Bond Pad Diameter (µm)		105	-5, +15
Au Bond Pad Thickness (μm)		1.2	± 0.5
Back Contact Metal Area (µm)		140 x 300	± 15
Back Contact Metal Thickness (µm)	-C (Sn Flux Eutectic) -D (Sn Eutectic)	2.0 0.5	± 0.3 ± 0.1

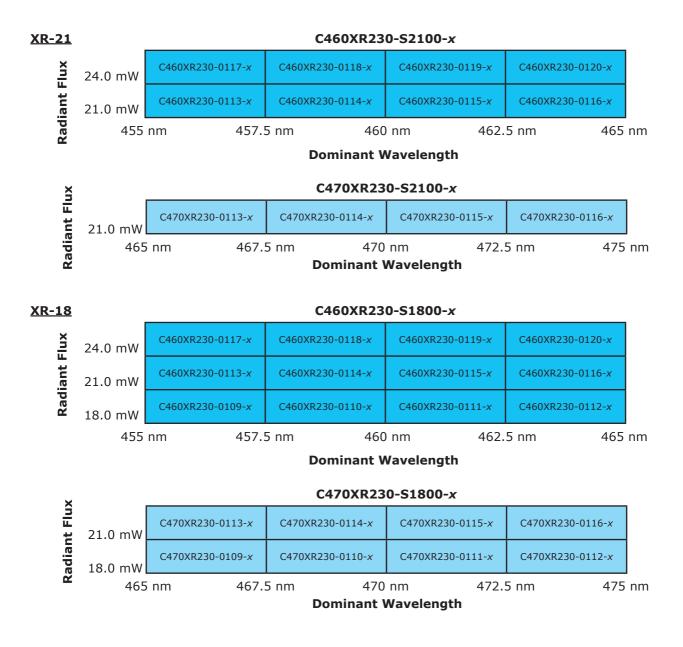
Notes:

- 1. This product is intended for use in a pre-molded surface mount package. It should be tested in the package and environment consistent with the final use to validate applicability. CxxxXR230-Sxxxx-C and -D are not intended for use where extended reliable operation in high temperature and high humidity environments is required. For this condition or for use in a leaded radial lamp, use CxxxXR230-Sxx00-A. See Cree XThin® Applications Note for more information.
- 2. Maximum ratings are package-dependent. The above ratings were determined using a T-1 3/4 package (with Hysol OS4000 epoxy) for characterization. Ratings for other packages may differ. The forward currents (DC and Peak) are not limited by the die but by the effect of the LED junction temperature on the package. The junction temperature limit of 125°C is a limit of the T-1 3/4 package; junction temperature should be characterized in a specific package to determine limitations. Assembly processing temperature must not exceed 325°C (< 5 seconds). See Cree XThin Applications Note for more assembly process information.
- 3. Product resistance to electrostatic discharge (ESD) according to the HBM is measured by simulating ESD using a rapid avalanche energy test (RAET). The RAET procedures are designed to approximate the maximum ESD ratings shown. The RAET procedure is performed on each die. The ESD classification of Class 2 is based on sample testing according to MIL-STD-883E.
- 4. All products conform to the listed minimum and maximum specifications for electrical and optical characteristics when assembled and operated at 20 mA within the maximum ratings shown above. Efficiency decreases at higher currents. Typical values given are within the range of average values expected by manufacturer in large quantities and are provided for information only. All measurements were made using lamps in T-1 3/4 packages (with Hysol OS4000 epoxy). Optical characteristics measured in an integrating sphere using Illuminance E.
- 5. Caution: To avoid leakage currents and achieve maximum output efficiency, die attach material must not contact the side of the chip. See Cree XThin Applications Note for more information.
- 6. Specifications are subject to change without notice.
- 7. XThin chips are shipped with the junction side down, not requiring a die transfer prior to die attach.



Standard Bins for CxxxXR230-Sxx00-x

LED chips are sorted to the **radiant flux** and **dominant wavelength** bins shown. A sorted die sheet contains die from only one bin. Sorted die kit (C460XR230-Sxx00-x) orders may be filled with any or all bins (C460XR230-01xx-x) contained in the kit. All radiant flux and all dominant wavelength values shown and specified are at If = 20 mA.





Characteristic Curves

These are representative measurements for the XThin product. Actual curves will vary slightly for the various radiant flux and dominant wavelength bins.

