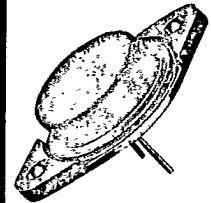


T-33-01 D

10 AMP/NPN-PNP Pirgo Silicon Planar Power Transistors

10 AMP
100 VOLT
65 WATT
60 MHz
TO-3

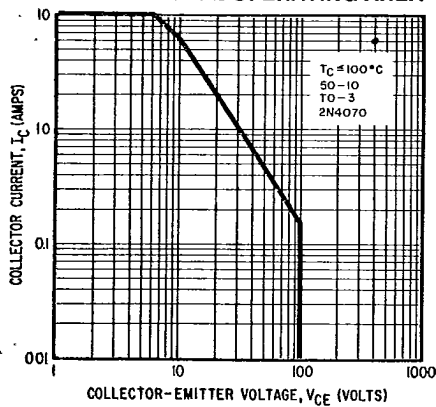


TO-3

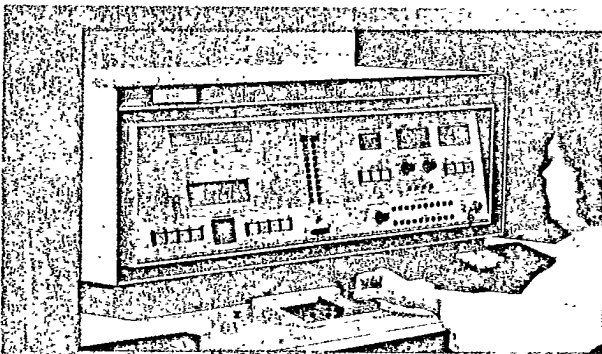
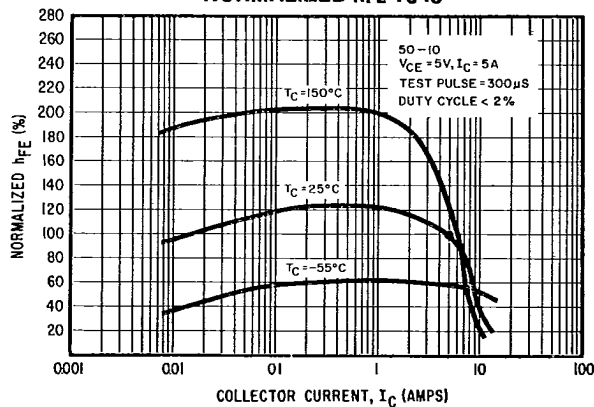
- Linear h_{FE} from 50 mA to 10 amps
- Low saturation voltage at maximum collector current
- High frequency $f_t = 60$ MHz (typical)
- High voltage, $BV_{CEO(sus)}$ to 100 volts

NPN Type	Package	BV_{CBO}	BV_{CEO} (sus)	BV_{EBO}	h_{FE}			h_{FE}			V_{CE} (sat)		$C_{ob}Max$ pf	P_{DW} @ 100°C	PNP Complement
					Min	Max	@ I_{cA}	Min	@ I_{cA}	Max	@ I_{cA}				
2N4070	TO-3	120	100	8	40	120	5	10	10	1.5	5	200	65	PG2396	
PG1380	TO-3	80	50	8	20	60	5	10	10	2.5	10	350	65	PG2380	
PG1381	TO-3	80	50	8	40	120	5	15	10	1.5	10	350	65	PG2381	
PG1382	TO-3	120	70	8	20	60	5	10	10	2.5	10	350	65	PG2382	
PG1383	TO-3	120	70	8	40	120	5	15	10	1.5	10	350	65	PG2383	
PG1384	TO-3	120	100	8	20	60	5	10	10	2.5	10	350	65	PG2384	
PG1385	TO-3	120	100	8	40	120	5	15	10	1.5	10	350	65	PG2385	
PG1386	TO-3	120	100	8	100	300	5	20	10	1.5	10	350	65	PG2386	
PG1387	TO-3	70	50	6	20	60	5			.6	5	350	65	PG2387	
PG1388	TO-3	80	70	6	20	60	5			.6	5	350	65	PG2388	
PG1389	TO-3	120	100	6	20	60	5			.6	5	350	65	PG2389	
PG1390	TO-3	70	50	6	40	120	5			.6	5	350	65	PG2390	
PG1391	TO-3	80	70	6	40	120	5			.6	5	350	65	PG2391	
PG1392	TO-3	120	100	6	40	120	5			.6	5	350	65	PG2392	
PG1393	TO-3	70	50	6	100	300	5			.6	5	350	65	PG2393	
PG1394	TO-3	80	70	6	100	300	5			.6	5	350	65	PG2394	
PG1395	TO-3	120	100	6	100	300	5			.6	5	350	65	PG2395	

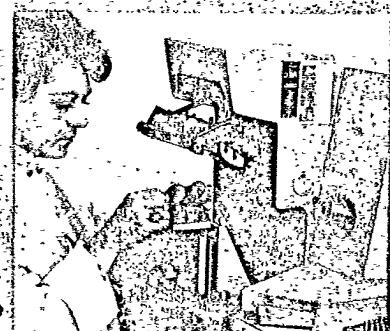
MAXIMUM SAFE OPERATING AREA



NORMALIZED h_{FE} Vs I_C



Final 100% electrical test.



Application of type marking and date code on finished product.

2206

PG-2110-13