



INTERNATIONAL CMOS  
TECHNOLOGY, INC.

Product Preview

T-46-19-07

## PEEL™ 22CV10-12/PEEL™ 22CV10-15 CMOS Programmable Electrically Erasable Logic Device

### Features

- **Advanced CMOS EEPROM Technology**
- **Low Power Consumption**
  - 105mA + 0.5mA/MHz max
- **Ultra High Performance**
  - 22CV10P-12 t<sub>PD</sub> = 12ns
  - 22CV10P-15 t<sub>PD</sub> = 15ns
- **EE Reprogrammability**
  - Low-risk reprogrammable inventory
  - Superior programming and functional yield
  - Erases and programs in seconds
- **Development and Programming Support**
  - Third-party software and programmers
  - ICT PEEL Development System and software.
- **Architectural Flexibility**
  - 132 product term x 44 input AND array
  - Up to 22 inputs and 10 outputs
  - Variable product term distribution (8 to 16 per output) for greater logic flexibility
  - Independently programmable I/O macrocells
  - Synchronous preset, asynchronous clear
  - Independently programmable output enables
- **Application Versatility**
  - Replaces random SSI/MSI logic
  - Pin-compatible with the bipolar AmPAL22V10 and CMOS PALC22V10

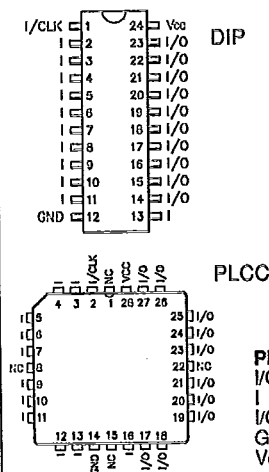
### General Description

The ICT PEEL22CV10P-12 or PEEL22CV10P-15 is a CMOS Programmable Electrically Erasable Logic Devices that provides a high-performance, low-power, reprogrammable, and architecturally enhanced alternative to early generation programmable logic devices (PLDs). Designed in advanced CMOS EEPROM technology, the PEEL22CV10 rivals speed parameters of comparable bipolar PLDs while providing a dramatic improvement in active power consumption. The EE reprogrammability of the PEEL22CV10 allows cost effective plastic packaging, low risk inventory, reduced development and retrofit costs, and enhanced tes-

tability to ensure 100% field programmability and function. The PEEL22CV10's flexible architecture offers complete function and JEDEC-file compatibility with the bipolar AmPAL22V10 and the CMOS PALC22V10. Applications for the PEEL22CV10 include: replacement of random SSI/MSI logic circuitry and user customized sequential and combinatorial functions such as counters, shift registers, state machines, address decoders, multiplexers, etc. Development and programming support for the PEEL22CV10 is provided by ICT and third-party manufacturers.



Pin Configuration (Figure 1)



Block Diagram (Figure 2)

