

**H11C1X, H11C2X, H11C3X, H11C4X, H11C5X, H11C6X
H11C1, H11C2, H11C3, H11C4, H11C5, H11C6**



**PHOTON COUPLED ISOLATOR Ga As
INFRARED EMITTING DIODE &
LIGHT ACTIVATED SCR**

APPROVALS

- UL recognised, File No. E91231

'X' SPECIFICATION APPROVALS

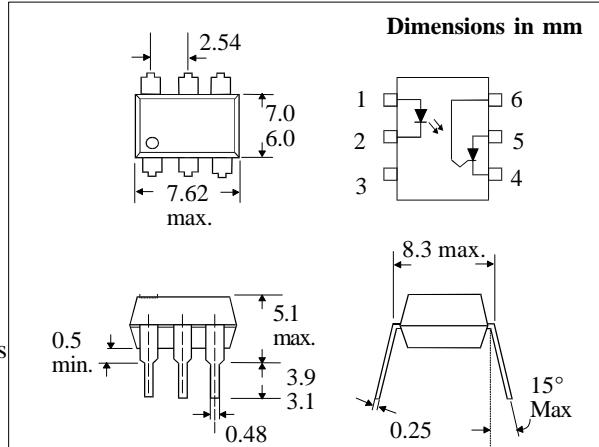
- VDE 0884 in 2 available lead forms : -
 - STD
 - G form

DESCRIPTION

The H11C_n series are optically coupled isolators consisting of infrared light emitting diode and a light activated silicon controlled rectifier in a standard 6pin dual in line plastic package.

FEATURES

- Options :-
10mm lead spread - add G after part no.
Surface mount - add SM after part no.
Tape&reel - add SMT&R after part no.
- High Isolation Voltage (5.3kV_{RMS}, 7.5kV_{PK})
- High Surge Anode Current (5.0 A)
- High Blocking Voltage (200V^{*1}, 400V^{*1})
- Low Turn on Current (5mA typical)
- All electrical parameters 100% tested
- Custom electrical selections available



**ABSOLUTE MAXIMUM RATINGS
(25°C unless otherwise specified)**

Storage Temperature	-55°C to + 150°C
Operating Temperature	-55°C to + 100°C
Lead Soldering Temperature (1/16 inch (1.6mm) from case for 10 secs)	260°C

INPUT DIODE

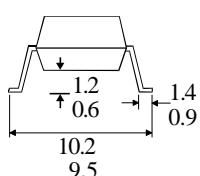
Forward Current	60mA
Forward Current (Peak) (1μs pulse, 300 pps)	3A
Reverse Voltage	6V
Power Dissipation	100mW

DETECTOR

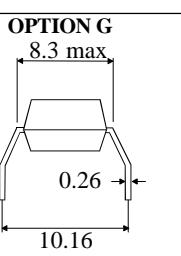
Peak Forward Voltage H11C1, H11C2, H11C3	200V ^{*1}
H11C4, H11C5, H11C6	400V ^{*1}
Peak Reverse Gate Voltage	6V
RMS On-state Current	300mA
Peak On-state Current (100μs, 1% duty cycle)	10A
Surge Current (10ms)	5A
Power Dissipation	300mW

^{*1} IMPORTANT : A resistor must be connected between gate and cathode (pins 4 & 6) to prevent false firing ($R_{GK} < 56k\Omega$)

**OPTION SM
SURFACE MOUNT**



OPTION G



ISOCOM COMPONENTS LTD
Unit 25B, Park View Road West,
Park View Industrial Estate, Brenda Road
Hartlepool, Cleveland, TS25 1YD
Tel: (01429) 863609 Fax :(01429) 863581

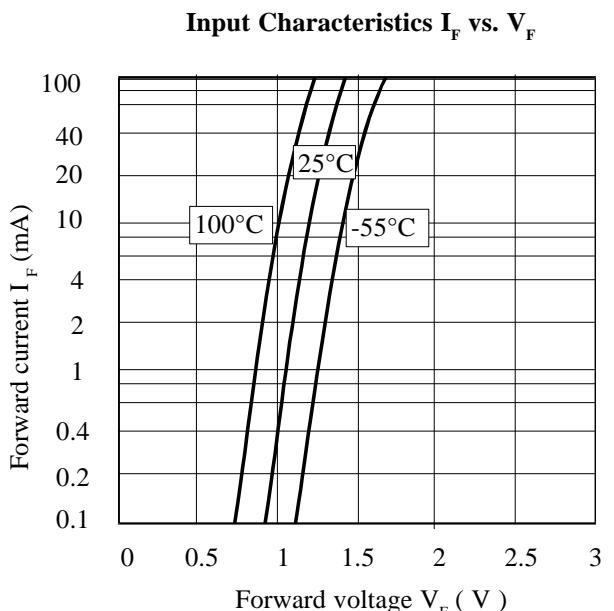
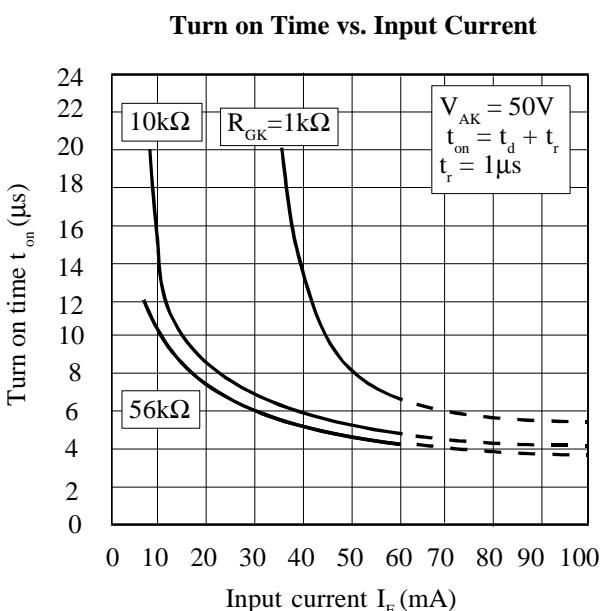
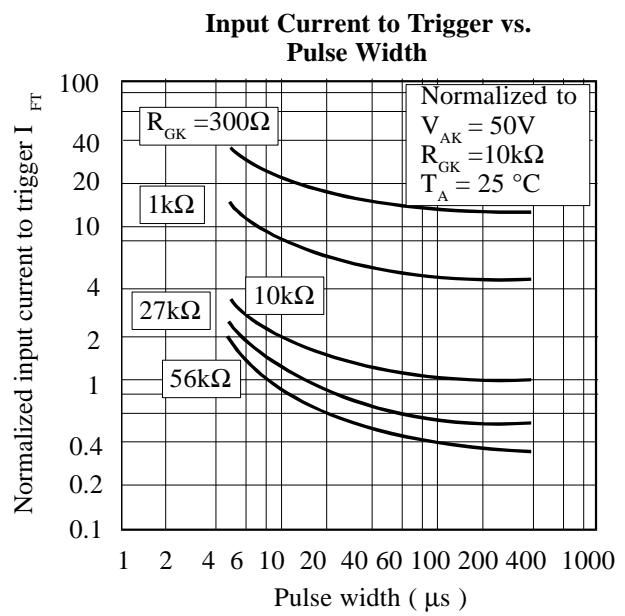
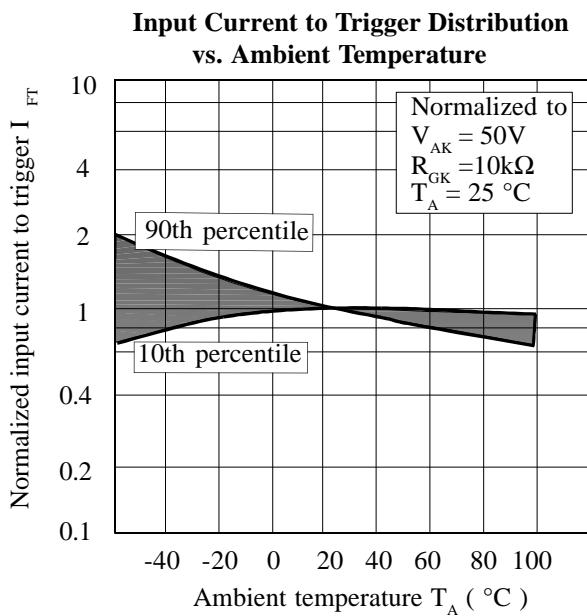
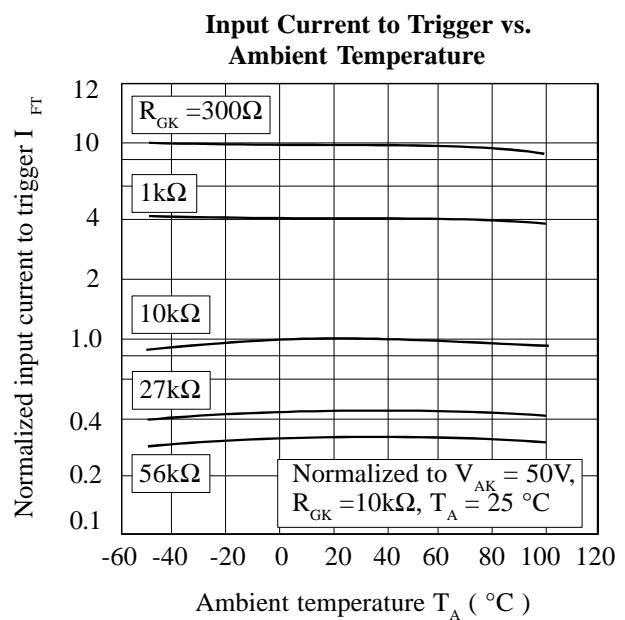
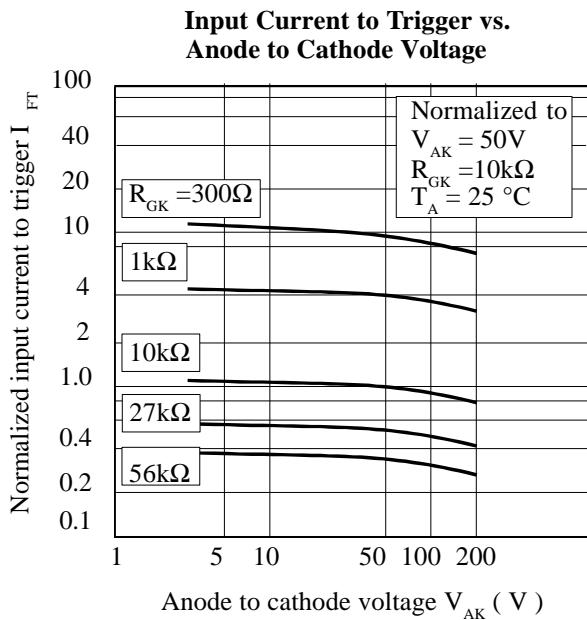
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ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ Unless otherwise noted)

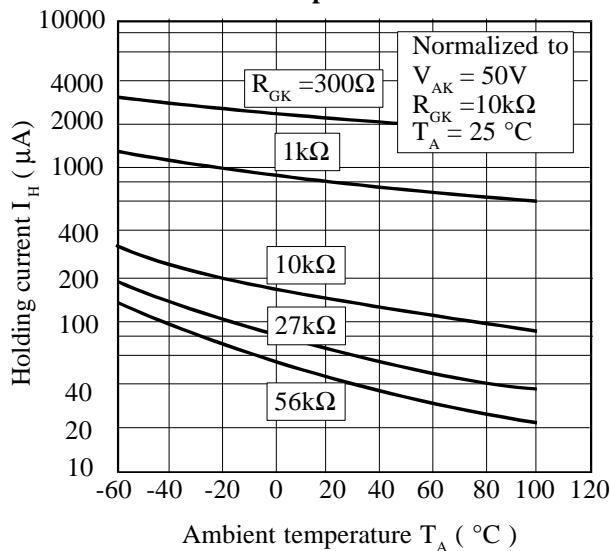
PARAMETER		MIN	TYP	MAX	UNITS	TEST CONDITION
Input	Forward Voltage (V_F) Reverse Voltage (V_R)	3	1.2	1.5	V V	$I_F = 10\text{mA}$ $I_R = 10\mu\text{A}$
Output (note 2)	Peak Off-state Voltage (V_{DM}) H11C1, H11C2, H11C3	200			V	$R_{GK}=10\text{k}\Omega$, $I_D=50\mu\text{A}$, $T_A=100^\circ\text{C}$
	H11C4, H11C5, H11C6	400			V	$R_{GK}=10\text{k}\Omega$, $I_D=150\mu\text{A}$, $T_A=100^\circ\text{C}$
	Peak Reverse Voltage (V_{RM}) H11C1, H11C2, H11C3	200			V	$R_{GK}=10\text{k}\Omega$, $I_D=50\mu\text{A}$, $T_A=100^\circ\text{C}$
	H11C4, H11C5, H11C6	400			V	$R_{GK}=10\text{k}\Omega$, $I_D=150\mu\text{A}$, $T_A=100^\circ\text{C}$
	On-state Voltage (V_{TM})		1.1	1.3	V	$I_{TM} = 300\text{mA}$
	Off-state Current (I_{DM}) H11C1, H11C2, H11C3		50	μA		$R_{GK}=10\text{k}\Omega$, $I_F=0$, $V_{DM}=200\text{V}$, $T_A=100^\circ\text{C}$
	H11C4, H11C5, H11C6		150	μA		$R_{GK}=10\text{k}\Omega$, $I_F=0$, $V_{DM}=400\text{V}$, $T_A=100^\circ\text{C}$
	Reverse Current (I_R) H11C1, H11C2, H11C3		50	μA		$R_{GK}=10\text{k}\Omega$, $I_F=0$, $V_{DM}=200\text{V}$, $T_A=100^\circ\text{C}$
	H11C4, H11C5, H11C6		150	μA		$R_{GK}=10\text{k}\Omega$, $I_F=0$, $V_{DM}=400\text{V}$, $T_A=100^\circ\text{C}$
	Coupled					
Coupled	Input Current to Trigger (I_{FT}) (note 2) H11C1, H11C2, H11C4, H11C5 H11C3, H11C6 H11C1, H11C2, H11C4, H11C5 H11C3, H11C6 Coupled dv/dt, Input to Output (dv/dt) Input to Output Isolation Voltage V_{ISO}		20 30 11 14 500 5300 7500	mA mA mA mA V/ μs V _{RMS} V _{PK}		$V_{AK}=50\text{V}$, $R_{GK}=10\text{k}\Omega$ $V_{AK}=50\text{V}$, $R_{GK}=10\text{k}\Omega$ $V_{AK}=100\text{V}$, $R_{GK}=27\text{k}\Omega$ $V_{AK}=100\text{V}$, $R_{GK}=27\text{k}\Omega$
	Input-output Isolation Resistance R_{ISO} Input-output Capacitance C_f	10^{11}		2	Ω pF	See note 1 See note 1 $V_{IO}=500\text{V}$ (note 1) $V=0$, $f=1\text{MHz}$

Note 1 Measured with input leads shorted together and output leads shorted together.

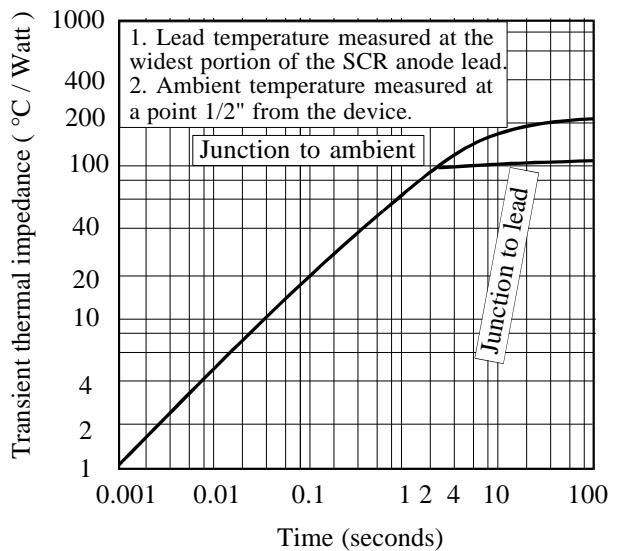
Note 2 Special Selections are available on request. Please consult the factory.



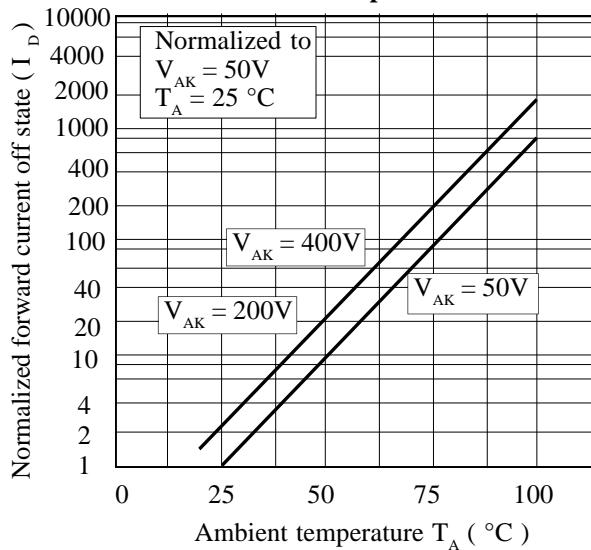
Holding Current vs. Ambient Temperature



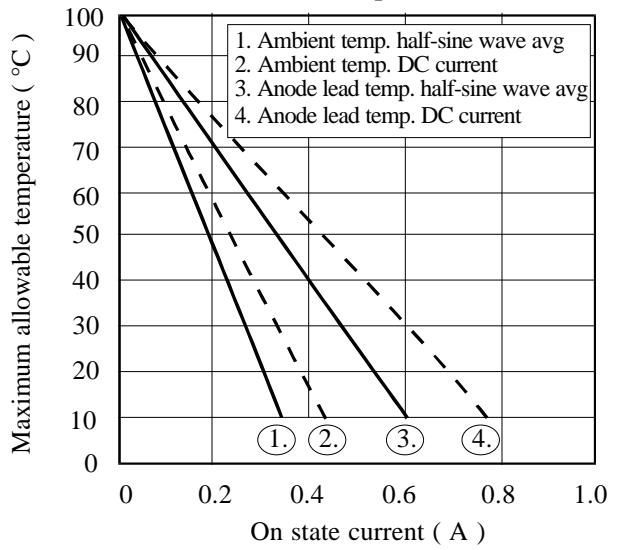
Maximum Transient Thermal Impedance



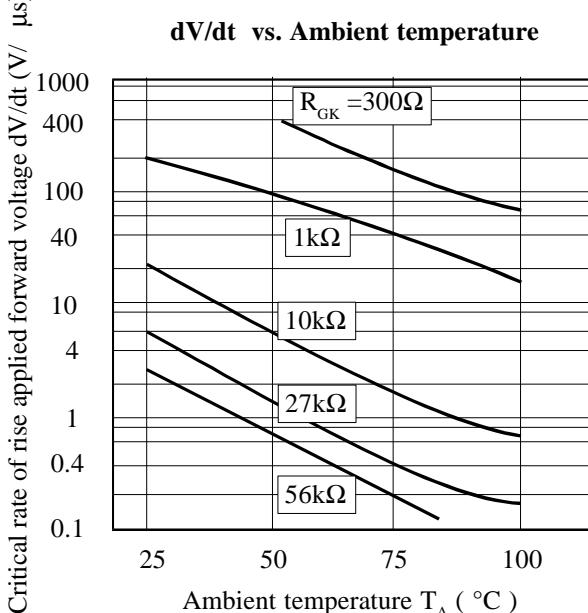
Off State Forward Current vs. Ambient Temperature



On State Current vs. Maximum Allowable Temperature



dV/dt vs. Ambient temperature



On State Characteristics

