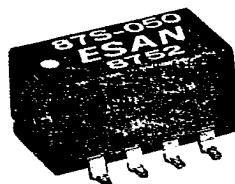


Surface Mounting Delay Line (Active)

87S Series

- TTL Compatible
- Triple In/Out Delay
- 8 Pin Dll Package

**Specifications:**

- Delay tolerance : $\pm 5\%$ or 2ns
whichever is greater
- Rise time : 4ns max.
- Temp. coefficient : 100 ppm/ $^{\circ}\text{C}$
- Operating Temp. Range : -0°C to 70°C
- Minimum Pulse Width : 40% of total delay
- Supply Voltage : 5Vdc $\pm 5\%$
- Logic 1 Input current : 50 μA max.
- Logic 0 Input current : -2 mA max.
- Logic 1 Output : 2.75V min.
- Logic 0 Output : 0.4V max.
- Logic 1 Fan out : 20/TAP max.
- Logic 0 Fan out : 10/TAP max.
- Power Dissipation : 375 mW max.

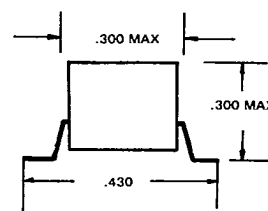
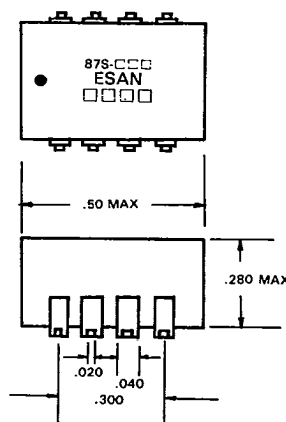
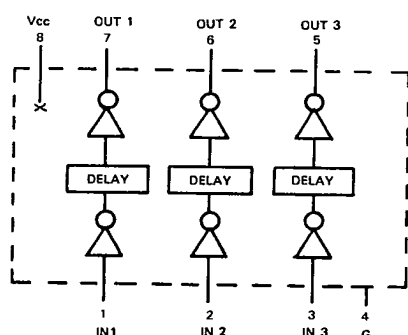
Test Conditions:

- Input pulse voltage : 3.2V
- Input pulse width : $1.2 \times$ total delay
- Input pulse spacing : $2 \times$ total delay
- Input pulse rise time : 2 ns
- Rise time : 0.75V to 2.4V
- Time delay measured : @1.5V level
- All measurement : @Vcc = 5.0V, $T_A = 25^{\circ}\text{C}$

Features:

- Schottky TTL interface
- Low power schottky available named "87SL- xxx" series
- Both Leading and Trailing edge available under request named "87SB- xxx" series

PART NO.	DELAY TIME (ns)
87S-025	25
87S-050	50
87S-075	75
87S-100	100
87S-125	125
87S-150	150
87S-200	200
87S-250	250
87S-300	300
87S-400	400
87S-500	500



Unit: Inch