

Blue Chip Technology



DATA

ACQUISITION CARDS

AIP-8d

8 Channel Analogue to Digital Card

The AIP-8d card provides eight channels of 12 bit analogue inputs. The inputs range from $\pm 50\text{mV}$ to ± 5 volts, or 0 to +10 volts full scale.

The card features user selectable base address, interrupt source, interrupt level and an on-board timer. The timer may be used as a pacer clock to control A-D conversion cycles

User selection base address, interrupt level and on-board timer which can be used to control A to D conversion cycles

12 μS conversion time

25 way male D type connector

3 x 16 bit timers (82C54)

Power requirements 5V d.c. at 150mA



Technical Specification

Analogue Inputs

Number of Analogue Input Channels:	8 (Single Ended)
Voltage Input Range:	From $\pm 50\text{mV}$ to +10 Volts or, ± 5 Volts
Programmable Gains:	x1, x10, x100
System Conversion Time:	12 μS min
A-D Conversion Time::	3 μS
Resolution:	12 bit
Measurement Accuracy:	
+5V Range	0.1% FS ± 4 Counts
+10V Range	0.1% FS ± 4 Counts
$\pm 5\text{V}$ Range	0.1% FS ± 4 Counts
Input Common Mode Range:	± 12 Volts
Data Transfer Modes:	I/O Ports
Data Ready Flags:	Interrupt or Polling
Interrupt Channels:	IRQ-2 to -7

Timers

Number Of Timer Channels:	3
Timer 0:	Feeds Timer 1
Timer 1:	Feeds Timer 2
Timer 2:	Pacer For A-D Conversions TIMER 0 (Output Feeds TIMER 1 Clock)
Timer 0: Resolution	250nS
Minimum Time Interval:	500nS
Maximum Time Interval:	16.384mS
Timer 1: Resolution (Cascade from TIMER 0)	500nS
Minimum Time Interval:	1 μS
Maximum Time Interval:	32.768mS

Timer 2: Resolution (Cascade from TIMER 1)	1 μS
Minimum Time Interval:	2 μS
Maximum Time Interval:	65.535mS

Combined Timer Limits (Time Between Start Converts)

Minimum Time Period	2 μS
Maximum Time Period	2.23 years

Board Connections

Analogue Input Signals:	1 x 50 way male D-type
Bus:	PC 8-bit ISA

Options

Windows® 3.1 DLL driver



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YEAR
WARRANTY

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