

FMP16N60ES

FUJI POWER MOSFET

Super FAP-E^{3S} series

N-CHANNEL SILICON POWER MOSFET

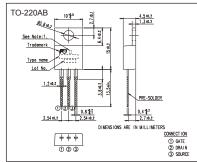
■ Features

Maintains both low power loss and low noise Lower R_{DS}(on) characteristic More controllable switching dv/dt by gate resistance Smaller V_{GS} ringing waveform during switching Narrow band of the gate threshold voltage (4.2±0.5V) High avalanche durability

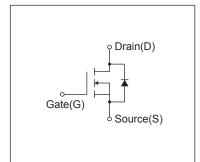
Applications

Switching regulators UPS (Uninterruptible Power Supply) DC-DC converters

■ Outline Drawings [mm]



■ Equivalent circuit schematic



Maximum Ratings and Characteristics

● Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)

Description	Symbol	Characteristics	Unit	Remarks
Drain Sauras Valtara	V _{DS}	600	V	
Drain-Source Voltage	VDSX	600	V	V _{GS} = -30V
Continuous Drain Current	ID	±16	Α	
Pulsed Drain Current	IDP	±64	Α	
Gate-Source Voltage	V _{GS}	±30	V	
Repetitive and Non-Repetitive Maximum AvalancheCurrent	IAR	16	Α	Note*1
Non-Repetitive Maximum Avalanche Energy	Eas	554.8	mJ	Note*2
Repetitive Maximum Avalanche Energy	Ear	27	mJ	Note*3
Peak Diode Recovery dV/dt	dV/dt	3.8	kV/μs	Note*4
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5
Maximum Power Dissipation	PD	2.02	W	Ta=25°C
		270	VV	Tc=25°C
Oneveting and Starone Temperature range	Tch	150	°C	
Operating and Storage Temperature range	T _{stg}	-55 to + 150	°C	

● Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions		min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BVDSS	I _D =250μA, V _{GS} =0V		600	-	-	V
Gate Threshold Voltage	V _{GS} (th)	I _D =250µA, V _{DS} =V _{GS}		3.7	4.2	4.7	V
Zero Gate Voltage Drain Current	Ipss	V _{DS} =600V, V _{GS} =0V	T _{ch} =25°C	-	-	25	μА
	IDSS	V _{DS} =480V, V _{GS} =0V	T _{ch} =125°C	-	-	250	
Gate-Source Leakage Current	Igss	V _{GS} =±30V, V _{DS} =0V		-	10	100	nA
Drain-Source On-State Resistance	R _{DS} (on)	I _D =8A, V _{GS} =10V		-	0.40	0.47	Ω
Forward Transconductance	g fs	I _D =8A, V _{DS} =25V	I _D =8A, V _{DS} =25V		10	-	S
Input Capacitance	Ciss	V _{DS} =25V V _{GS} =0V f=1MHz		-	2100	3150	pF
Output Capacitance	Coss			-	230	345	
Reverse Transfer Capacitance	Crss			-	13	19.5	
Turn-On Time	td(on)	V _{cc} =300V V _{cs} =10V I _D =8A R _c =18Ω		-	43	64.5	ns
	tr			-	41	61.5	
Turn-Off Time	td(off)			-	94	141	
	tf			-	20	30	
Total Gate Charge	Q _G	\/ -200\/			56	114	nC
Gate-Source Charge	QGS	V _{cc} =300V I _D =16A V _{GS} =10V		-	20	25.5	
Gate-Drain Charge	Q _{GD}			-	21	33	
Gate-Drain Crossover Charge	Qsw			-	9.5	10	
Avalanche Capability	lav	L=1.74mH, Tch=25°C	L=1.74mH, Tch=25°C		-	-	Α
Diode Forward On-Voltage	V _{SD}	I _F =16A, V _{GS} =0V, T _{ch} =25°C		-	0.90	1.35	V
Reverse Recovery Time	trr	I _F =16A, V _{GS} =0V	I _F =16A, V _{GS} =0V		0.7	-	μS
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	9	-	μC

Thermal Characteristics

Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to case			0.460	°C/W
	Rth (ch-a)	Channel to ambient			62.0	°C/W

Note *1 : Tch≤150°C

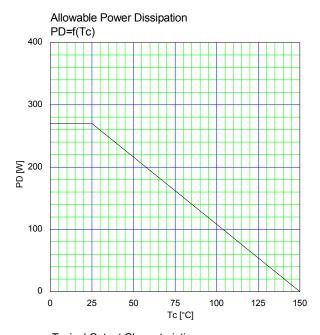
Note *2 : Stating Tch=25°C, Ias=7A, L=20.8mH, Vcc=60V, Rg=50 Ω Eas limited by maximum channel temperature and avalanche current. See to 'Avalanche Energy' graph.

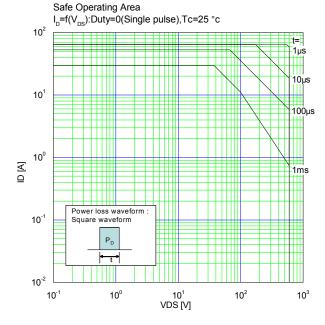
Note *3 : Repetitive rating : Pulse width limited by maximum channel temperature

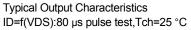
See to the 'Transient Themal impeadance' graph.

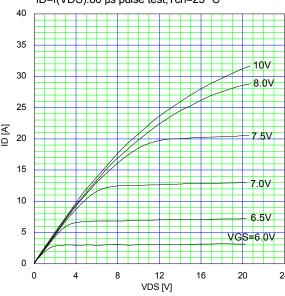
Note *4 : IFS-ID, -di/dt=100A/µs, Vcc≤BVoss, Tch≤150°C. Note *5 : IF<-ID, dv/dt=3.8kV/ μ s, Vcc \leq BVDSS, Tch \leq 150°C.

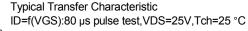
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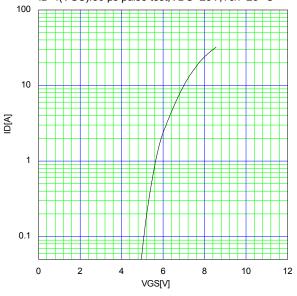




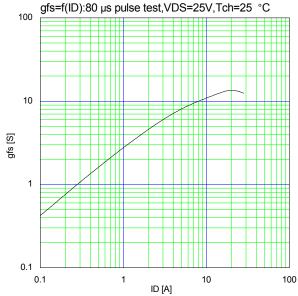




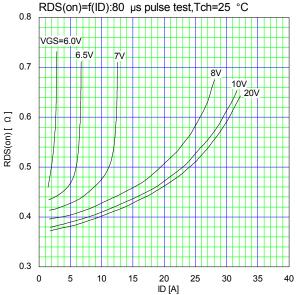




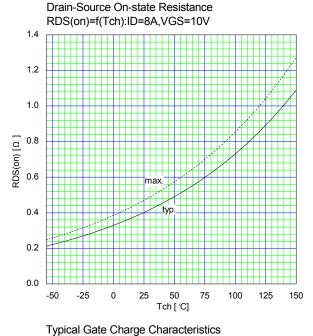
Typical Transconductance
ofs=f(ID):80 us pulse test VDS=25V Tch=25 °C

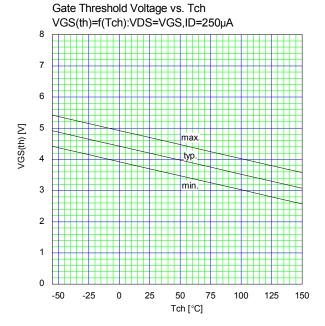


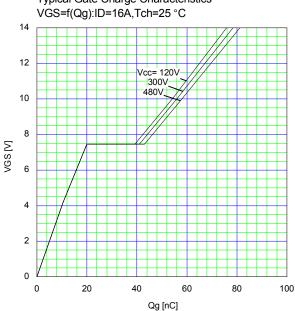
Typical Drain-Source on-state Resistance RDS(on)=f(ID):80 us pulse test Tch=25 °C

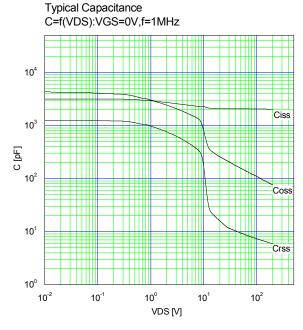


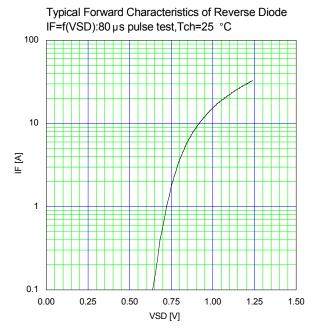
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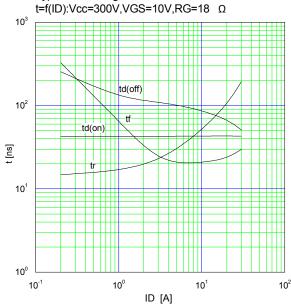


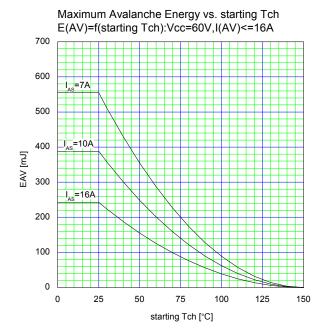


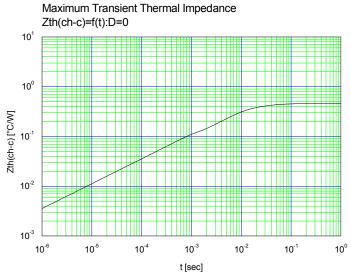












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