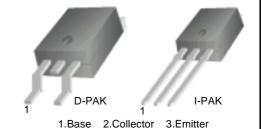


MJD32/32C

General Purpose Amplifier Low Speed Switching Applications D-PACK for Surface Mount Applications Load Formed for Surface Mount Application (No Suffix)

- Straight Lead (I.PACK, "- I" Suffix)
- Electrically Similar to Popular TIP32 and TIP32C



PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings $T_C=25$ °C unless otherwise noted

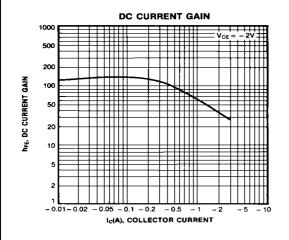
Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	- 40	V
	: MJD32	- 100	V
	: MJD32C		
V _{CEO}	Collector-Emitter Voltage	- 40	V
	: MJD32	- 100	V
	: MJD32C		
V _{EBO}	Emitter-Base Voltage	- 5	V
I _C	Collector Current (DC)	- 3	Α
I _{CP}	Collector Current (Pulse)	- 5	Α
I _B	Base Current	- 1	Α
P _C	Collector Dissipation (T _C =25°C)	15	W
P _C	Collector Dissipation (T _a =25°C)	1.56	W
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 65 ~ 150	°C

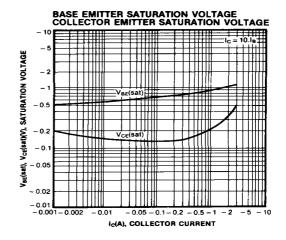
Electrical Characteristics $T_C=25^{\circ}C$ unless otherwise noted

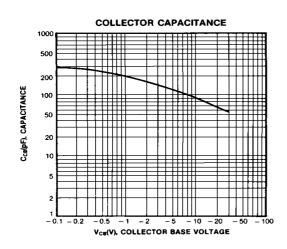
Symbol	Parameter	Test Condition	Min.	Max.	Units
V _{CFO} (sus)	* Collector-Emitter Sustaining Voltage				
0201	: MJD32	$I_C = -30 \text{mA}, I_B = 0$	-40		V
	: MJD32C		-100		V
I _{CEO}	Collector Cut-off Current				
	: MJD32	$V_{CE} = -40V, I_{B} = 0$		-50	μΑ
	: MJD32C	$V_{CE} = -60V, I_{B} = 0$		-50	μΑ
I _{CES}	Collector Cut-off Current				
020	: MJD32	$V_{CE} = -40V, V_{BE} = 0$		-20	μΑ
	: MJD32C	$V_{CE} = -100V, V_{BE} = 0$		-20	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{BE} = -5V, I_{C} = 0$		-1	mA
h _{FE}	* DC Current Gain	$V_{CE} = -4V, I_{C} = -1A$	25		
		$V_{CE} = -4V, I_{C} = -3A$	10	50	
V _{CE} (sat)	* Collector-Emitter Saturation Voltage	I _C = - 3, I _B = - 375mA		-1.2	V
V _{BE} (on)	* Base-Emitter ON Voltage	V _{CE} = - 4A, I _C = - 3A		-1.8	V
f _T	Current Gain Bandwidth Product	$V_{CE} = -10V, I_{C} = -500mA$	3		MHz
		•	•		

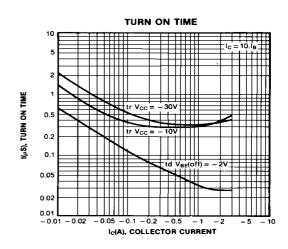
^{*} Pulse Test: PW≤300µs, Duty Cycle≤2%

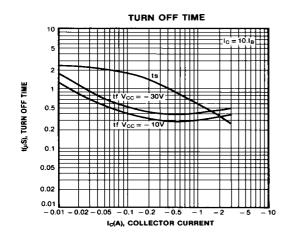
Typical Characteristics

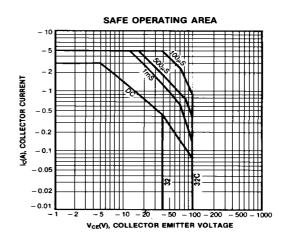






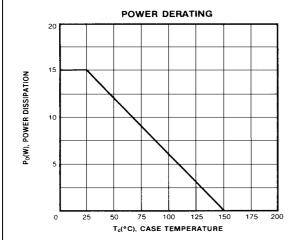






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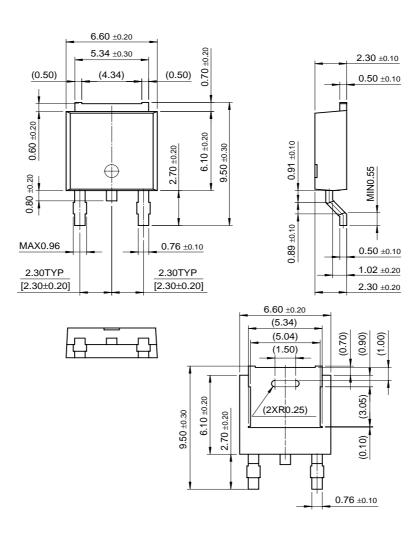
Typical Characteristics (continued)



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Package Dimensions

D-PAK



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