

# 2MBI300U4N-170-50

**IGBT Modules** 

# IGBT MODULE (U series) 1700V / 300A / 2 in one package

#### ■ Features

High speed switching Voltage drive Low Inductance module structure

### Applications

Inverter for Motor Drive AC and DC Servo Drive Amplifier Uninterruptible Power Supply Industrial machines, such as Welding machines



# ■ Maximum Ratings and Characteristics

### ● Absolute Maximum Ratings (at Tc=25°C unless otherwise specified)

Items		Symbols	Conditions		Maximum ratings	Units	
Collector-Emitter voltage		V <sub>CES</sub>			1700	V	
Gate-Emitter voltage		V <sub>GES</sub>			±20	V	
Collector current		Ic	Continuous	Tc=25°C	450		
			Continuous	Tc=80°C	300		
		Icp	1	Tc=25°C	900	^	
			1ms	Tc=80°C	600	Α	
		-lc	1ms		300		
		-lc pulse			600		
Collector power dissipation		Pc	1 device		1385	W	
Junction temperature		Tj			150	°C	
Storage temperature		Tstg			-40 to +125		
Isolation voltage	between terminal and copper base (*1)	V	A.C 4i		2400	\/A.C	
	between thermistor and others (*2)	V <sub>iso</sub>	AC : 1min.		3400	VAC	
Screw torque	Mounting (*3)				3.5	NI ma	
	Terminals (*4)	1-			4.5	N m	

Note \*1: All terminals should be connected together when isolation test will be done.

Note \*2: Two thermistor terminals should be connected together, each other terminals should be connected together and shorted to base plate when isolation test will be done.

Note \*3: Recommendable value : Mounting : 2.5-3.5 Nm (M5) Note \*4: Recommendable value : Terminals : 3.5-4.5 Nm (M6)

# ● Electrical characteristics (at Tj= 25°C unless otherwise specified)

Items		Cumbala	Canditions		Characteristics			I I mit m
116	ems	Symbols	Conditions		min.	typ.	max.	Units
	Zero gate voltage collector current	Ices	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 1700V		-	-	3.0	mA
	Gate-Emitter leakage current	Iges	$V_{CE} = 0V, V_{GE} = \pm 20V$		-	-	600	nA
	Gate-Emitter threshold voltage	V <sub>GE (th)</sub>	V <sub>CE</sub> = 20V, I <sub>C</sub> = 300mA		4.5	6.5	8.5	V
	Collector-Emitter saturation voltage	V <sub>CE (sat)</sub>		Tj=25°C	-	2.70	2.90	V
		(terminal)	V <sub>GE</sub> = 15V	Tj=125°C	-	3.10	-	
		V <sub>CE (sat)</sub>	Ic = 300A	Tj=25°C	-	2.30	2.45	
		(chip)		Tj=125°C	-	2.65	-	
	Input capacitance	Cies	V <sub>CE</sub> = 10V, V <sub>GE</sub> = 0V, f = 1MHz		-	28	-	nF
ā	Turn-on time	ton	1/ 0001/	-	0.62	1.20	μs	
Inverter		tr	Vcc = 900V Ic = 300A		-	0.39		0.60
		tr (i)	- Ic = 300A - V <sub>GE</sub> = ±15V	-	0.05	-		
	Turn-off time	toff	$R_{\rm G} = 1.5\Omega$		-	0.55		1.50
		tf	RG - 1.502		-	0.09	0.30	
	Forward on voltage	VF		Tj=25°C	-	2.10	2.40	V
		(terminal)	$V_{GE} = 0V$	Tj=125°C	-	2.30	-	
		VF	I <sub>F</sub> = 300A	Tj=25°C	-	1.80	1.95	
		(chip)		Tj=125°C	-	2.00	-	
	Reverse recovery time	trr	I <sub>F</sub> = 300A		-	0.18	0.6	μs
	Lead resistance, terminal-chip (*5)	R lead			-	1.00	-	mΩ
Thermistor	Besistance	П	T=25°C			5000	-	Ω
	Resistance	R	T=100°C			495	520	
트	B value	В	T=25/50°C		3305	3375	3450	K

Note \*5: Biggest internal terminal resistance among arm.

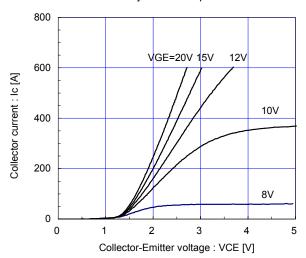
#### Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units		
Items	Symbols		min.	typ.	max.	Units		
Thermal resistance (1device)	Rth(j-c)	IGBT	-	-	0.09	°C/W		
Thermal resistance (Tuevice)		FWD	-	-	0.15			
Contact thermal resistance (1device) (*6)	Rth(c-f)	with Thermal Compound	-	0.0167	-			

Note \*6: This is the value which is defined mounting on the additional cooling fin with thermal compound.

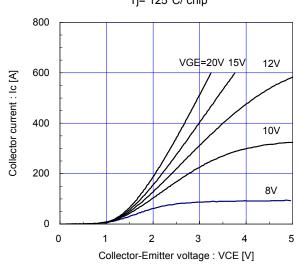
## ■ Characteristics (Representative)

Collector current vs. Collector-Emitter voltage (typ.) Tj= 25°C / chip

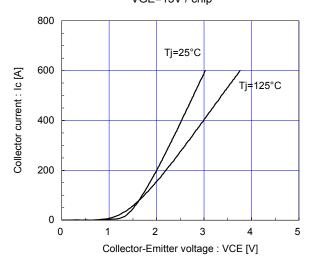


Collector current vs. Collector-Emitter voltage (typ.)

Tj= 125°C/ chip

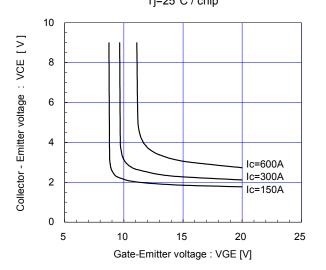


Collector current vs. Collector-Emitter voltage (typ.) VGE=15V / chip

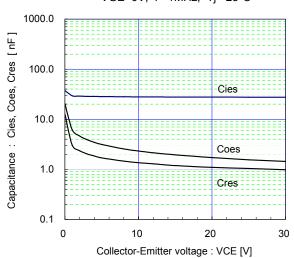


Collector-Emitter voltage vs. Gate-Emitter voltage (typ.)

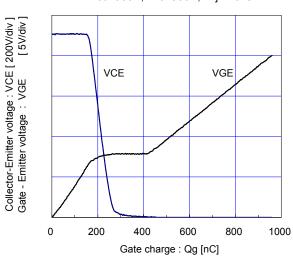
Tj=25°C / chip



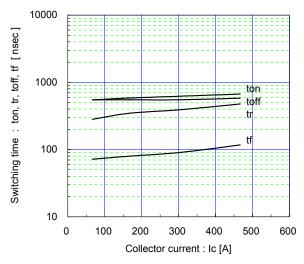
Capacitance vs. Collector-Emitter voltage (typ.) VGE=0V, f= 1MHz, Tj= 25°C



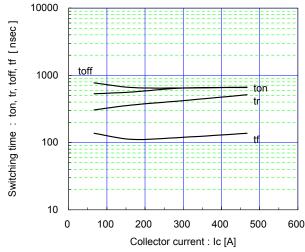
Dynamic Gate charge (typ.) Vcc=900V, Ic=300A, Tj= 25°C



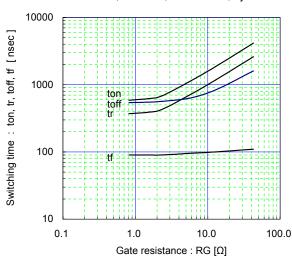
Switching time vs. Collector current (typ.) Vcc=900V, VGE=±15V, Rg=1.5Ω, Tj= 25°C



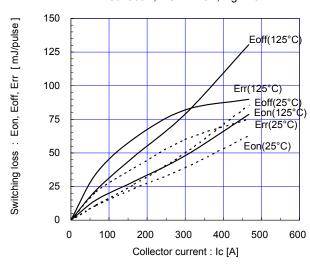
Switching time vs. Collector current (typ.) Vcc=900V, VGE=±15V, Rg=1.5Ω, Tj=125°C



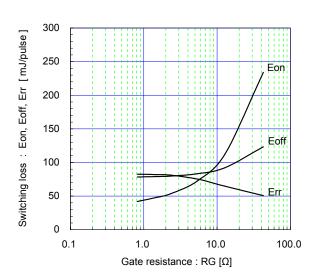
Switching time vs. Gate resistance (typ.) Vcc=900V, Ic=300A, VGE=±15V, Tj= 25°C



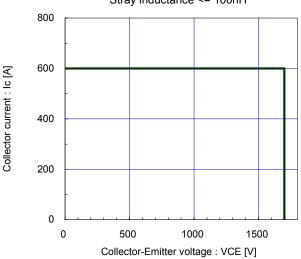
Switching loss vs. Collector current (typ.) Vcc=900V, VGE= $\pm$ 15V, Rg=1.5 $\Omega$ 



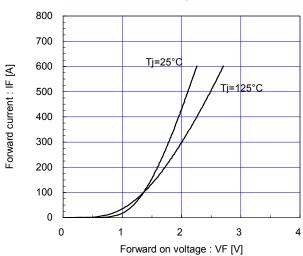
Switching loss vs. Gate resistance (typ.) Vcc=900V, Ic=300A, VGE=±15V, Tj= 125°C



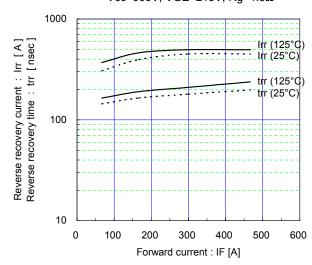
Reverse bias safe operating area (max.)  $+VGE=15V,-VGE <= 15V, RG >= 1.5\Omega, Tj <= 125°C$ Stray inductance <= 100nH



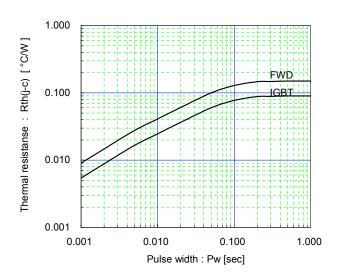
Forward current vs. Forward on voltage (typ.) chip



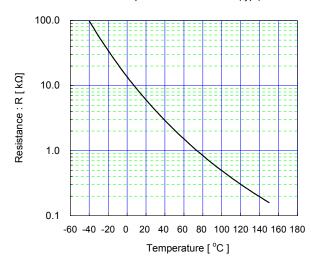
Reverse recovery characteristics (typ.) Vcc=900V,  $VGE=\pm15V$ ,  $Rg=1.5\Omega$ 



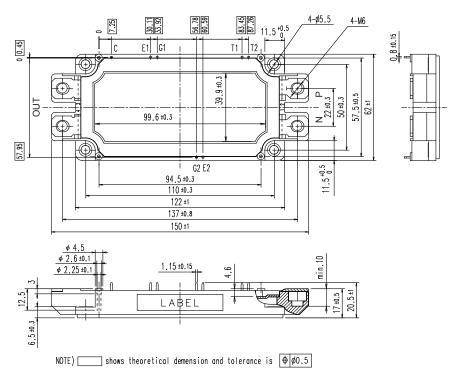
Transient thermal resistance (max.)



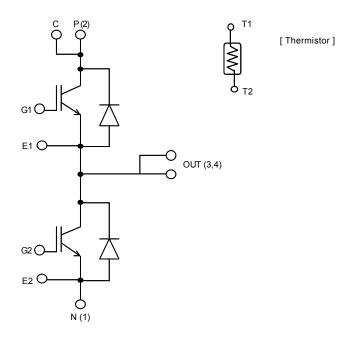
[ Thermistor ]
Temperature characteristic (typ.)



# ■ Outline Drawings, mm



# **■** Equivalent Circuit Schematic



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