

APT4065GN	400V	8.5A	0.65Ω
APT3565GN	350V	8.5A	0.65Ω
APT4080GN	400V	7.5A	0.80Ω
APT3580GN	350V	7.5A	0.80Ω

POWER MOS IV™

N - CHANNEL ENHANCEMENT MODE HIGH VOLTAGE POWER MOSFETS

MAXIMUM RATINGS

All Ratings: $T_C = 25^\circ\text{C}$ unless otherwise specified.

Symbol	Parameter	APT 3565GN	APT 4065GN	APT 3580GN	APT 4080GN	UNIT
V _{DSS}	Drain-Source Voltage	350	400	350	400	Volts
I _D	Continuous Drain Current @ T _C = 25°C	8.5		7.5		Amps
I _{DM}	Pulsed Drain Current ①	34		30		
V _{GS}	Gate-Source Voltage	±30				Volts
P _D	Total Power Dissipation @ T _C = 25°C	100				Watts
	Linear Derating Factor	0.8				W/°C
T _J ,T _{STG}	Operating and Storage Junction Temperature Range	-55 to 150				°C
T _L	Lead Temperature: 0.063" from Case for 10 Sec.	300				

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions / Part Number		MIN	TYP	MAX	UNIT
BV_{DS}	Drain-Source Breakdown Voltage ($V_{GS} = 0\text{V}$, $I_D = 250\text{ }\mu\text{A}$)	APT4065GN / APT4080GN	400			Volts
		APT3565GN / APT3580GN	350			
$I_{D(ON)}$	On State Drain Current ② ($V_{DS} > I_{D(ON)} \times R_{DS(ON)}$ Max, $V_{GS} = 10\text{V}$)	APT4065GN / APT3565GN	8.5			Amps
		APT4080GN / APT3580GN	7.5			
$R_{DS(ON)}$	Drain-Source On-State Resistance ② ($V_{GS} = 10\text{V}$, $0.5 I_D$ [Cont.])	APT4065GN / APT3565GN			0.65	Ohms
		APT4080GN / APT3580GN			0.80	
I_{DSS}	Zero Gate Voltage Drain Current ($V_{DS} = V_{DSS}$, $V_{GS} = 0\text{V}$)				250	μA
	Zero Gate Voltage Drain Current ($V_{DS} = 0.8 V_{DSS}$, $V_{GS} = 0\text{V}$, $T_C = 125^\circ\text{C}$)				1000	
I_{GSS}	Gate-Source Leakage Current ($V_{GS} = \pm 30\text{V}$, $V_{DS} = 0\text{V}$)				± 100	nA
$V_{GS(TH)}$	Gate Threshold Voltage ($V_{DS} = V_{GS}$, $I_D = 1.0\text{mA}$)		2		4	Volts

THERMAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to Case			1.20	$^\circ\text{C/W}$
$R_{\theta JA}$	Junction to Ambient			80	

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DYNAMIC CHARACTERISTICS

APT4065/3565/4080/3580GN

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C_{DC}	Drain-to-Case Capacitance	$f = 1 \text{ MHz}$		8	12	pF
C_{iss}	Input Capacitance	$V_{GS} = 0V$		730	950	
C_{oss}	Output Capacitance	$V_{DS} = 25V$		193	270	
C_{rss}	Reverse Transfer Capacitance	$f = 1 \text{ MHz}$		77	115	
Q_g	Total Gate Charge ③	$V_{GS} = 10V$		36	55	nC
Q_{gs}	Gate-Source Charge	$V_{DD} = 0.5 V_{DSS}$		4.7	7	
Q_{gd}	Gate-Drain ("Miller") Charge	$I_D = I_D [\text{Cont.}] @ 25^\circ\text{C}$		17	25	
$t_{d(on)}$	Turn-on Delay Time	$V_{GS} = 15V$		9	18	ns
t_r	Rise Time	$V_{DD} = 0.5 V_{DSS}$		14	28	
$t_{d(off)}$	Turn-off Delay Time	$I_D = I_D [\text{Cont.}] @ 25^\circ\text{C}$		33	50	
t_f	Fall Time	$R_G = 1.8\Omega$		12	24	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
I_S	Continuous Source Current (Body Diode)	APT4065GN / APT3565GN		8.5	Amps
		APT4080GN / APT3580GN		7.5	
I_{SM}	Pulsed Source Current ① (Body Diode)	APT4065GN / APT3565GN		34	
		APT4080GN / APT3580GN		30	
V_{SD}	Diode Forward Voltage ② ($V_{GS} = 0V, I_S = -I_D [\text{Cont.}]$)			1.3	Volts
t_{rr}	Reverse Recovery Time ($I_S = -I_D [\text{Cont.}]$, $di_S/dt = 100A/\mu s$)	111	223	446	ns
Q_{rr}	Reverse Recovery Charge ($I_S = -I_D [\text{Cont.}]$, $di_S/dt = 100A/\mu s$)	1.2	2.5	5.0	μC

SAFE OPERATING AREA CHARACTERISTICS

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
SOA1	Safe Operating Area	$V_{DS} = 0.4 V_{DSS}, I_{DS} = P_D / 0.4 V_{DSS}, t = 1 \text{ Sec.}$	100			Watts
SOA2	Safe Operating Area	$I_{DS} = I_D [\text{Cont.}], V_{DS} = P_D / I_D [\text{Cont.}], t = 1 \text{ Sec.}$	100			
I_{LM}	Inductive Current Clamped	APT4065GN / APT3565GN	34			Amps
		APT4080GN / APT3580GN	30			

① Repetitive Rating: Pulse width limited by maximum junction temperature. See Transient Thermal Impedance Curve. (Fig.1)

② Pulse Test: Pulse width < 380 μs , Duty Cycle < 2%

③ See MIL-STD-750 Method 3471

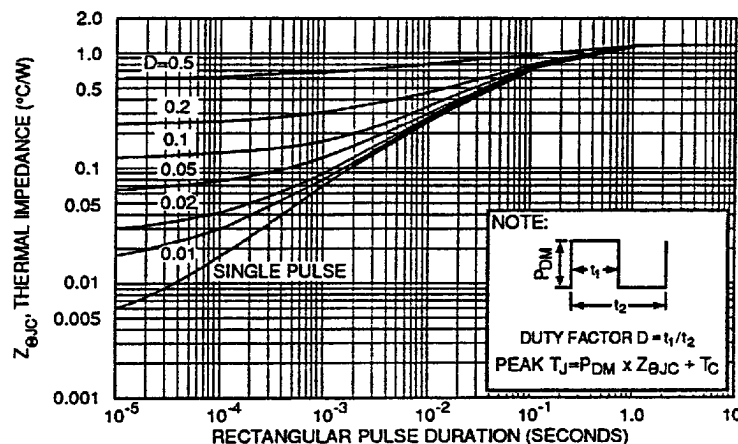


FIGURE 1, MAXIMUM EFFECTIVE TRANSIENT THERMAL IMPEDANCE, JUNCTION-TO-CASE vs PULSE DURATION

APT4065/3565/4080/3580GN

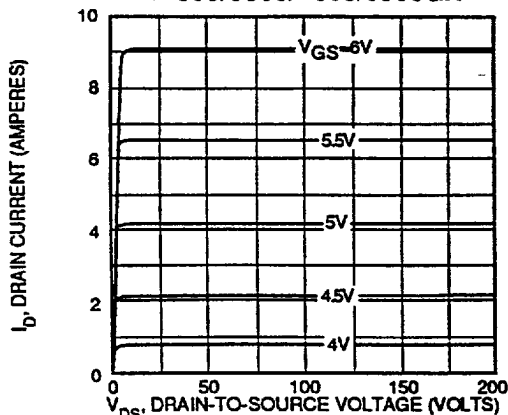


FIGURE 2, TYPICAL OUTPUT CHARACTERISTICS

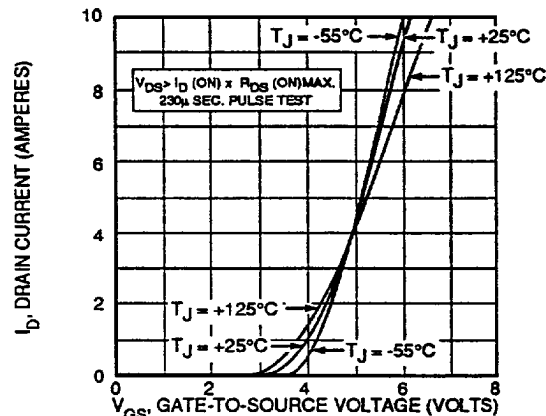


FIGURE 4, TYPICAL TRANSFER CHARACTERISTICS

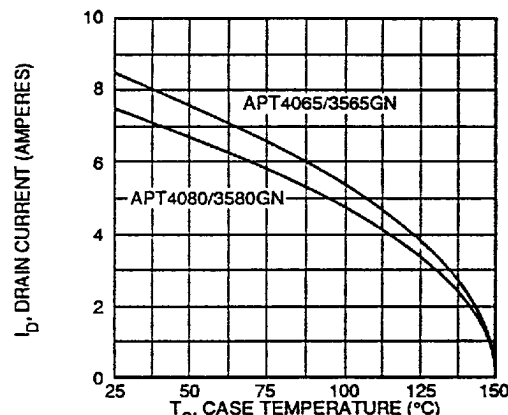


FIGURE 6, MAXIMUM DRAIN CURRENT vs CASE TEMPERATURE

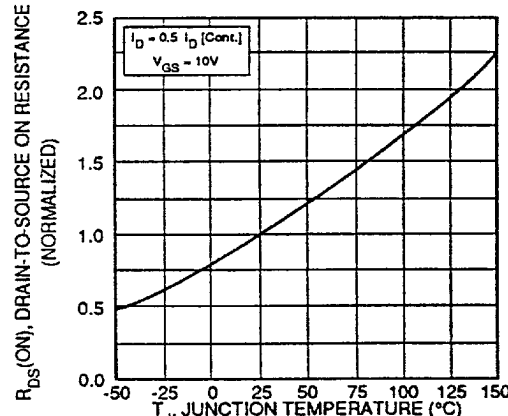


FIGURE 8, ON-RESISTANCE vs. TEMPERATURE

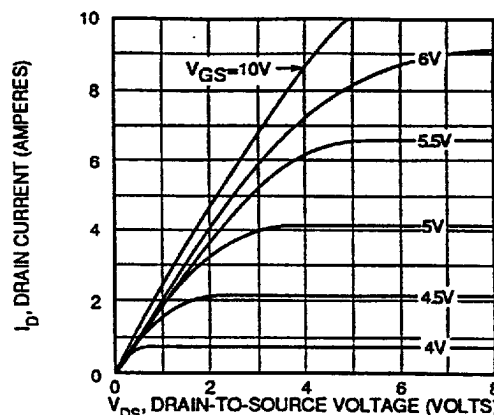


FIGURE 3, TYPICAL OUTPUT CHARACTERISTICS

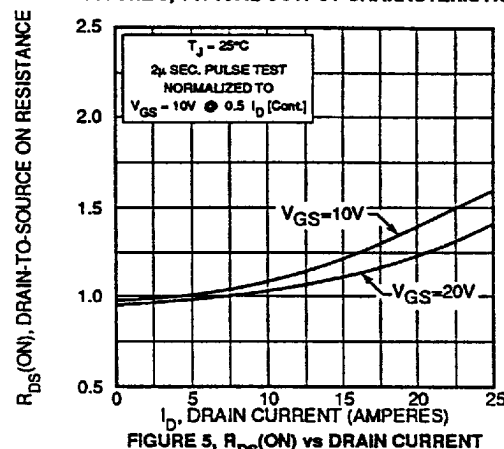


FIGURE 5, $R_{DS(ON)}$ vs DRAIN CURRENT

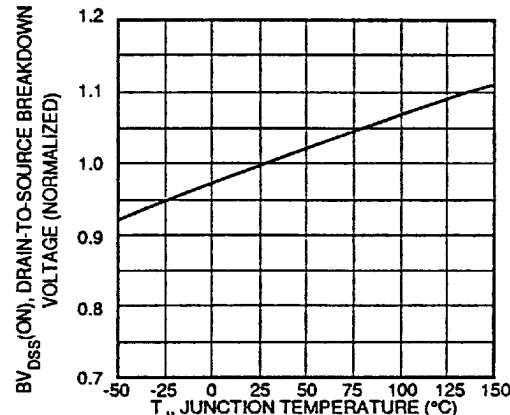


FIGURE 7, BREAKDOWN VOLTAGE vs TEMPERATURE

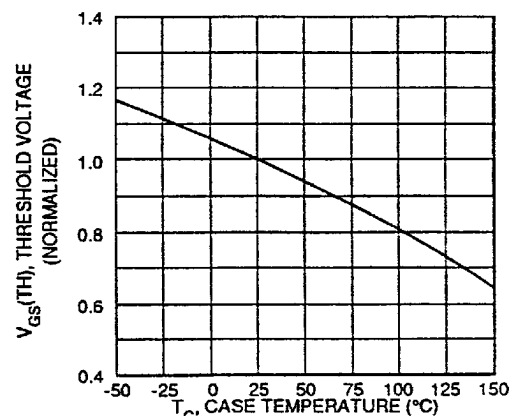


FIGURE 9, THRESHOLD VOLTAGE vs TEMPERATURE

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APT4065/3565/4080/3580GN

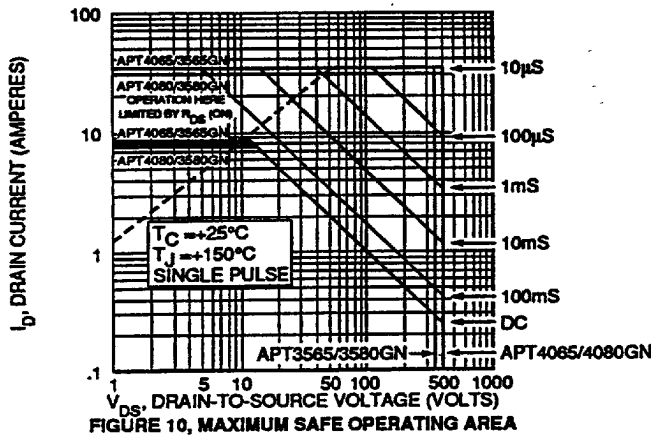


FIGURE 10, MAXIMUM SAFE OPERATING AREA

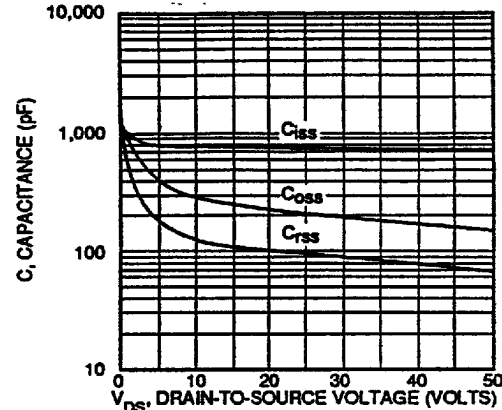


FIGURE 11, TYPICAL CAPACITANCE vs DRAIN-TO-SOURCE VOLTAGE

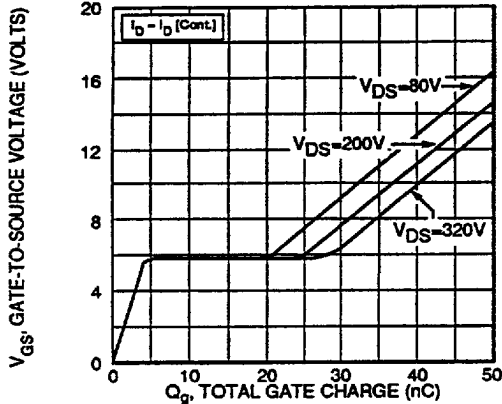


FIGURE 12, GATE CHARGES vs GATE-TO-SOURCE VOLTAGE

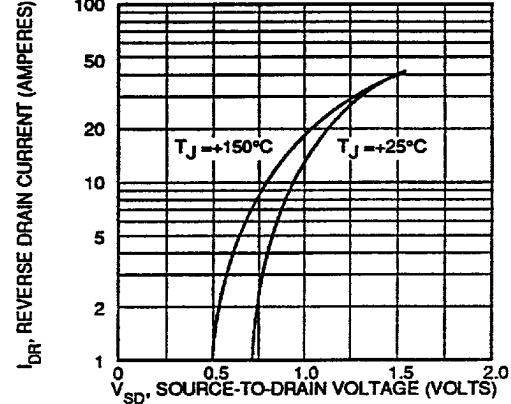
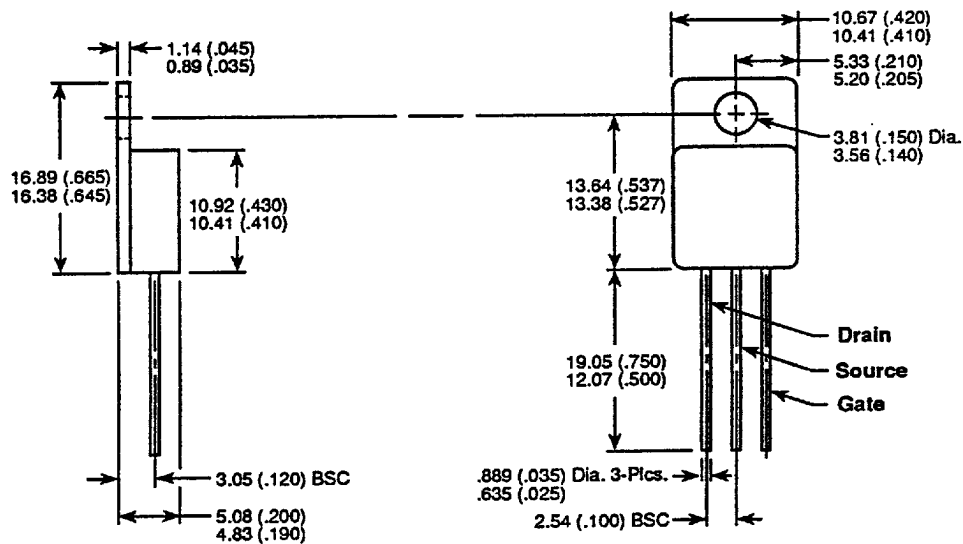


FIGURE 13, TYPICAL SOURCE-DRAIN DIODE FORWARD VOLTAGE

TO-257AA Package Outline



Dimensions in Millimeters and (Inches)

050-4019
Rev A

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