Application Notes

TITLE: EM61 Wake-up Time

Notes number :	AP-EM61-0007E-V1
REVISION DATE :	5 September 2002
REVISION VERSION :	1.0
AFFECTED CHIPS :	All EM61 Series Chips

The EM61 series chips wake-up time is WDT time based.   As the WDT timer utilizes internal RC mode for oscillation, fluctuation of the Vcc relative to wake-up time is apparent.   Hence, when Vcc is low, wake-up time is longer.

This condition is illustrated in the following chart:
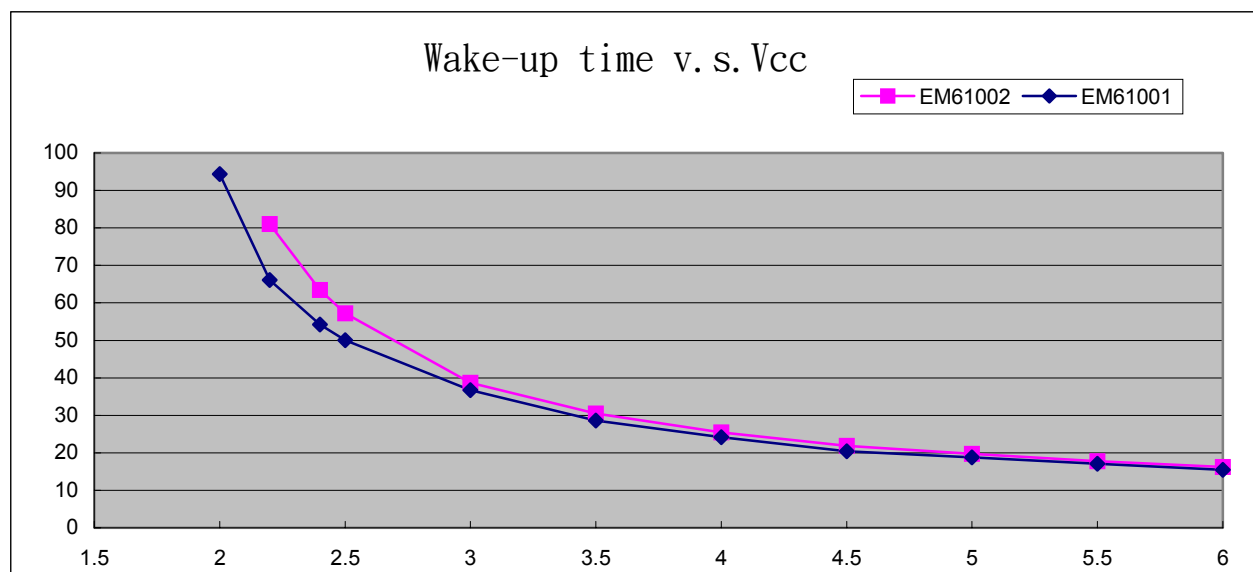
When working with EM61 series chip at low Vcc, do take into account the expected change in the length of wake-up time.

Elan has established that the wake-up time length should be –
<center>120 ms (max.) at Vcc=2.4V.</center>

Elan guarantees that this wake-up timing specification will be implemented to all future releases of the EM61 series chips.