

2SK1255

Silicon N-channel Power F-MOS FET

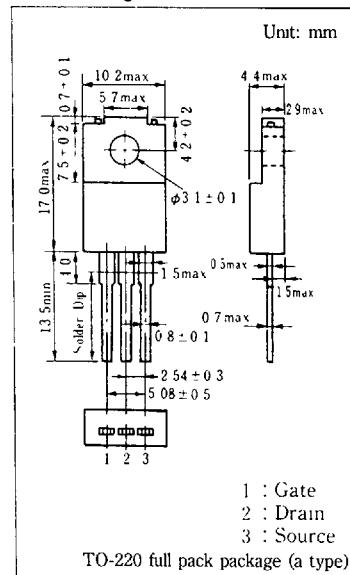
■ Features

- Low ON resistance R_{DS} (on) : R_{DS} (on) $= 0.135\Omega$ (typ.)
- High switching rate : $t_f = 53\text{ns}$ (typ.)
- No secondary breakdown
- Low voltage drive is possible ($V_{GS} = 4\text{V}$).

■ Application

- DC-DC converter
- No contact relay
- Solenoid drive
- Motor drive

■ Package Dimensions



■ Absolute Maximum Ratings ($T_c=25^\circ\text{C}$)

Item	Symbol	Value	Unit
Drain-source voltage	V_{DSS}	60	V
Gate-source voltage	V_{GS}	± 20	V
Drain current	At 4V driving	I_D	3
	DC	I_D	5
	Peak-to-peak value	I_{DP}	10
Power dissipation	$T_c = 25^\circ\text{C}$	P_D	30
	$T_a = 25^\circ\text{C}$		2.0
Channel temperature	T_{ch}	150	°C
Storage temperature	T_{stg}	-55 ~ +150	°C

■ Electrical Characteristics ($T_c=25^\circ\text{C}$)

Item	Symbol	Condition	min.	typ.	max.	Unit
Drain current	I_{DSS}	$V_{DS} = 40\text{V}$, $V_{GS} = 0$			10	μA
Gate-source current	I_{GSS}	$V_{GS} = \pm 20\text{V}$, $V_{DS} = 0$			± 1	μA
Drain-source voltage	V_{DSS}	$I_D = 1\text{mA}$, $V_{GS} = 0$	60			V
Gate threshold voltage	V_{th}	$V_{DS} = 10\text{V}$, $I_D = 1\text{mA}$	1		2.5	V
Drain-source ON resistance	$R_{DS(on)1}$	$V_{GS} = 10\text{V}$, $I_D = 3\text{A}$		0.135	0.2	Ω
Drain-source ON resistance	$R_{DS(on)2}$	$V_{GS} = 4\text{V}$, $I_D = 2\text{A}$		0.2	0.3	Ω
Forward transfer admittance	Y _{fs}	$V_{DS} = 10\text{V}$, $I_D = 3\text{A}$	2.4	4.0		S
Input capacitance	C_{iss}	$V_{DS} = 10\text{V}$, $V_{GS} = 0$, $f = 1\text{MHz}$		400		pF
Output capacitance	C_{oss}			210		pF
Reverse transfer capacitance	C_{rss}			80		pF
Turn-on time	t_{on}	$V_{GS} = 10\text{V}$, $I_D = 3\text{A}$		29		ns
Fall time	t_f			53		ns
Delay time	$t_d(\text{off})$			97		ns

