

# FML12N60ES

FUJI POWER MOSFET

## Super FAP-E<sup>3</sup> series

N-CHANNEL SILICON POWER MOSFET

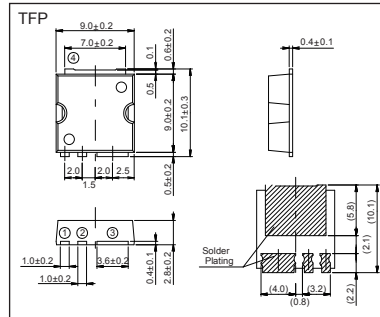
### Features

- Maintains both low power loss and low noise
- Lower R<sub>DS(on)</sub> characteristic
- More controllable switching dv/dt by gate resistance
- Smaller V<sub>GS</sub> ringing waveform during switching
- Narrow band of the gate threshold voltage (4.2±0.5V)
- High avalanche durability

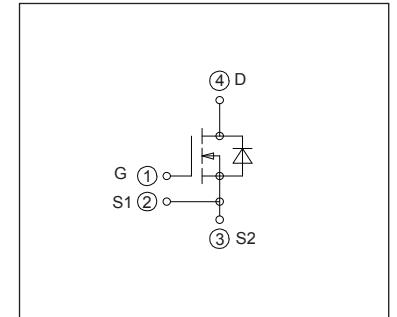
### Applications

- Switching regulators
- UPS (Uninterruptible Power Supply)
- DC-DC converters

### Outline Drawings [mm]



### Equivalent circuit schematic



### Maximum Ratings and Characteristics

#### Absolute Maximum Ratings at T<sub>c</sub>=25°C (unless otherwise specified)

Description	Symbol	Characteristics	Unit	Remarks
Drain-Source Voltage	V <sub>DS</sub>	600	V	
	V <sub>DSDX</sub>	600	V	V <sub>GS</sub> = -30V
Continuous Drain Current	I <sub>D</sub>	±12	A	
Pulsed Drain Current	I <sub>DP</sub>	±48	A	
Gate-Source Voltage	V <sub>GS</sub>	±30	V	
Repetitive and Non-Repetitive Maximum Avalanche Current	I <sub>AR</sub>	12	A	Note*1
Non-Repetitive Maximum Avalanche Energy	E <sub>AS</sub>	384	mJ	Note*2
Repetitive Maximum Avalanche Energy	E <sub>AR</sub>	18	mJ	Note*3
Peak Diode Recovery dV/dt	dV/dt	4.4	kV/μs	Note*4
Peak Diode Recovery -di/dt	-di/dt	100	A/μs	Note*5
Maximum Power Dissipation	P <sub>D</sub>	1.44	W	T <sub>a</sub> =25°C
		180		T <sub>c</sub> =25°C
Operating and Storage Temperature range	T <sub>ch</sub>	150	°C	
	T <sub>slg</sub>	-55 to +150	°C	

#### Electrical Characteristics at T<sub>c</sub>=25°C (unless otherwise specified)

Description	Symbol	Conditions	min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V	600	-	-	V
Gate Threshold Voltage	V <sub>GS</sub> (th)	I <sub>D</sub> =250μA, V <sub>DS</sub> =V <sub>GS</sub>	3.7	4.2	4.7	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =600V, V <sub>GS</sub> =0V	-	-	25	μA
		V <sub>DS</sub> =480V, V <sub>GS</sub> =0V	-	-	250	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V	-	10	100	nA
Drain-Source On-State Resistance	R <sub>DS</sub> (on)	I <sub>D</sub> =6A, V <sub>GS</sub> =10V	-	0.641	0.75	Ω
Forward Transconductance	g <sub>fs</sub>	I <sub>D</sub> =6A, V <sub>DS</sub> =25V	4	8	-	S
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V	-	1300	1950	pF
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> =0V	-	150	225	
Reverse Transfer Capacitance	C <sub>rss</sub>	f=1MHz	-	8.5	13	
Turn-On Time	td(on)	V <sub>cc</sub> =300V	-	40	60	ns
	tr	V <sub>GS</sub> =10V	-	40	60	
Turn-Off Time	td(off)	I <sub>D</sub> =6A	-	74	111	
	tf	R <sub>G</sub> =27Ω	-	19	29	
Total Gate Charge	Q <sub>G</sub>	V <sub>cc</sub> =300V	-	37	56	nC
Gate-Source Charge	Q <sub>GS</sub>	I <sub>D</sub> =12A	-	15	23	
Drain-Source Crossover Charge	Q <sub>SW</sub>	V <sub>GS</sub> =10V	-	6.5	10	
Gate-Drain Charge	Q <sub>GD</sub>		-	12	18	
Avalanche Capability	I <sub>AV</sub>	L=2.64mH, T <sub>ch</sub> =25°C	12	-	-	A
Diode Forward On-Voltage	V <sub>SD</sub>	I <sub>F</sub> =12A, V <sub>GS</sub> =0V, T <sub>ch</sub> =25°C	-	0.86	1.30	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =12A, V <sub>GS</sub> =0V	-	0.52	-	μs
Reverse Recovery Charge	Q <sub>rr</sub>	-di/dt=100A/μs, T <sub>ch</sub> =25°C	-	5.5	-	μC

#### Thermal Characteristics

Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	R <sub>th</sub> (ch-c)	Channel to case			0.69	°C/W
	R <sub>th</sub> (ch-a)	Channel to Ambient			87	°C/W
	R <sub>th</sub> (ch-a)	Channel to Ambient Note*6			52	°C/W

Note \*1 : T<sub>ch</sub>≤150°C

Note \*2 : Stating T<sub>ch</sub>=25°C, I<sub>AS</sub>=5A, L=33.8mH, V<sub>cc</sub>=50V, R<sub>G</sub>=10Ω,

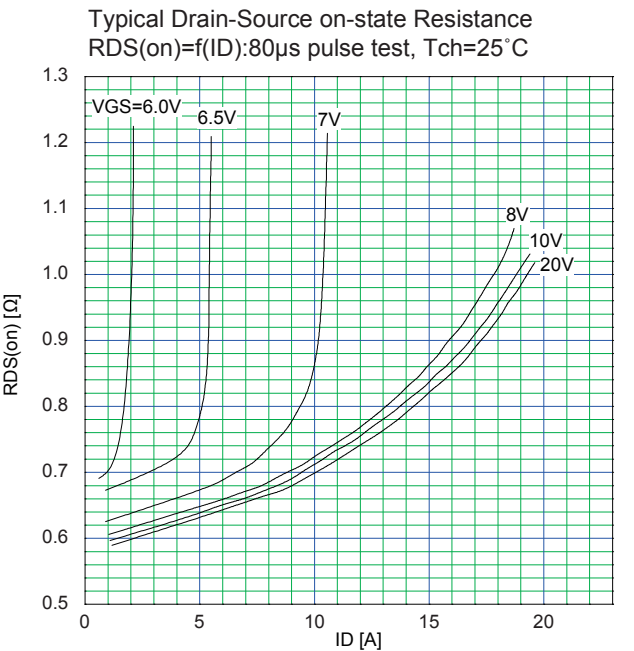
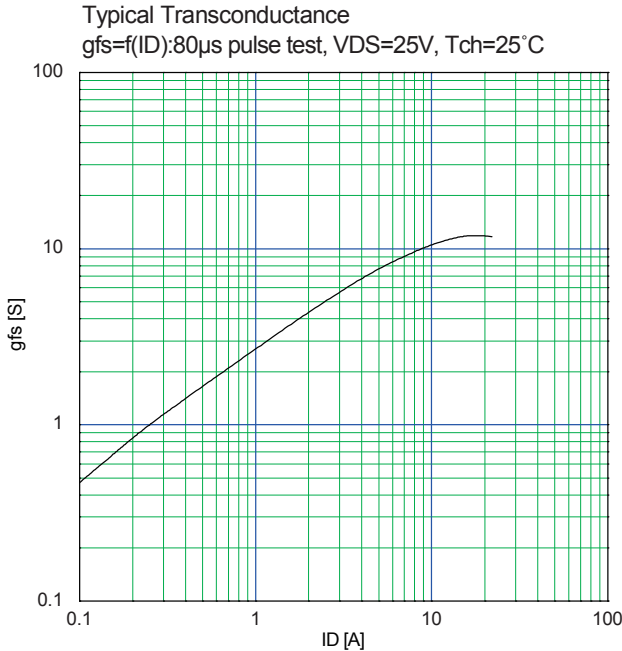
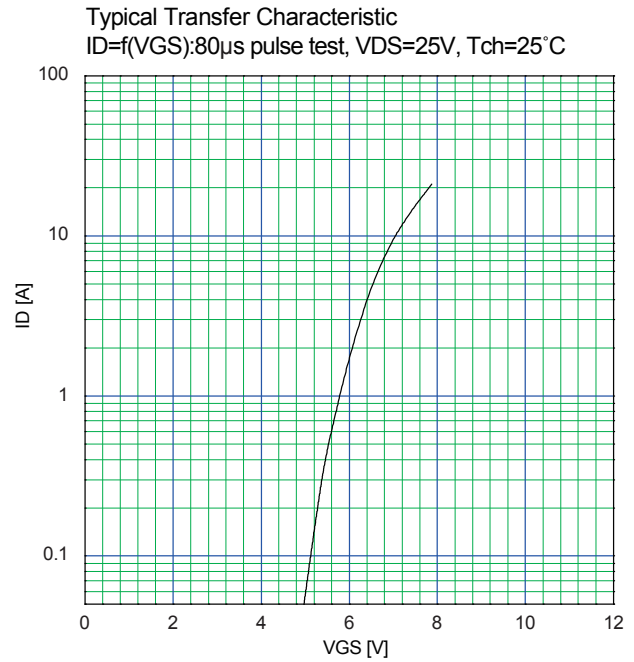
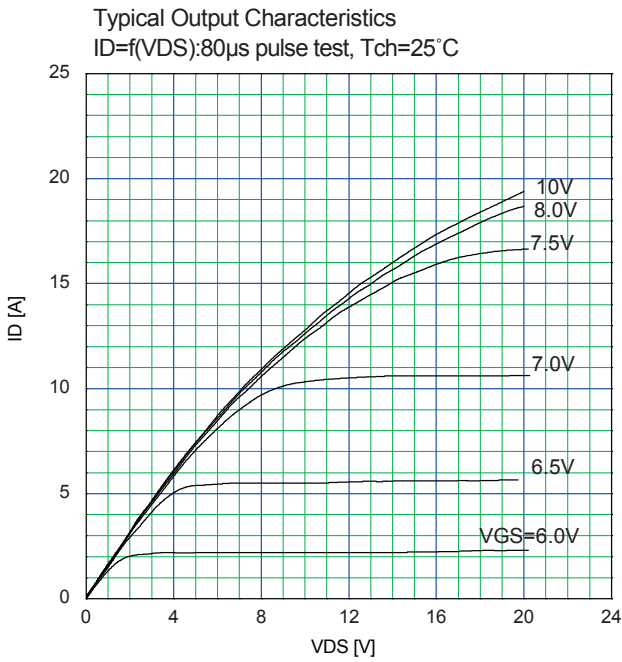
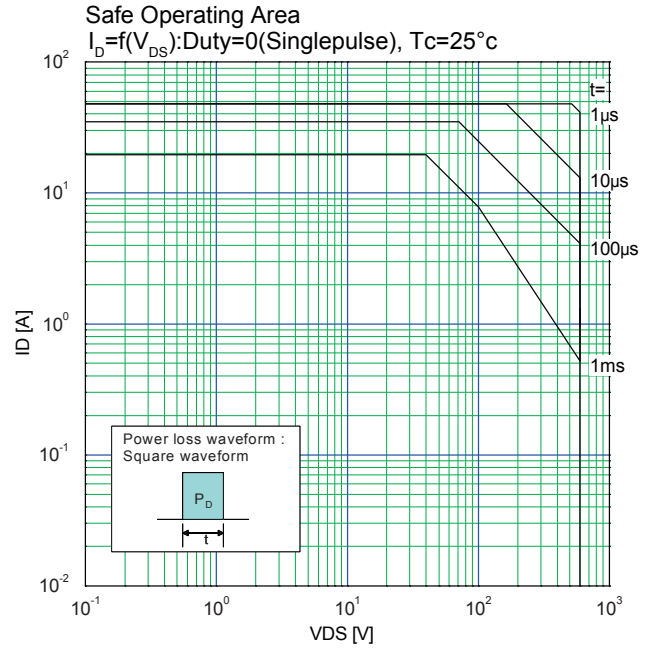
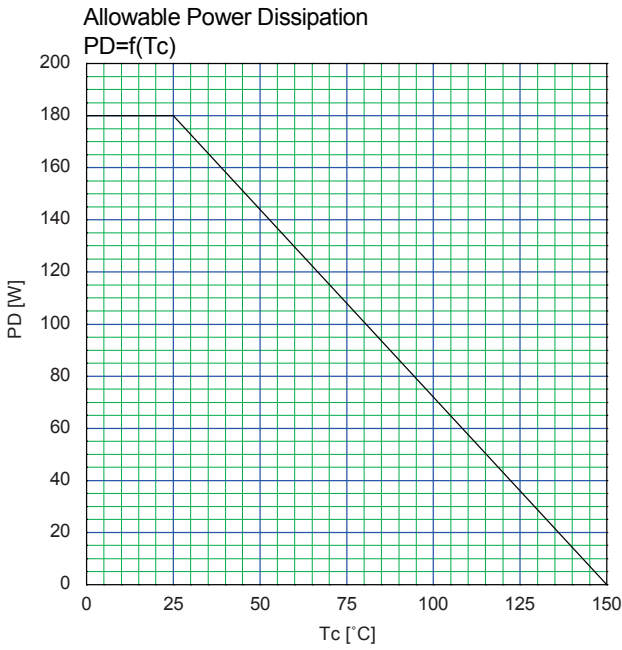
E<sub>AS</sub> limited by maximum channel temperature and avalanche current.

Note \*3 : Repetitive rating : Pulse width limited by maximum channel temperature.

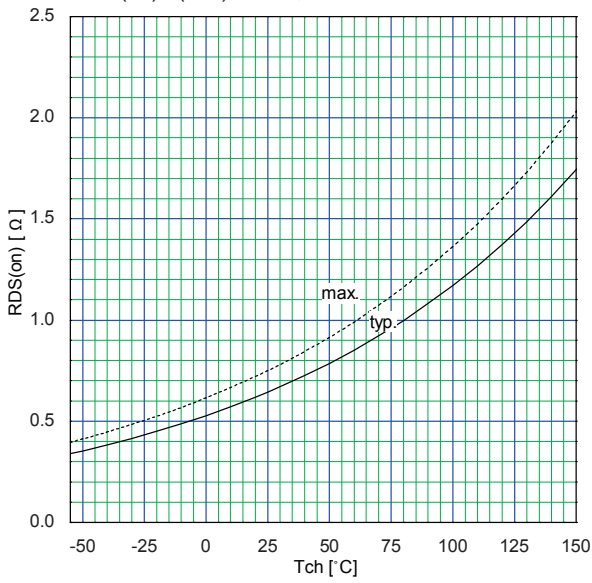
Note \*4 : I<sub>F</sub>≤I<sub>D</sub>, -di/dt=100A/μs, V<sub>CC</sub>≤BV<sub>DSS</sub>, T<sub>ch</sub>≤150°C.

Note \*5 : I<sub>F</sub>≤I<sub>D</sub>, dv/dt=6.3kV/μs, V<sub>CC</sub>≤BV<sub>DSS</sub>, T<sub>ch</sub>≤150°C.

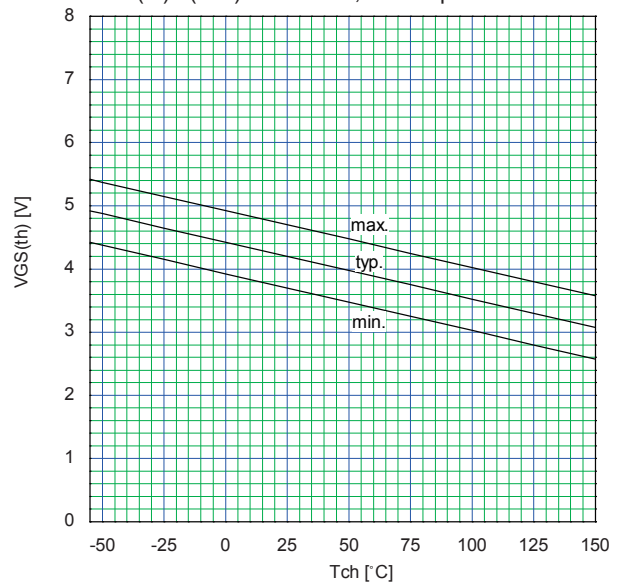
Note \*6 : Surface mounted on 1000mm<sup>2</sup>, t=1.6mm FR-4 PCB (Drain pad area : 500mm<sup>2</sup>)



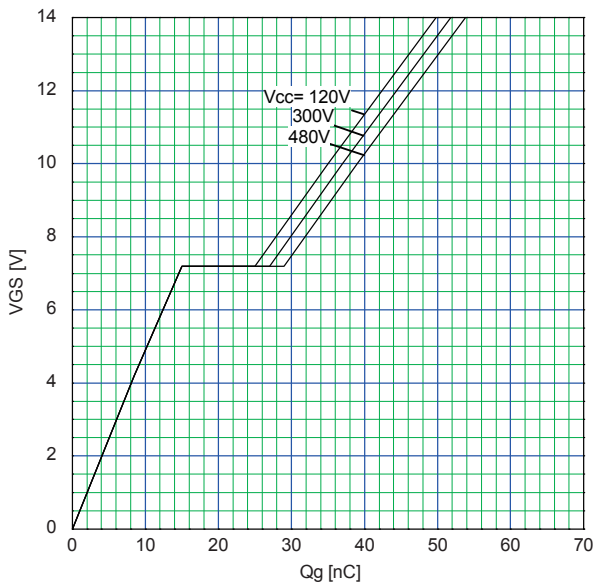
Drain-Source On-state Resistance  
 $R_{DS(on)}=f(T_{ch}):I_D=6A, V_{GS}=10V$



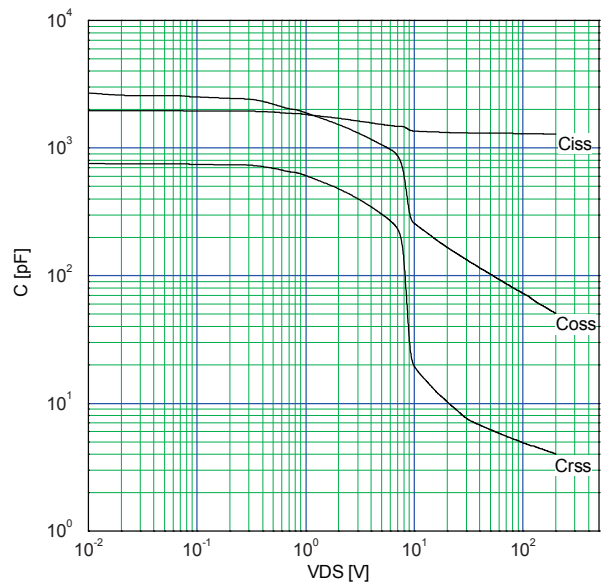
Gate Threshold Voltage vs. T<sub>ch</sub>  
 $V_{GS(th)}=f(T_{ch}):V_{DS}=V_{GS}, I_D=250\mu A$



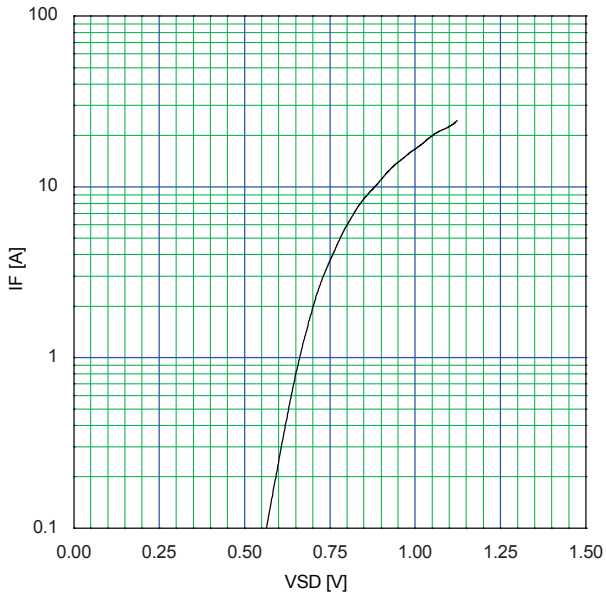
Typical Gate Charge Characteristics  
 $V_{GS}=f(Q_g):I_D=12A, T_{ch}=25^\circ C$



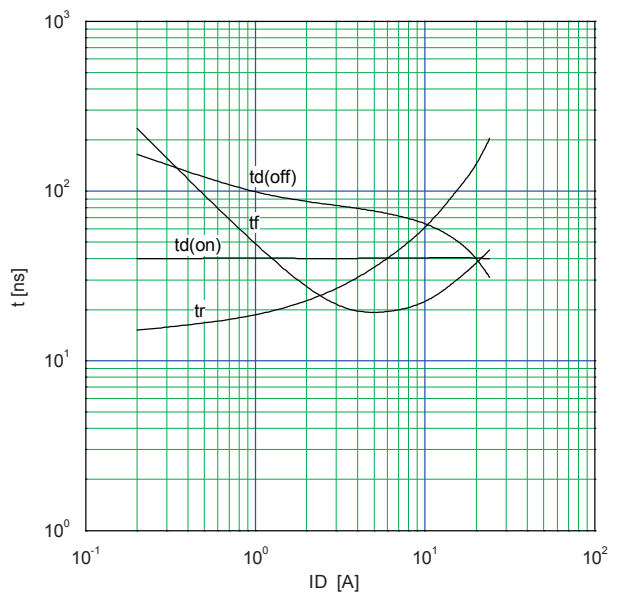
Typical Capacitance  
 $C=f(V_{DS}):V_{GS}=0V, f=1MHz$



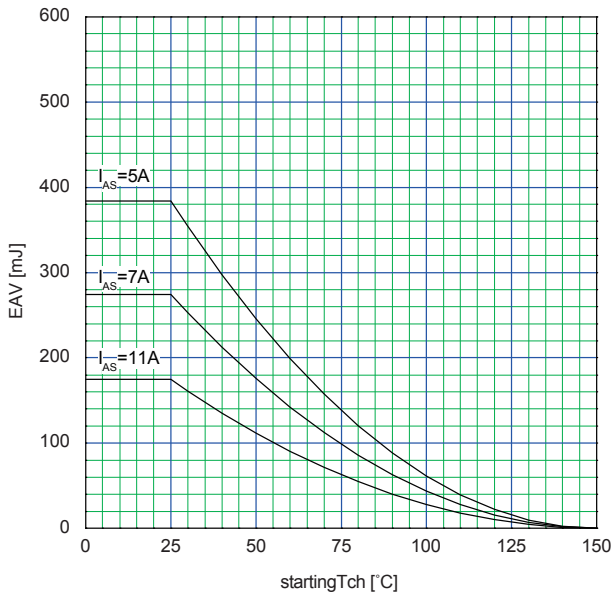
Typical Forward Characteristics of Reverse Diode  
 $I_F=f(V_{SD}):80\mu s \text{ pulse test}, T_{ch}=25^\circ C$



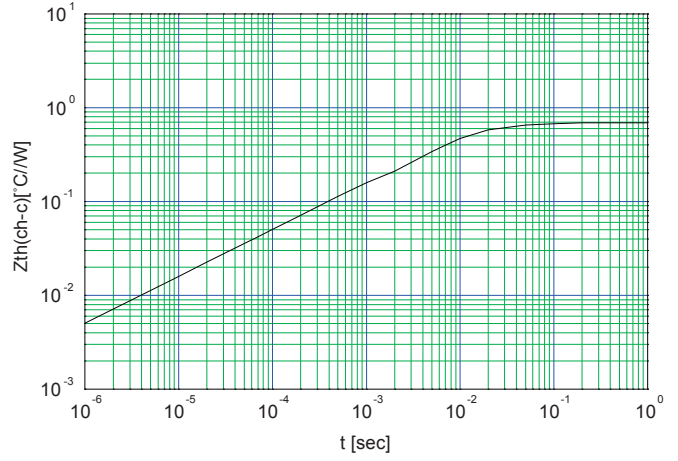
Typical Switching Characteristics vs. ID  
 $t=f(I_D):V_{cc}=300V, V_{GS}=10V, R_G=27\Omega$



Maximum Avalanche Energy vs. starting Tch  
 $E(AV)=f(\text{starting Tch}):V_{CC}=60V, I(AV)\leq 12A$



Transient Thermal Impedance  
 $Z_{th(ch-c)}=f(t):D=0$



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