# μPD7823X Family Of 30E D 8-Bit, Single-Chip, Real-Time Microcomputer With Internal A/D and D/A Converters

NEC Electronics Inc.

# **Advance Information**

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## Description

The  $\mu$ PD7823X high-performance, 8-bit single-chip microcomputers contain extended addressing capabilities for up to 1M byte of external memory. These devices also integrate sophisticated analog and digital peripherals, as well as two low-power standby modes which make it ideal for low-power/battery backup applications.

The µPD7823X core focuses on embedded control with features like hardware multiply and divide, two levels of interrupt response, four banks of main registers for multitasking, and macro service for processor independent peripheral and memory DMA. Augmenting this high performance core are advanced components like a high precision A/D converter, D/A converter, two independent serial interfaces, several counter/timers for PWM outputs, one dedicated PWM output, as well as a real time output port. On-board memory includes 640 bytes of RAM and 16K bytes of either mask-programmable ROM or EPROM.

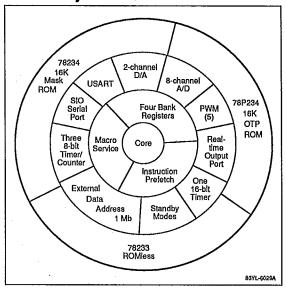
The macro service routine allows data to be transferred between any combination of memory and peripherals independent of the current program execution. The four banks of processor registers allow simplified context switching to be performed. Both features, combined with powerful on-chip peripherals, make this part ideal for a wide variety of embedded control applications.

#### Features'

- □ Complete single-chip microcomputer
  - 8-blt ALU
  - 16K ROM
  - 640-byte RAM
  - Both 1- and 8-bit logic
- Instruction prefetch queueHardware multiply and divide
- □ Memory expansion
- 8085A bus-compatible
  - 64K program address space
  - 1M data address spaces
- Large I/O capacity: up to 54 I/O port lines
- Software programmable pull-up option
- Extensive timer/counter functions
  - One 16-bit timer/counter/event
  - Three 8- bit timer/counter/event
- □ Four PWM channels
- One dedicated PWM output
- □ Two 4-bit real-time output ports
- □ Eight channel 8-bit A/D converter

- □ 2-channel, 8-bit D/A Converter
- Extensive interrupt handler
  - Seven external interrupt ports (vectored)
  - Programmable priority
  - Macro-service mode
- □ Two independent serial ports
- □ Refresh output for pseudostatic RAM
- On-chip clock generator
  - 12 MHz maximum CPU clock frequency (0.33 μs minimum instruction cycle)
- CMOS silicon gate technology
- □ 5 V power supply

### 7823X Family Architecture



#### Ordering Information

Part Number	ROM/RAM	Package	Avallable
μPD78233GC μPD78233GJ μPD78233L	ROMiess	80-pin plastic miniflat 94-pin plastic miniflat 84-pin PLCC	Q489
μPD78234GC μPD78234GJ μPD78234L	16K Mask ROM	80-pin plastic miniflat 94-pin plastic miniflat 84-pin PLCC	Q389
μPD78P234G-36 μPD78P234GJ μPD78P234L	16K OTP ROM	80-pin plastic miniflat 94-pin plastic miniflat 84-pin PLGC	Q489



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**μPD7823X** 

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Block Diagram T-49-19-59

