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PMIC N/A	STANDARDIZED MILITARY DRAWING	PREPARED BY <i>Larry T. Gaudin</i> CHECKED BY <i>Tim H. Nash</i> APPROVED BY <i>William K. Beckman</i>		DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444									
THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE		DRAWING APPROVAL DATE		SIZE		CAGE CODE		MICROCIRCUIT, DIGITAL, BIPOLAR, LOW POWER SCHOTTKY TTL, OCTAL BUS TRANCEIVERS, OPEN COLLECTOR OUTPUTS, MONOLITHIC SILICON					
		15 JUNE 1990		A		67268		5962-89748					
AMSC N/A		REVISION LEVEL		SHEET 1									

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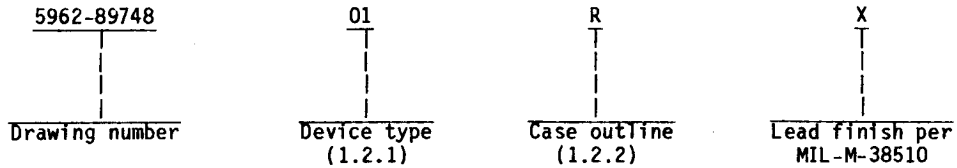
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5962-E1640

DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:



1.2.1 Device type. The device type shall identify the circuit function as follows:

Device type	Generic number	Circuit function
01	54LS642	Octal bus transceivers with inverting, open collector outputs

1.2.2 Case outline. The case outline shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter	Case outline
R	D-8 (20-lead, 1.060" x .310" x .200"), dual-in-line package
S	F-9 (20-lead, .540" x .300" x .100"), flat package
2	C-2 (20-terminal, .358" x .358" x .100"), square chip carrier package

1.3 Absolute maximum ratings.

Supply voltage range V_{CC}	- - - - -	-0.5 V dc to +7.0 V dc
DC input voltage: All inputs and I/O ports	- - - - -	-1.5 V dc at -18 mA to +7.0 V dc
Storage temperature range	- - - - -	-65°C to +150°C
Maximum power dissipation (P_D) 1/	- - - - -	522.5 mW
Lead temperature (soldering, 10 ⁻ seconds)	- - - - -	+300°C
Thermal resistance, junction-to-case (θ_{JC})	- - - - -	See MIL-M-38510, appendix C
Junction temperature (T_J)	- - - - -	+175°C

1.4 Recommended operating conditions.

Supply voltage range (V_{CC})	- - - - -	+4.5 V dc to +5.5 V dc
Minimum high-level input voltage (V_{IH})	- - - - -	2.0 V dc
Maximum low-level input voltage (V_{IL})	- - - - -	0.5 V dc
Maximum high-level output voltage (V_{OH})	- - - - -	5.5 V dc
Maximum input clamp current (I_{IC})	- - - - -	-18 mA
Maximum low-level output current (I_{OL})	- - - - -	+12 mA
Case operating temperature range (T_C)	- - - - -	-55°C to +125°C

1/ Maximum power dissipation is defined as $V_{CC} \times I_{CC}$.

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2. APPLICABLE DOCUMENTS

2.1 Government specification, standard, and bulletin. Unless otherwise specified, the following specification, standard, and bulletin of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

BULLETIN

MILITARY

MIL-BUL-103 - List of Standard Military Drawings (SMD's).

(Copies of the specification, standard, and bulletin required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.2 Truth table. The truth table shall be as specified on figure 2.

3.2.3 Test circuit and switching waveforms. The test circuit and switching waveforms shall be as specified on figure 3.

3.2.4 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and apply over the full case operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C < T _C < +125°C unless otherwise specified		Group A subgroups	Limits		Unit	
					Min	Max		
Hysteresis (V _{T+} - V _{T-})	HYST	V _{CC} = 4.5 V	A or B input 1/	1,2,3	0.1		V	
High level output current	I _{OH}	V _{CC} = 4.5 V, V _{IH} = 2 V, V _{IL} = 0.5 V	V _{OH} = 5.5 V,	1,2,3		0.1	mA	
Low level output voltage	V _{OL}	V _{CC} = 4.5 V, V _{IH} = 2.0 V, V _{IL} = 0.5 V	I _{OL} = 12 mA,	1,2,3		0.4	V	
Input clamp voltage	V _{IC}	V _{CC} = 4.5 V,	I _{IN} = -18 mA	1,2,3		-1.5	V	
High level input current	I _{IH1}	V _{CC} = 5.5 V	A or B	V _{IN} = 5.5 V	1,2,3		100	μA
			DIR or \bar{G}			V _{IN} = 7.0 V		
	I _{IH2}		V _{IN} = 2.7 V			20		
Low level input current	I _{IL}	V _{CC} = 5.5 V, V _{IN} = 0.4 V		1,2,3		-0.4	mA	
Supply current	I _{CCH}	V _{CC} = 5.5 V	Outputs high	1,2,3		70	mA	
	I _{CCL}		Outputs open			90		
	I _{CCZ}		Outputs disabled			95		
Functional tests		See 4.3.1c		7,8				

See footnotes at end of table.

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TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C < T _C < +125°C unless otherwise specified	Group A subgroups	Limits		Unit
				Min	Max	
Propagation delay time A to B	t _{PLH1}	V _{CC} = 5.0 V, 2/ C _L = 50 pF, R _L = 667Ω, See figure 3	9,10,11	1	35	ns
Propagation delay time B to A				1	35	
Propagation delay time A to B	t _{PHL1}		9,10,11	1	35	ns
Propagation delay time B to A				1	35	
Output disable time G, DIR to A	t _{PLH2}		9,10,11	1	56	ns
Output disable time G, DIR to B				1	56	
Output enable time G, DIR to A	t _{PHL2}		9,10,11	1	84	ns
Output enable time G, DIR to B				1	84	

1/ This parameter is guaranteed, but not tested.

2/ Testing may be performed using either C_L = 45 pF or C_L = 50 pF; however, the manufacturer shall certify that the microcircuits meet the switching test limits - specified for a 50 pF load.

3.5 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in MIL-BUL-103 (see 6.6 herein).

3.6 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-BUL-103 (see 6.6 herein). The certificate of compliance submitted to DESC-ECC prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

3.7 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change. Notification of change to DESC-ECC shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.9 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

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Terminal connections	
Terminal number	Device 01
	Cases
	R, S, and 2
1	DIR
2	A1
3	A2
4	A3
5	A4
6	A5
7	A6
8	A7
9	A8
10	GND
11	B8
12	B7
13	B6
14	B5
15	B4
16	B3
17	B2
18	B1
19	G
20	VCC

NC = No connection

FIGURE 1. Terminal connections.

Control inputs	Operation
G DIR	
L L	B data to A bus
L H	A data to B bus
H X	Isolation

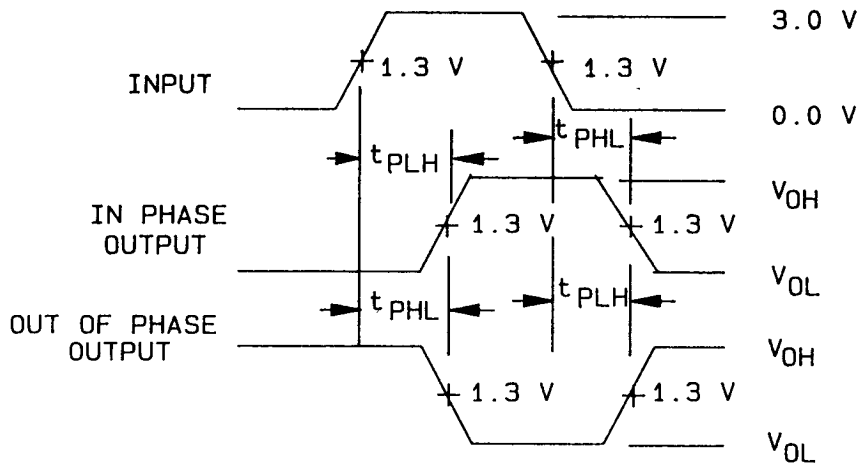
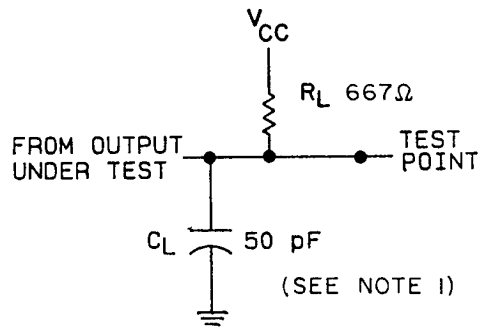
H = High level voltage
L = Low level voltage
X = Irrelevant

FIGURE 2. Truth table.

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- NOTES:
1. C_L includes probe and jig capacitance.
 2. All input pulses are supplied by generators having the following characteristics:
 $PRR \leq 1$ MHz, Z_{OUT} approximately 50 ohms, $t_r \leq 15$ ns, $t_f \leq 6$ ns.

FIGURE 3. Test circuit and switching waveforms.

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4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.6 herein).

(2) $T_A = +125^\circ\text{C}$, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 4, 5, and 6 in table I, method 5005 of MIL-STD-883 shall be omitted.

c. Subgroup 7 and 8 tests shall verify the truth table as specified on figure 2.

4.3.2 Groups C and D inspections.

a. End-point electrical parameters shall be as specified in table II herein.

b. Steady-state life test conditions, method 1005 of MIL-STD-883.

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.6 herein).

(2) $T_A = +125^\circ\text{C}$, minimum.

(3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	---
Final electrical test parameters (method 5004)	1*, 2, 3, 7, 8, 9, 10**, 11**
Group A test requirements (method 5005)	1, 2, 3, 7, 8, 9, 10**, 11**
Groups C and D end-point electrical parameters (method 5005)	1, 2, 3

* PDA applies to subgroup 1.

** Subgroups 10 and 11, if not tested, shall be guaranteed to the specified limits in table I.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-481 using DD Form 1693, Engineering Change Proposal (Short Form).

6.4 Record of users. Military and industrial users shall inform Defense Electronics Supply Center when a system application requires configuration control and the applicable SMD. DESC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DESC-ECS, telephone (513) 296-6022.

6.5 Comments. Comments on this drawing should be directed to DESC-ECC, Dayton, Ohio 45444, or telephone 513-296-8525.

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6.6 Approved source of supply. An approved source of supply is listed in MIL-BUL-103. Additional sources will be added to MIL-BUL-103 as they become available. The vendor listed in MIL-BUL-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DESC-ECS. The approved source of supply listed below is for information purposes only and is current only to the date of the last action of this document.

Military drawing part number	Vendor CAGE number	Vendor similar part number ^{1/}
5962-8974801RX	01295	SNJ54LS642J
5962-8974801SX	01295	SNJ54LS642W
5962-89748012X	01295	SNJ54LS642FK

^{1/} Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number

01295

Vendor name and address

Texas Instruments, Incorporated
 13500 N. Central Expressway
 P.O. Box 655303
 Dallas, TX 75265
 Point of contact: I-20 at FM 1788
 Midland, TX 79711-0448

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