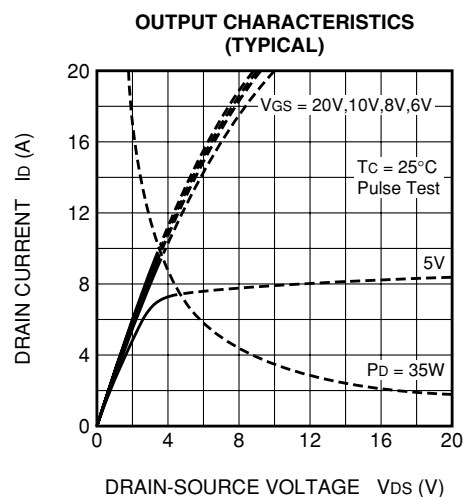
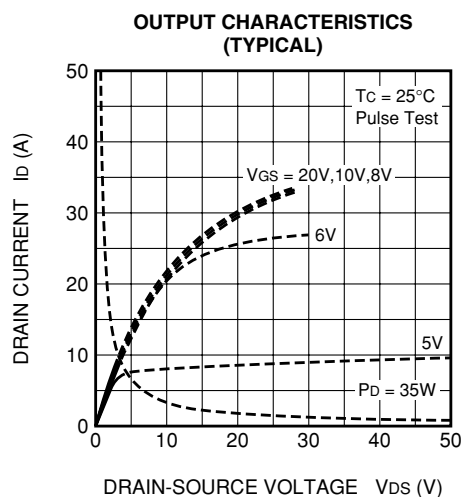
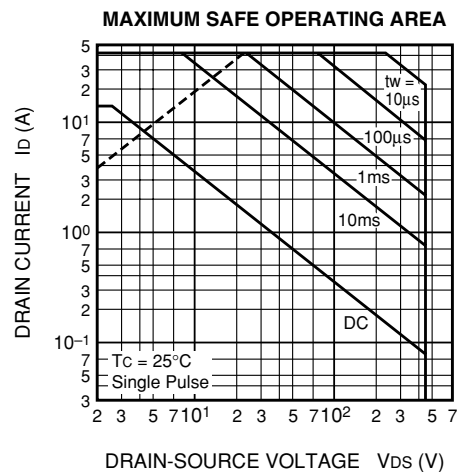
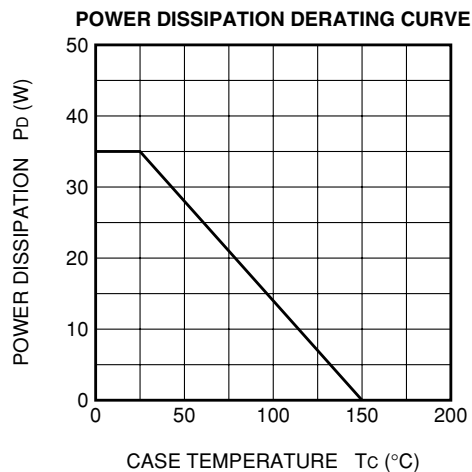


ELECTRICAL CHARACTERISTICS ($T_{ch} = 25^{\circ}\text{C}$)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
$V_{(BR)DSS}$	Drain-source breakdown voltage	$I_D = 1\text{mA}$, $V_{GS} = 0\text{V}$	450	—	—	V
$V_{(BR)GSS}$	Gate-source breakdown voltage	$I_{GS} = \pm 100\mu\text{A}$, $V_{DS} = 0\text{V}$	± 30	—	—	V
I_{GSS}	Gate-source leakage current	$V_{GS} = \pm 25\text{V}$, $V_{DS} = 0\text{V}$	—	—	± 10	μA
I_{DSS}	Drain-source leakage current	$V_{DS} = 450\text{V}$, $V_{GS} = 0\text{V}$	—	—	1	mA
$V_{GS(th)}$	Gate-source threshold voltage	$I_D = 1\text{mA}$, $V_{DS} = 10\text{V}$	2.5	3.0	3.5	V
$r_{DS(on)}$	Drain-source on-state resistance	$I_D = 7\text{A}$, $V_{GS} = 10\text{V}$	—	0.41	0.52	Ω
$V_{DS(on)}$	Drain-source on-state voltage	$I_D = 7\text{A}$, $V_{GS} = 10\text{V}$	—	2.87	3.64	V
$ y_{fs} $	Forward transfer admittance	$I_D = 7\text{A}$, $V_{DS} = 10\text{V}$	7.2	12.0	—	S
C_{iss}	Input capacitance	$V_{DS} = 25\text{V}$, $V_{GS} = 0\text{V}$, $f = 1\text{MHz}$	—	1500	—	pF
C_{oss}	Output capacitance		—	160	—	pF
C_{rss}	Reverse transfer capacitance		—	35	—	pF
$t_{d(on)}$	Turn-on delay time	$V_{DD} = 200\text{V}$, $I_D = 7\text{A}$, $V_{GS} = 10\text{V}$, $R_{GEN} = R_{GS} = 50\Omega$	—	25	—	ns
t_r	Rise time		—	40	—	ns
$t_{d(off)}$	Turn-off delay time		—	190	—	ns
t_f	Fall time		—	50	—	ns
V_{SD}	Source-drain voltage	$I_S = 7\text{A}$, $V_{GS} = 0\text{V}$	—	1.5	2.0	V
$R_{th(ch-c)}$	Thermal resistance	Channel to case	—	—	3.57	$^{\circ}\text{C/W}$

PERFORMANCE CURVES



FS14KM-9A

HIGH-SPEED SWITCHING USE

