

LC4016BM



3034A

CMOS Standard Logic LC4000B Series

## Quad Bilateral Switch

E1157C

The LC4016BM is a bilateral switch (also called analog switch) IC (meeting EIA/JEDEC standards) having such features as wide operating voltage range, high noise margin, low power dissipation. It has 4 independent bilateral switches. Setting control input (CONT) at "H" level causes the switches to be conducting, thereby resulting in a low impedance between input and output; and setting control input (CONT) at "L" level causes the switches to be nonconducting, thereby resulting in a high impedance between input and output. Its mini flat package enables compactness of sets.

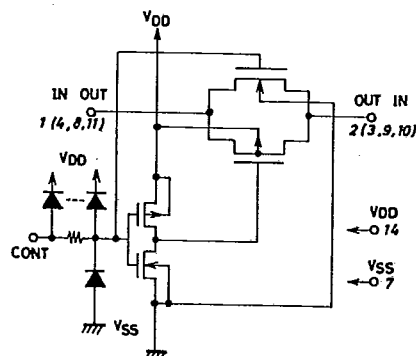
Absolute Maximum Ratings/ $T_a=25^\circ\text{C}$ ,  $V_{SS}=0\text{V}$ 

|                             |              |                    |                  |
|-----------------------------|--------------|--------------------|------------------|
| Maximum supply voltage      | $V_{DD}$ max | -0.5~+20           | unit             |
| Input voltage               | $V_{IN}$ max | -0.5~ $V_{DD}+0.5$ | V                |
| Input current               | $I_{IN}$ max | $\pm 10$           | mA               |
| Allowable power dissipation | $P_d$ max    | 150                | mW               |
| Operating temperature       | $T_{opg}$    | -40~+85            | $^\circ\text{C}$ |
| Storage temperature         | $T_{stg}$    | -65~+150           | $^\circ\text{C}$ |

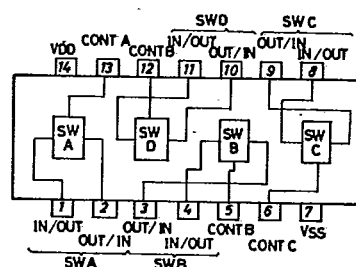
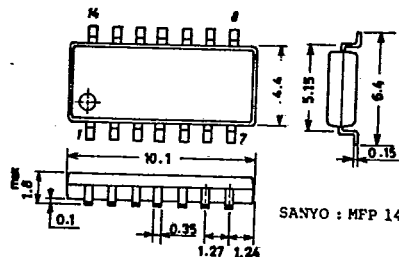
Allowable Operating Conditions/ $T_a=-40$  to  $+85^\circ\text{C}$ ,  $V_{SS}=0\text{V}$ 

|                |          |             |      |
|----------------|----------|-------------|------|
| Supply voltage | $V_{DD}$ | 3~18        | unit |
| Input voltage  | $V_{IN}$ | 0~ $V_{DD}$ | V    |

## Equivalent Circuit (1/4 LC4016BM)



## Pin Assignment and Block Diagram

Case Outline 3034A-M14IC  
(unit:mm)

For details, refer to the description of the LC4016B.