

ULTRA HIGH VOLTAGE VERY HIGH SPEED DIFFERENTIAL OP-AMP

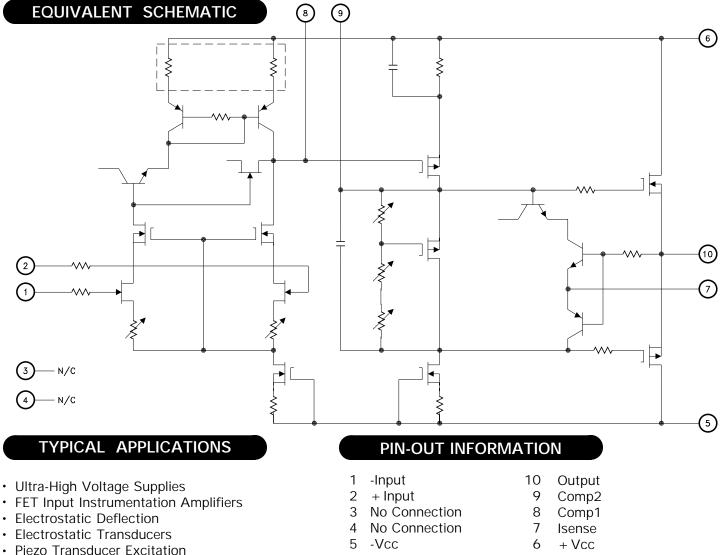
4707 Dey Road Liverpool, N.Y. 13088

FEATURES:

- Wide Supply Voltage Range 15V to 400V
- Fast Slew Rate 500 V/µS Typ.
- FET Input Accurate DC Specifications
- Electrically Isolated Case
- Low Cost Innovative Packaging
- Very Low Quiescent Current 9mA Typ.
- Output Current to ± 200 mA
- · Adjustable Current Limit

DESCRIPTION:

The MSK 131 is a high speed, high voltage differential amplifier designed for output currents up to \pm 150mA. Since the MSK 131 utilizes external compensation, it exhibits wide bandwidth and greater stability over a wide gain range. High frequency, high voltage instrumentation circuits and electrostatic transducers are just a sample of the applications that the MSK 131 is well suited for. The device is packaged in a 10 pin insulated ceramic SIP with holes for direct heat sink attachment.



131

(315) 701-6751

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ABSOLUTE MAXIMUM RATINGS

Vcc	Supply Voltage (Total) 400V		
ООТ	Output Current ± 300mA		
VIND	Differential Input Voltage ± 25V		
VINCM	Common Mode Input Voltage ± Vcc		
Rтн	Thermal Resistance		
www.datasheet4Junction to Case @ 125°C			
	(Output Devices)		

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Tst	Storage Temperature Range	-65°C to +150°C

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Tld
(10 Seconds)
ΤJ
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Case Operating Temperature Range Τc

ELECTRICAL SPECIFICATIONS

Parameter	Test Conditions ①		MSK 131			
Parameter		Min.	Тур. ③	Max.	Units	
STATIC						
Supply Voltage Range 2 ④	Total + Vcc to -Vcc	20	-	400	V	
Quiescent Current	VIN=OV	-	± 9	± 15	mA	
INPUT						
Input Offset Voltage	VIN=OV	-	± 0.5	± 2.0	mV	
Input Offset Voltage Drift ②	VIN=OV	-	± 10	± 50	µV/°C	
Input Bias Current ②	Vcm=OV Either Input	-	± 10	± 200	рА	
Input Offset Current ②	Vcm=0V	-	10	50	рА	
Input Impedance	F = DC	-	10 ¹¹	-	Ω	
Input Capacitance	Either Input	-	4.0	-	pF	
Power Supply Rejection Ratio ②	Δ Vcc= \pm 15V	-	± 10	± 20	μV/V	
Common Mode Rejection Ratio ②	$F = DC$ $Vcm = \pm 50V$	80	90	-	dB	
Common Mode Range (2)	Linear Operation	± Vcc-15	± Vcc-13	-	V	
Input Noise Voltage	F = 100KHz	-	1.5	-	μVrms	
OUTPUT						
Output Voltage Swing	$IOUT = \pm 50 mA$	± 91.5	± 95	-	V	
Output Current ②	Within SOA	± 150	± 200	-	mA	
Output Resistance	f≤10KHz, No Load	-	50	-	Ω	
TRANSFER CHARACTERISTICS						
Slew Rate Limit Rising Edge	Av = 100v/v $Cc = 0pF$	500	800	-	V/µS	
Open Loop Voltage Gain ②	F=15Hz Cc=OpF	95	110	-	dB	
Settling Time to 0.1%	$R_L = 1K\Omega 2V$ step $Cc = 10pF$	-	1	-	μS	

NOTES:

(1) \pm Vcc= \pm 100V, Tc= 25°C, Rc= 100 Ω , Cc= 68pF unless otherwise specified.

2 Devices shall be capable of meeting the parameter, but need not be tested.

③ Typical parameters are representative of actual device performance but are for reference only.
 ④ Maximum supply voltage should be derated 0.625V/°C below 25°C case temperature.

SAFE OPERATING AREA

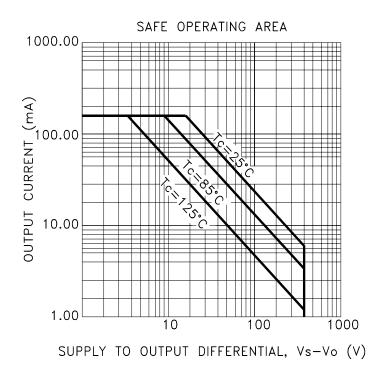
The output stage of the MSK 131 is fabricated using state of the art complimentary MOSFETs and is free from secondary breakdown limitations. There are two distinct limitations for the output stage:

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1. The internal wire bonds and the geometry of the MOSFET have a maximum peak current capability of \pm 300mA.

2. The junction temperature of each MOSFET should be kept below the maximum rating of 150° C.

The SOA Curves below illustrate various conditions of power dissipation.

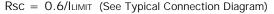


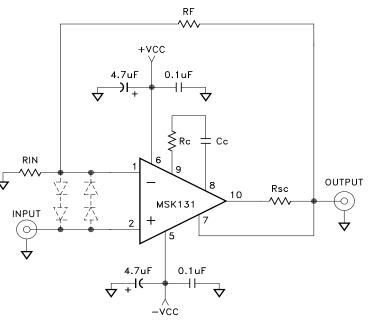
INPUT PROTECTION

The MSK 131 can safely handle up to $\pm 25V$ of differential input voltages. In applications where this may be violated, external protection is required. Four diodes can be used as shown in the typical connection diagram. If leakage current is of concern, use JFETs connected as diodes instead. JFETs will also yield very low capacitance for high speed applications.

CURRENT LIMIT

The MSK 131 has an internal active current limit circuit that can be programmed with a single external resistor Rsc. The value of this resistor should be kept between 2Ω and 150Ω . The following equation is used to select the resistor for a given current limit value:





TYPICAL CONNECTION DIAGRAM

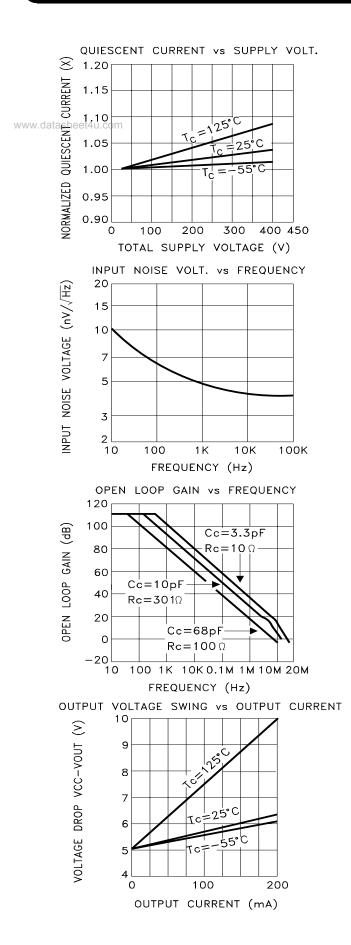
STABILITY AND COMPENSATION

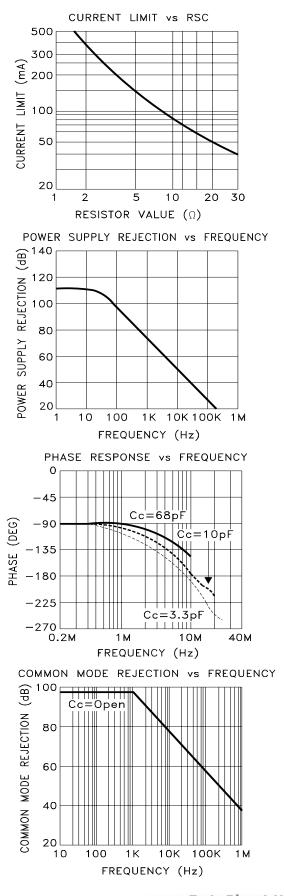
Since the MSK 131 is externally compensated the bandwidth can be optimized for any gain selection. The external compensation components should be located as close to the compensation pins as possible to avoid unwanted oscillations. The capacitor Cc should be rated for the full supply voltage. Use a high quality dielectric such as NPO to maintain a desired compensation over the full operating temperature. Refer to the typical performance curves for a guide to select the desired compensation. Refer to the typical connection diagram for the location of the Rc and Cc components.

POWER SUPPLIES

Both the negative and positive power supplies must be effectively decoupled with a high and low frequency bypass circuit to avoid power supply induced oscillation. An effective decoupling scheme consists of a 0.1 microfarad ceramic capacitor in parallel with a 4.7 microfarad tantalum capacitor for each power supply pin to ground. All power supply decoupling capacitors should be placed as close to the package power supply pins as possible (pins 5 and 6).

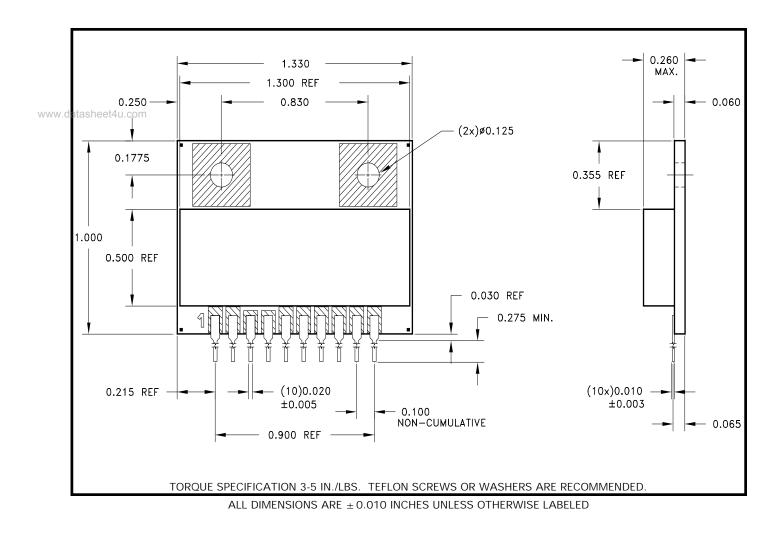
TYPICAL PERFORMANCE CURVES





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MECHANICAL SPECIFICATIONS



ORDERING INFORMATION

Part Number	Screening Level
MSK131	Industrial

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