

SPECIFICATION

Device Name : IGBT Module
 Type Name : 6MBI225U-170
 Spec. No. : MS5F 5391

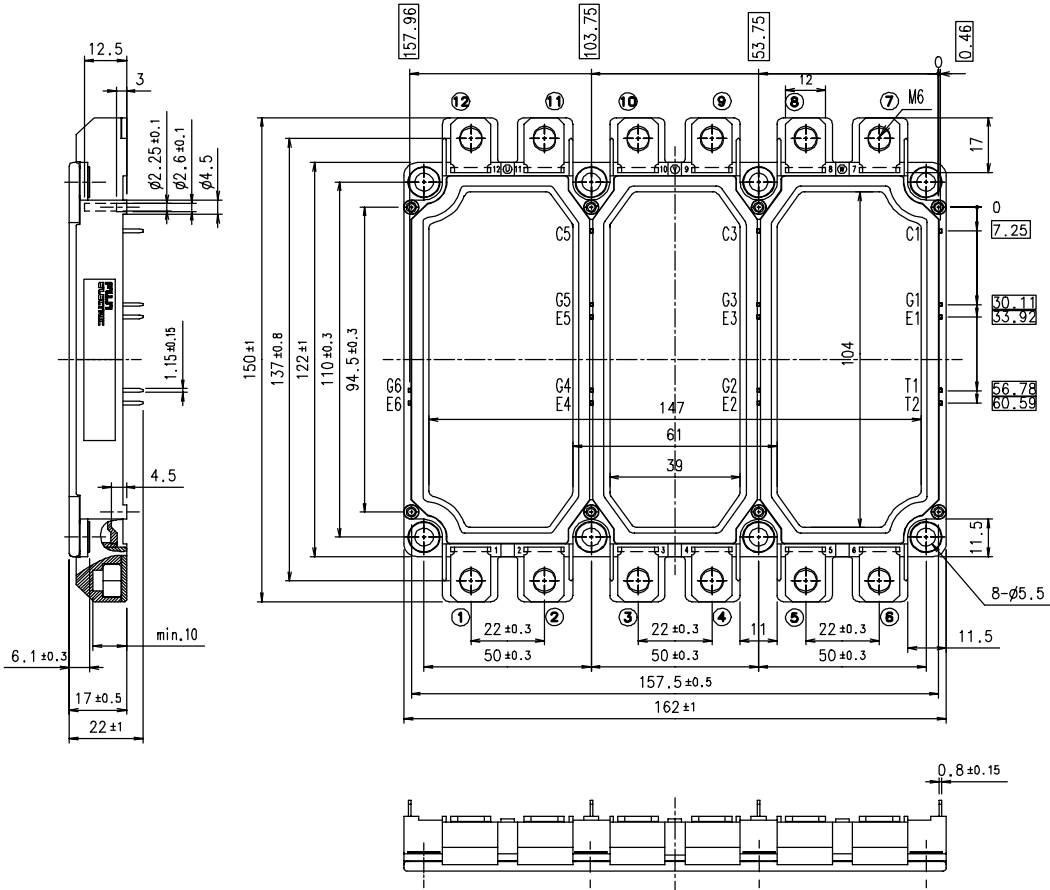
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Fuji Electric Co., Ltd.
Matsumoto Factory

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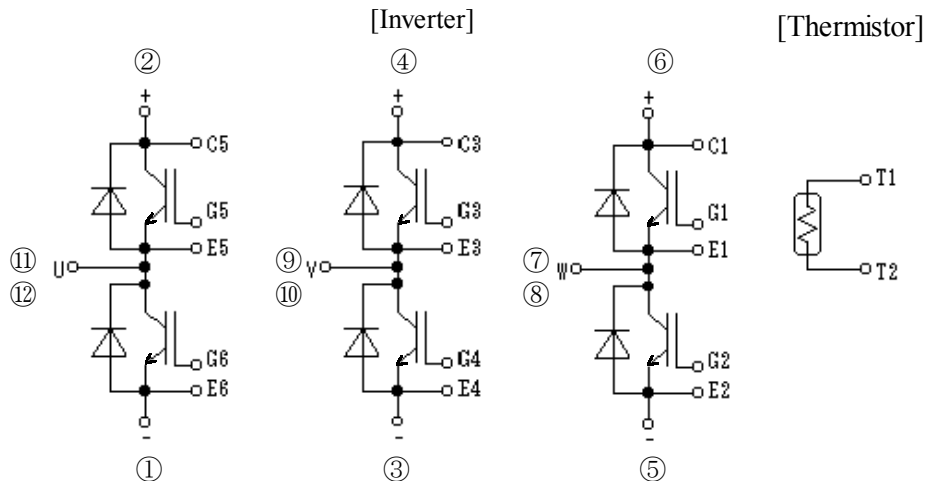
6MBI225U-170

1. Outline Drawing (Unit : mm)



NOTE) shows theoretical dimension and tolerance is ± 0.5 .

2. Equivalent circuit



3. Absolute Maximum Ratings (at Tc= 25°C unless otherwise specified)

Items		Symbols	Conditions	Maximum Ratings	Units
Collector-Emitter voltage		VCES		1700	V
Gate-Emitter voltage		VGES		±20	V
Collector current	Ic	Continuous	Tc=25°C	300	A
			Tc=80°C	225	
	Icp	1ms	Tc=25°C	600	
			Tc=80°C	450	
	-Ic			225	
-Ic pluse			450		
Collector Power Dissipation		Pc	1 device	1050	W
Junction temperature		Tj		150	°C
Storage temperature		Tstg		-40~ +125	
Isolation voltage	between terminal and copper base *1	Viso	AC : 1min.	3400	VAC
	between thermistor and others *2				
Screw Torque	Mounting *3	-		3.5	N·m
	Terminals *4			4.5	

(*1) All terminals should be connected together when isolation test will be done.

(*2) Two thermistor terminals should be connected together, each other terminals should be connected together and shorted to base plate when isolation test will be done.

(*3) Recommendable Value : 2.5~3.5 Nm (M5)

(*4) Recommendable Value : 3.5~4.5 Nm (M6)

4. Electrical characteristics (at Tj= 25°C unless otherwise specified)

Items	Symbols	Conditions	Characteristics			Units		
			min.	typ.	max.			
Inverter	Zero gate voltage Collector current	ICES	VGE = 0V VCE = 1700V	-	-	3.0	mA	
	Gate-Emitter leakage current	IGES	VCE = 0V VGE=±20V	-	-	600	nA	
	Gate-Emitter threshold voltage	VGE(th)	VCE = 20V Ic = 225mA	4.5	6.5	8.5	V	
	Collector-Emitter saturation voltage	VCE(sat) (terminal)	VGE=15V Ic = 225A	Tj= 25°C	-	2.30	2.80	V
				Tj=125°C	-	2.65	-	
		VCE(sat) (chip)	Tj= 25°C	-	2.05	2.55		
			Tj=125°C	-	2.40	-		
	Input capacitance	Cies	VCE=10V, VGE=0V, f=1MHz	-	23	-	nF	
	Turn-on time	ton	Vcc = 900V	-	0.58	1.20	μs	
		tr	Ic = 225A	-	0.32	0.60		
		tr (i)	VGE=±15V	-	0.10	-		
	Turn-off time	toff	Rg = 3 Ω	-	0.80	1.50	μs	
		tf		-	0.15	0.30		
	Forward on voltage	VF (terminal)	VGE=0V IF = 225A	Tj= 25°C	-	2.05	2.80	V
Tj=125°C				-	2.25	-		
VF (chip)		Tj= 25°C	-	1.80	2.55			
		Tj=125°C	-	2.00	-			
Reverse recovery time	trr	IF = 225A	-	0.3	0.6	μs		
Lead resistance, terminal-chip*	R lead		-	1.0	-	mΩ		
Thermistor	Resistance	T = 25°C	-	5000	-	Ω		
		T =100°C	465	495	520			
	B value	B	T = 25/50°C	3305	3375	3450	K	

(*) Biggest internal terminal resistance among arm.

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5. Thermal resistance characteristics

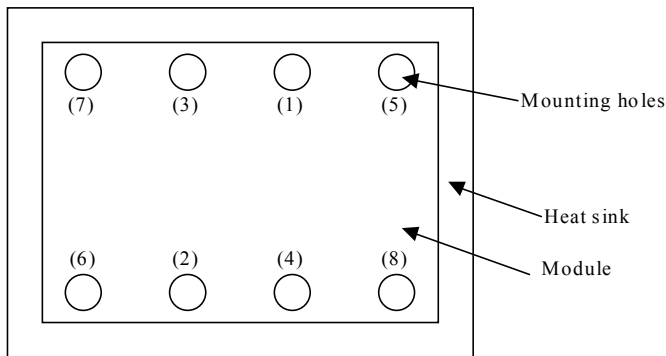
Items	Symbols	Conditions	Characteristics			Units
			min.	typ.	max.	
Thermal resistance(1device)	Rth(j-c)	IGBT	-	-	0.12	°C/W
		FWD	-	-	0.20	
Contact Thermal resistance	Rth(c-f)	with Thermal Compound (※)	-	0.0167	-	

※ This is the value which is defined mounting on the additional cooling fin with thermal compound.

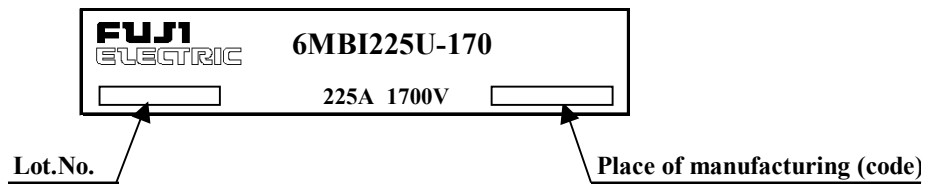
6.Recommend way of module mounting to Heat sink Clamping

(1) Initial : 1/3 specified torque, sequence (1)→(2)→(3)→(4)→(5)→(6)→(7)→(8)

(2) Final : Full specified torque (3.5 Nm),sequence(4)→(3)→(2)→(1)→(8)→(7)→(6)→(5)



7. Indication on module



8.Applicable category

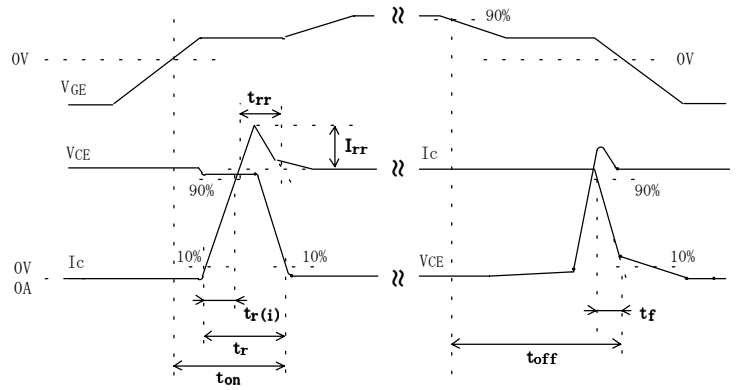
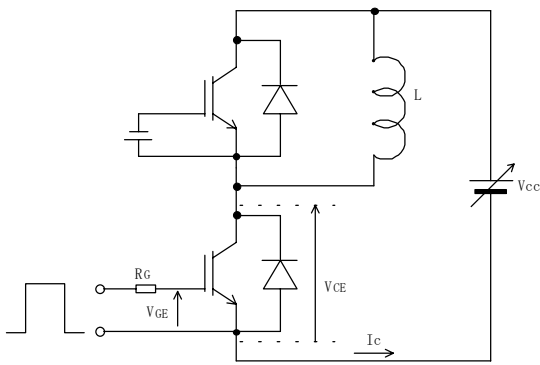
This specification is applied to IGBT Module named 6MBI225U-170 .

9.Storage and transportation notes

- The module should be stored at a standard temperature of 5 to 35°C and humidity of 45 to 75% .
- Store modules in a place with few temperature changes in order to avoid condensation on the module surface.
- Avoid exposure to corrosive gases and dust.
- Avoid excessive external force on the module.
- Store modules with unprocessed terminals.
- Do not drop or otherwise shock the modules when transporting.

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10. Definitions of switching time



11. Packing and Labeling

Display on the packing box

- Logo of production
- Type name
- Lot No
- Products quantity in a packing box

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11. Reliability test results

Reliability Test Items

Test categories	Test items	Test methods and conditions	Reference norms EIAJ ED-4701	Number of sample	Acceptance number
Mechanical Tests	1 Terminal Strength (Pull test)	Pull force : 20N (Control terminal) 40N (Main terminal) Test time : 10±1 sec.	A - 111 Method 1	5	(1 : 0)
	2 Mounting Strength	Screw torque : 2.5 ~ 3.5 N·m (M5) 3.5 ~ 4.5 N·m (M6) Test time : 10±1 sec.	A - 112 Method 2	5	(1 : 0)
	3 Vibration	Range of frequency : 10 ~ 500Hz Sweeping time : 15 min. Acceleration : 10G Sweeping direction : Each X,Y,Z axis Test time : 6 hr. (2hr./direction)	A - 121	5	(1 : 0)
	4 Shock	Maximum acceleratic : 1000G Pulse width : 0.5msec. Direction : Each X,Y,Z axis Test time : 3 times/direction	A - 122	5	(1 : 0)
	5 Solderability	Solder temp. : 235±5 °C Immersion time : 5±1sec. Test time : 1 time Each terminal should be Immersed in solder within 1~1.5mm from the body.	A - 131	5	(1 : 0)
	6 Resistance to Soldering Heat	Solder temp. : 260±5 °C Immersion time : 10±1sec. Test time : 1 time Each terminal should be Immersed in solder within 1~1.5mm from the body.	A - 132	5	(1 : 0)
Environment Tests	1 High Temperature Storage	Storage temp. : 125±5 °C Test duration : 1000hr.	B - 111	5	(1 : 0)
	2 Low Temperature Storage	Storage temp. : -40±5 °C Test duration : 1000hr.	B - 112	5	(1 : 0)
	3 Temperature Humidity Storage	Storage temp. : 85±3 °C Relative humidity : 85±5% Test duration : 1000hr.	B - 121	5	(1 : 0)
	4 Unsaturated Pressure Cooker	Test temp. : 121 °C Atmospheric pressure : 2.03×10 ⁵ Pa (Reference value) Test duration : 20hr.	B - 123	5	(1 : 0)
	5 Temperature Cycle	Test temp. : $\left\{ \begin{array}{l} \text{Low temp. } -40^{+3-5} \text{ °C} \\ \text{High temp. } 125^{+5-5} \text{ °C} \\ \text{RT } 5 \sim 35 \text{ °C} \end{array} \right.$ Dwell time : High ~ RT ~ Low ~ RT 1hr. 0.5hr. 1hr. 0.5hr. Number of cycles : 100 cycles	B - 131	5	(1 : 0)
	6 Thermal Shock	Test temp. : $\left\{ \begin{array}{l} \text{High temp. } 100^{+0-5} \text{ °C} \\ \text{Low temp. } 0^{+5-0} \text{ °C} \end{array} \right.$ Used liquid : Water with ice and boiling water Dipping time : 5 min. par each temp. Transfer time : 10 sec. Number of cycles : 10 cycles	B - 141	5	(1 : 0)

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Reliability Test Items

Test categories	Test items	Test methods and conditions	Reference norms EIAJ ED-4701	Number of sample	Acceptance number
Endurance Tests	1 High temperature Reverse Bias	Test temp. : $T_a = 125^{+0}_{-5} \text{ }^\circ\text{C}$ ($T_j \leq 150 \text{ }^\circ\text{C}$) Bias Voltage : $V_C = 0.8 \times V_{CES}$ Bias Method : Applied DC voltage to C-E $V_{GE} = 0V$ Test duration : 1000hr.	D - 313	5	(1 : 0)
	2 High temperature Bias	Test temp. : $T_a = 125^{+0}_{-5} \text{ }^\circ\text{C}$ ($T_j \leq 150 \text{ }^\circ\text{C}$) Bias Voltage : $V_C = V_{GE} = +20V$ or $-20V$ Bias Method : Applied DC voltage to G-E $V_{CE} = 0V$ Test duration : 1000hr.	D - 323	5	(1 : 0)
	3 Temperature Humidity Bias	Test temp. : $85 \pm 3^\circ\text{C}$ Relative humidity : $85 \pm 5\%$ Bias Voltage : $V_C = 0.8 \times V_{CES}$ Bias Method : Applied DC voltage to C-E $V_{GE} = 0V$ Test duration : 1000hr.	B - 121	5	(1 : 0)
	4 Intermitted Operating Life (Power cycle) (for IGBT)	ON time : 2 sec. OFF time : 18 sec. Test temp. : $\Delta T_j = 100 \pm 5 \text{ deg}$ $T_j \leq 150 \text{ }^\circ\text{C}$, $T_a = 25 \pm 5 \text{ }^\circ\text{C}$ Number of cycles : 15000 cycles	D - 322	5	(1 : 0)

Failure Criteria

Item	Characteristic	Symbol	Failure criteria		Unit	Note	
			Lower limit	Upper limit			
Electrical characteristic	Leakage current	ICES	-	USL×2	mA		
		±IGES	-	USL×2	μA		
	Gate threshold voltage	V _{GE(th)}	LSL×0.8	USL×1.2	mA		
	Saturation voltage	V _{CE(sat)}	-	USL×1.2	V		
	Forward voltage	V _F	-	USL×1.2	V		
	Thermal resistance	IGBT	Δ V _{GE} or Δ V _{CE}	-	USL×1.2	mV	
		FWD	Δ V _F	-	USL×1.2	mV	
	Isolation voltage	Viso	Broken insulation		-		
Visual inspection	Visual inspection						
	Peeling Plating and the others	-	The visual sample		-		

LSL : Lower specified limit.

USL : Upper specified limit.

Note : Each parameter measurement read-outs shall be made after stabilizing the components at room ambient for 2 hours minimum, 24 hours maximum after removal from the tests. And in case of the wetting tests, for example, moisture resistance tests, each component shall be made wipe or dry completely before the measurement.

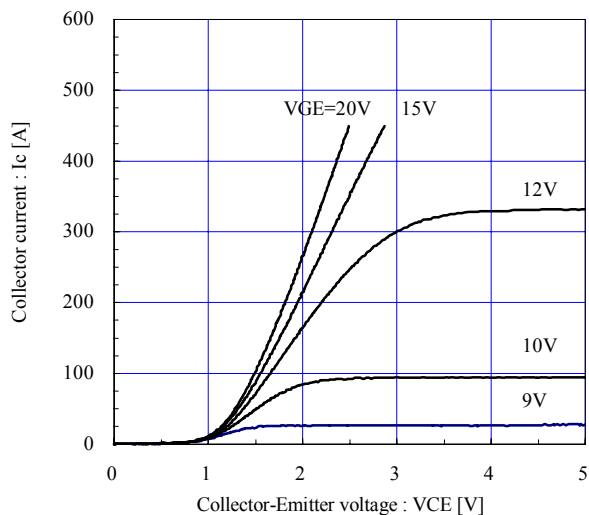
Reliability Test Results

Test categories	Test items	Reference norms EIAJ ED-4701	Number of test sample	Number of failure sample
Mechanical Tests	1 Terminal Strength (Pull test)	A - 111 Method 1	5	0
	2 Mounting Strength	A - 112 Method 2	5	0
	3 Vibration	A - 121	5	0
	4 Shock	A - 122	5	0
	5 Solderability	A - 131	5	0
	6 Resistance to Soldering Heat	A - 132	5	0
Environment Tests	1 High Temperature Storage	B - 111	5	*
	2 Low Temperature Storage	B - 112	5	*
	3 Temperature Humidity Storage	B - 121	5	*
	4 Unsaturated Pressure Cooker	B - 123	5	*
	5 Temperature Cycle	B - 131	5	0
	6 Thermal Shock	B - 141	5	0
Endurance Tests	1 High temperature Reverse Bias	D - 313	5	*
	2 High temperature Bias (for gate)	D - 323	5	*
	3 Temperature Humidity Bias	B - 121	5	*
	4 Intermitted Operating Life (Power cycling) (for IGBT)	D - 322	5	0

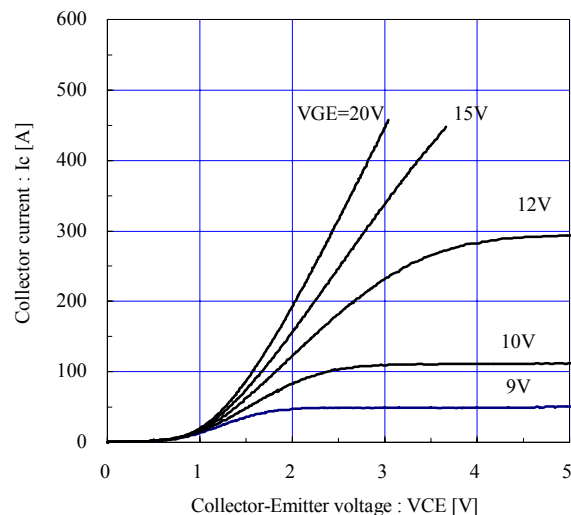
* . . . Under evaluation.

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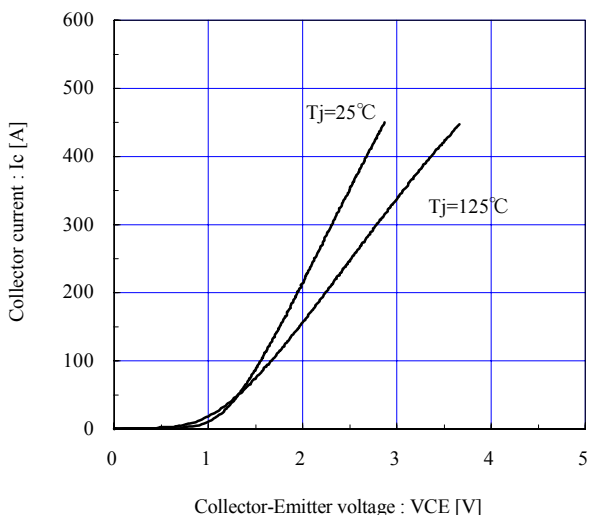
Collector current vs. Collector-Emittor voltage (typ.)
Tj= 25°C / chip



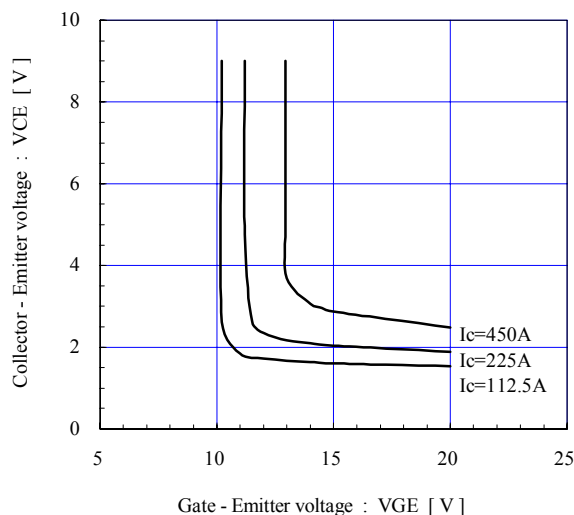
Collector current vs. Collector-Emittor voltage (typ.)
Tj= 125°C / chip



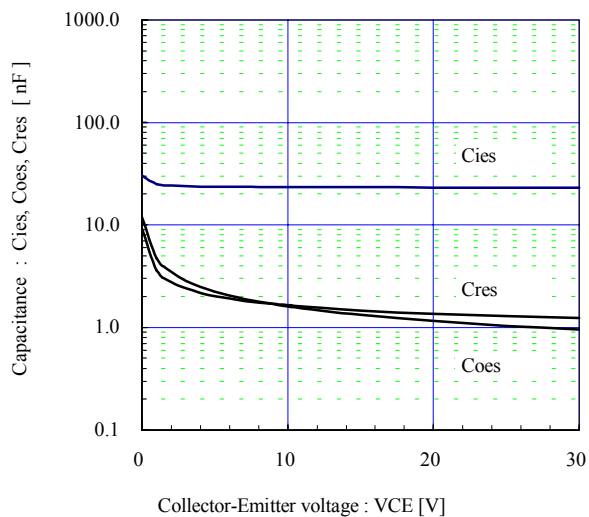
Collector current vs. Collector-Emittor voltage (typ.)
VGE=15V (typ.) / chip



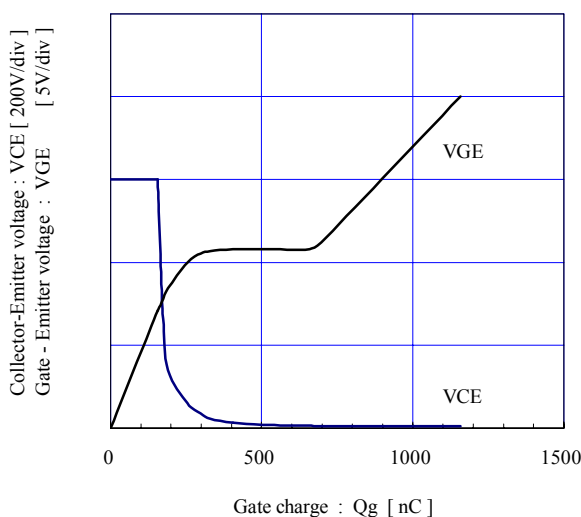
Collector-Emittor voltage vs. Gate-Emittor voltage (typ.)
Tj=25°C / chip



Capacitance vs. Collector-Emittor voltage (typ.)
VGE=0V, f= 1MHz, Tj= 25°C

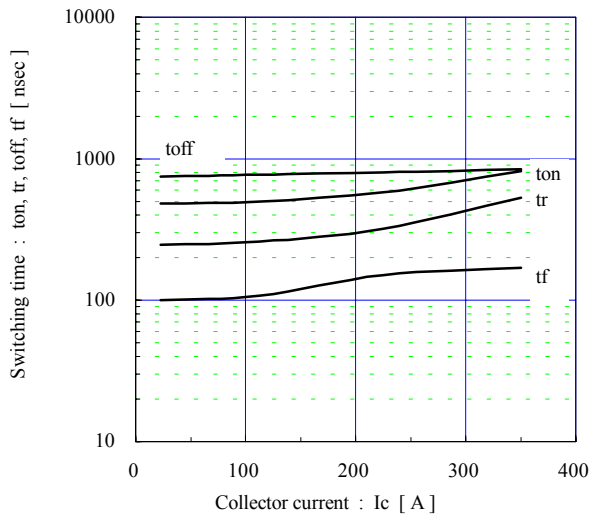


Dynamic Gate charge (typ.)
Vcc=900V, Ic=225A, Tj= 25°C

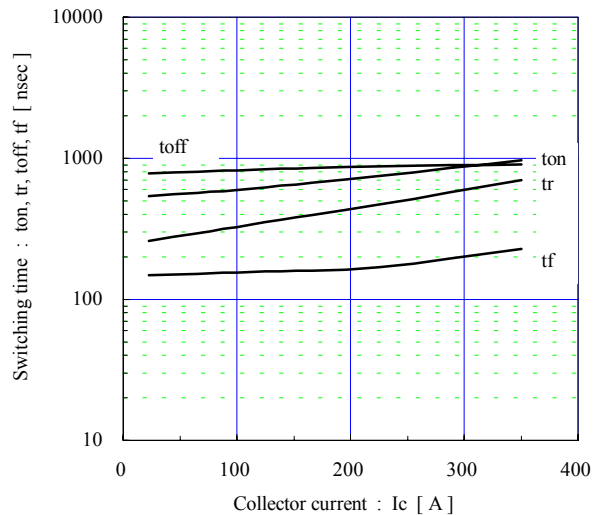


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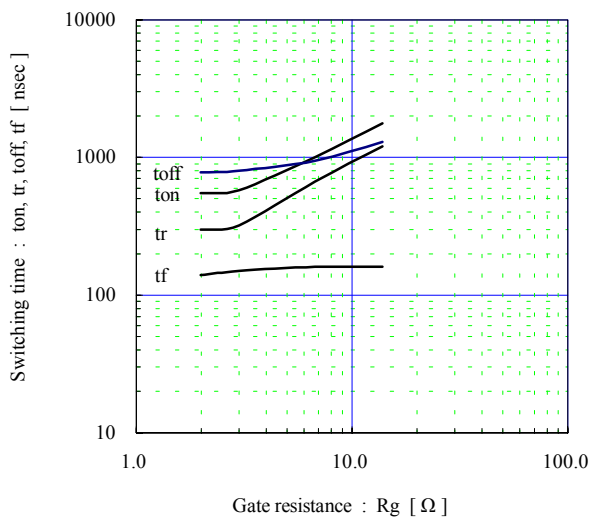
Switching time vs. Collector current (typ.)
 $V_{cc}=900V, V_{GE}=\pm 15V, R_g=3\Omega, T_j=25^\circ C$



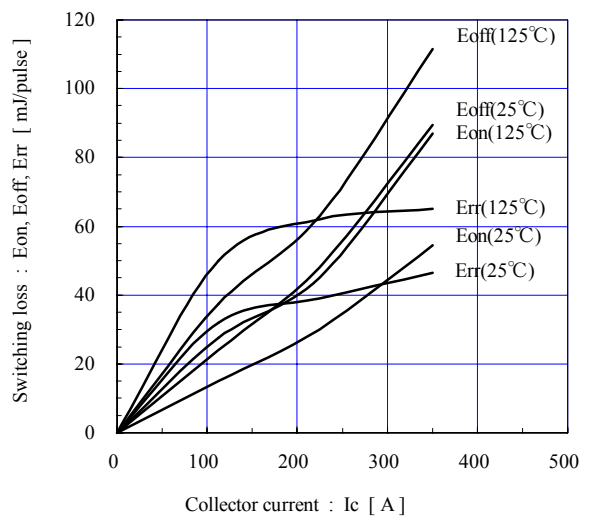
Switching time vs. Collector current (typ.)
 $V_{cc}=900V, V_{GE}=\pm 15V, R_g=3\Omega, T_j=125^\circ C$



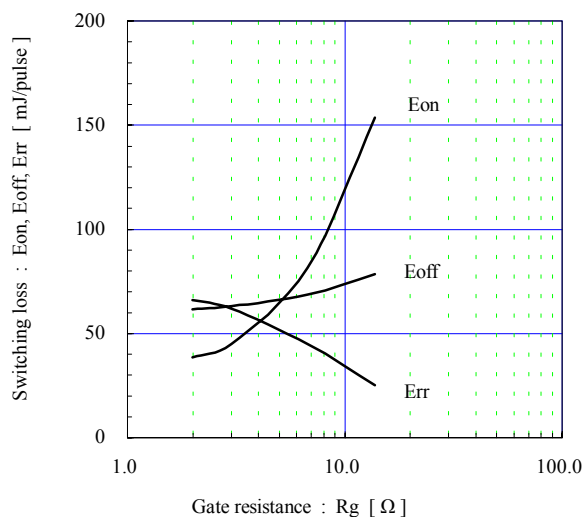
Switching time vs. Gate resistance (typ.)
 $V_{cc}=900V, I_c=225A, V_{GE}=\pm 15V, T_j=25^\circ C$



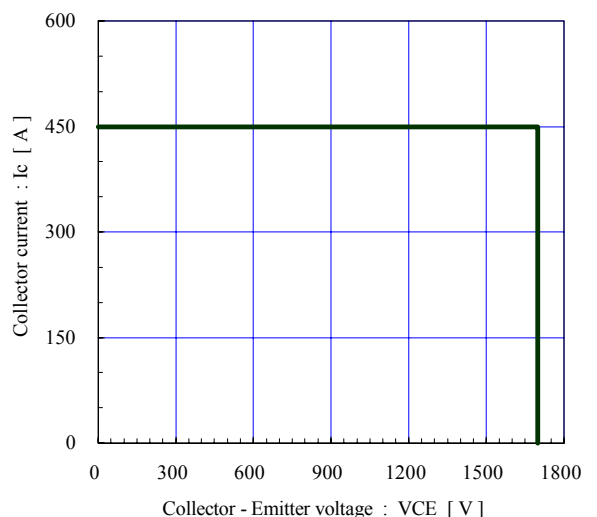
Switching loss vs. Collector current (typ.)
 $V_{cc}=900V, V_{GE}=\pm 15V, R_g=3\Omega$



Switching loss vs. Gate resistance (typ.)
 $V_{cc}=900V, I_c=225A, V_{GE}=\pm 15V, T_j=125^\circ C$

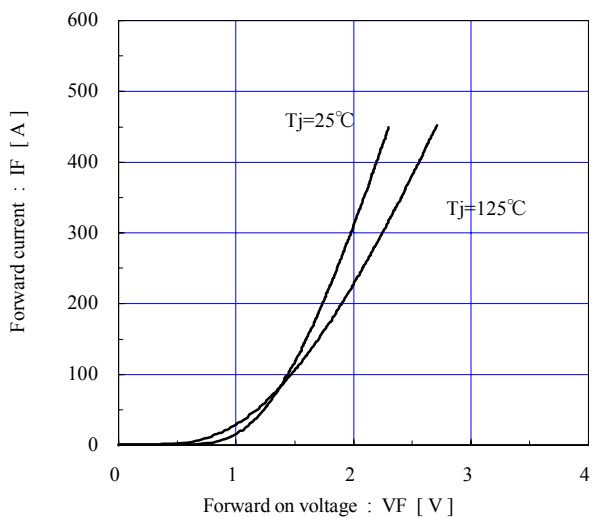


Reverse bias safe operating area (max.)
 $+V_{GE}=15V, -V_{GE}\le 15V, R_g\ge 3\Omega, T_j\le 125^\circ C$
 Stray inductance $\le 100nH$

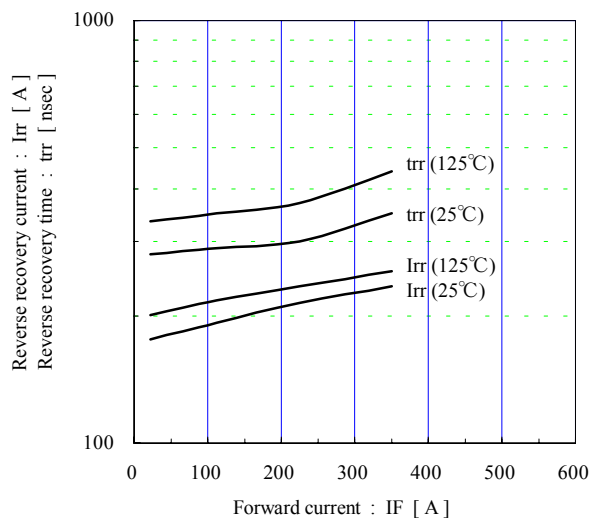


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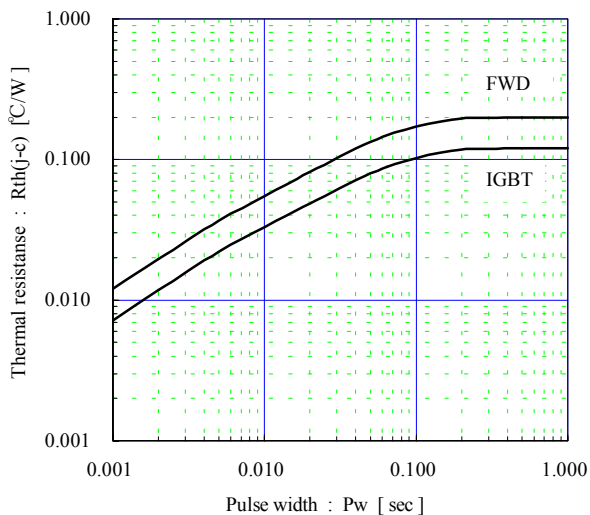
Forward current vs. Forward on voltage (typ.)
chip



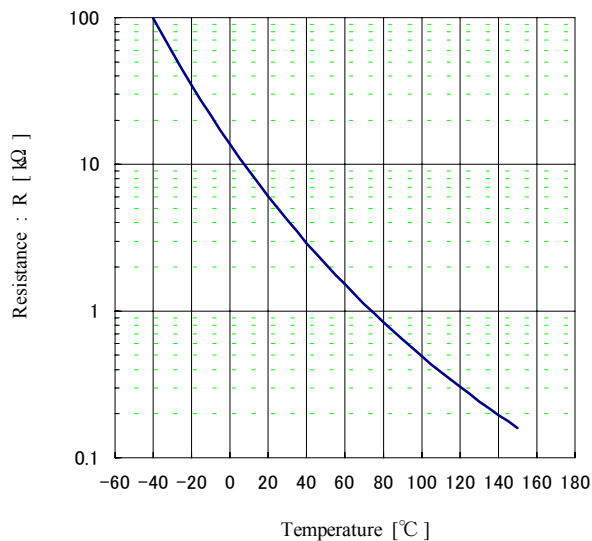
Reverse recovery characteristics (typ.)
Vcc=900V, VGE=±15V, Rg=3Ω



Transient thermal resistance (max.)



Temperature characteristic (typ.)



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Warnings

- This product shall be used within its absolute maximum rating (voltage, current, and temperature).
This product may be broken in case of using beyond the ratings.
製品の絶対最大定格(電圧, 電流, 温度等)の範囲内で御使用下さい。
絶対最大定格を超えて使用すると、素子が破壊する場合があります。
- Connect adequate fuse or protector of circuit between three-phase line and this product to prevent the equipment from causing secondary destruction.
万一の不慮の事故で素子が破壊した場合を考慮し、商用電源と本製品の間に適切な容量のヒューズ又はブレーカーを必ず付けて2次破壊を防いでください。
- Use this product after realizing enough working on environment and considering of product's reliability life.
This product may be broken before target life of the system in case of using beyond the product's reliability life.
製品の使用環境を十分に把握し、製品の信頼性寿命が満足できるか検討の上、本製品を適用して下さい。
製品の信頼性寿命を超えて使用した場合、装置の目標寿命より前に素子が破壊する場合があります。
- If the product had been used in the environment with acid, organic matter, and corrosive gas (hydrogen sulfide, sulfurous acid gas), the product's performance and appearance can not be ensured easily.
酸・有機物・腐食性ガス(硫化水素, 亜硫酸ガス等)を含む環境下で使用された場合、製品機能・外観などの保証は致しかねます。
- Use this product within the power cycle curve (Technical Rep.No. : MT5F12959)
本製品は、パワーサイクル寿命カーブ以下で使用下さい(技術資料No.: MT5F12959)
- Never add mechanical stress to deform the main or control terminal.
The deformed terminal may cause poor contact problem.
主端子及び制御端子に応力を与えて変形させないで下さい。 端子の変形により、接触不良などを引き起こす場合があります。
- According to the outline drawing, select proper length of screw for main terminal.
Longer screws may break the case.
本製品に使用する主端子用のネジの長さは、外形図に従い正しく選定下さい。
ネジが長いとケースが破損する場合があります。
- Use this product with keeping the cooling fin's flatness between screw holes within 100um at 100mm and the roughness within 10um. Also keep the tightening torque within the limits of this specification.
Improper handling may cause isolation breakdown and this may lead to a critical accident.
冷却フィンにネジ取り付け位置間で平坦度を100mmで100um以下、表面の粗さは10um以下にして下さい。 誤った取り扱いをすると絶縁破壊を起こし、重大事故に発展する場合があります。
- It shall be confirmed that IGBT's operating locus of the turn-off voltage and current are within the RBSOA specification. This product may be broken if the locus is out of the RBSOA.
ターンオフ電圧・電流の動作軌跡がRBSOA仕様内にあることを確認して下さい。
RBSOAの範囲を超えて使用すると素子が破壊する可能性があります。
- If excessive static electricity is applied to the control terminals, the devices may be broken.
Implement some countermeasures against static electricity.
制御端子に過大な静電気が印加された場合、素子が破壊する場合があります。
取り扱い時は静電気対策を実施して下さい。
- Never add the excessive mechanical stress to the main or control terminals when the product is applied to equipments. The module structure may be broken.
素子を装置に実装する際に、主端子や制御端子に過大な応力を与えないで下さい。
端子構造が破壊する可能性があります。
- In case of insufficient -VGE, erroneous turn-on of IGBT may occur.
-VGE shall be set enough value to prevent this malfunction. (Recommended value : -VGE = -15V)
逆バイアスゲート電圧-VGEが不足しますと誤点弧を起こす可能性があります。
誤点弧を起こさない為に-VGEは十分な値で設定して下さい。(推奨値 : -VGE = -15V)

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Cautions

- Fuji Electric is constantly making every endeavor to improve the product quality and reliability. However, semiconductor products may rarely happen to fail or malfunction. To prevent accidents causing injury or death, damage to property like by fire, and other social damage resulted from a failure or malfunction of the Fuji Electric semiconductor products, take some measures to keep safety such as redundant design, spread-fire-preventive design, and malfunction-protective design.
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