

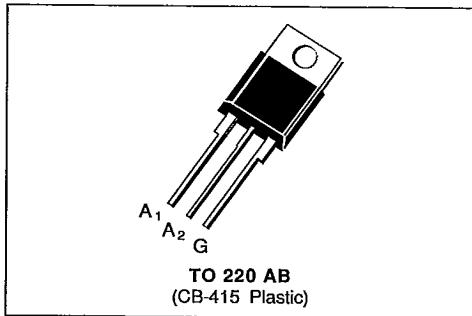
S G S-THOMSON

LOGIC LEVEL TRIACS

- $I_{TRMS} = 8 \text{ A}$ at $T_c = 80^\circ\text{C}$.
- $V_{DRM} : 200 \text{ V}$ to 800 V .
- $I_{GT} = 10 \text{ mA}$ (QI-II-III).
- $(di/dt)_c = 4.5 \text{ A/ms}$ @ $(dv/dt)_c = 50 \text{ V/}\mu\text{s}$.
- SUITED FOR LOW POWER TRIGGER CIRCUITS (INTEGRATED CIRCUITS AND MICROPROCESSORS).
- GLASS PASSIVATED CHIP.
- HIGH EFFICIENCY SWITCHING.
- AVAILABLE IN INSULATED VERSION → BTA SERIES (INSULATING VOLTAGE : $2500 \text{ V}_{\text{RMS}}$) OR IN UNINSULATED VERSION → BTB SERIES.
- UL RECOGNIZED FOR BTA SERIES (E81734).

DESCRIPTION

New range suited for applications such as phase control and static switching on inductive or resistive load.

**ABSOLUTE RATINGS** (limiting values)

Symbol	Parameter	Value	Unit
I_{TRMS}	RMS on-state current (360 ° conduction angle)	8	A
I_{TSM}	Non repetitive surge peak on-state current (T_j initial = 25°C)	$t = 8.3 \text{ ms}$	95
		$t = 10 \text{ ms}$	85
I^2t	I^2t value	$t = 10 \text{ ms}$	A^2s
di/dt	Critical rate of rise of on-state current (1)	Repetitive $F = 50 \text{ Hz}$	20
		Non Repetitive	100
T_{stg} T_j	Storage and operating junction temperature range	- 40, + 150 - 40, + 110	$^\circ\text{C}$ $^\circ\text{C}$

Symbol	Parameter	BTA/BTB 08-					Unit
		200 SW	400 SW	600 SW	700 SW	800 SW	
V_{DRM}	Repetitive peak off-state voltage (2)	± 200	± 400	± 600	± 700	± 800	V

(1) Gate supply : $I_G = 100 \text{ mA}$ - $di_G/dt = 1 \text{ A}/\mu\text{s}$.(2) $T_j = 110^\circ\text{C}$.

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th} (J - a)$	Junction to ambient	60	°C/W
$R_{th} (J - c)$ DC	Junction to case for DC	3.5	°C/W
$R_{th} (J - c)$ AC	Junction to case for 360 ° conduction angle ($F = 50$ Hz)	2.6	°C/W

GATE CHARACTERISTICS (maximum values)

$P_{GM} = 40$ W ($t = 10$ µs) $P_G (AV) = 1$ W $I_{GM} = 4$ A ($t = 10$ µs) $V_{GM} = 16$ V ($t = 10$ µs).

ELECTRICAL CHARACTERISTICS

Symbol	Test Conditions			Quadrants	Min.	Typ.	Max.	Unit
I_{GT}	$T_J = 25$ °C	$V_D = 12$ V	$R_L = 33 \Omega$	I-II-III			10	mA
	Pulse duration > 20 µs							
V_{GT}	$T_J = 25$ °C	$V_D = 12$ V	$R_L = 33 \Omega$	I-II-III			1.5	V
	Pulse duration > 20 µs							
V_{GD}	$T_J = 110$ °C	$V_D = V_{DRM}$	$R_L = 3.3 \text{ k}\Omega$	I-II-III	0.2			V
	Pulse duration > 20 µs							
I_H^*	$T_J = 25$ °C Gate open	$I_T = 100$ mA	$R_L = 140 \Omega$				25	mA
I_L	$T_J = 25$ °C	$V_D = 12$ V	$R_L = 33 \Omega$	I-III		25		
	Pulse duration > 20 µs			II		50		
V_{TM}^*	$T_J = 25$ °C	$I_{TM} = 11$ A	$t_p = 10$ ms				1.75	V
I_{DRM}^*	$T_J = 25$ °C $T_J = 110$ °C	V_{DRM} rated	Gate open				10	µA
							500	
dv/dt^*	$T_J = 110$ °C Gate open	Linear slope up to 0.67 V_{DRM}			50			V/µs
$(dI/dt)_c^*$	$T_J = 110$ °C	$(dv/dt)_c = 0.1$ V/µs			4.5	7		
	$T_J = 110$ °C	$(dv/dt)_c = 50$ V/µs			3.5	4.5		A/ms
t_{gt}	$T_J = 25$ °C $I_T = 11$ A	$dI_G / dt = 1$ A/µs $V_D = V_{DRM}$	$I_G = 50$ mA	I-II-III		2		µs

* For either polarity of electrode A₂ voltage with reference to electrode A₁.

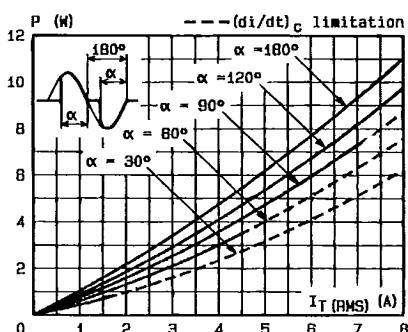


Fig.1 - Maximum mean power dissipation versus RMS on-state current ($f = 60$ Hz).

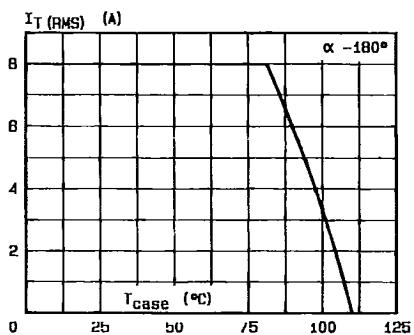


Fig.3 - RMS on-state current versus case temperature.

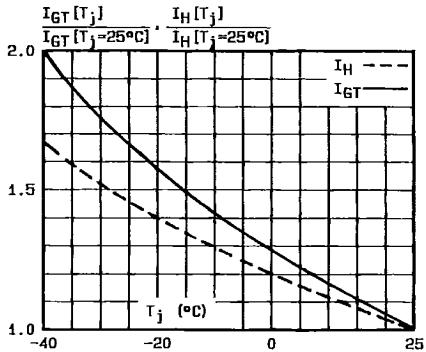


Fig.5 - Relative variation of gate trigger current and holding current versus junction temperature.

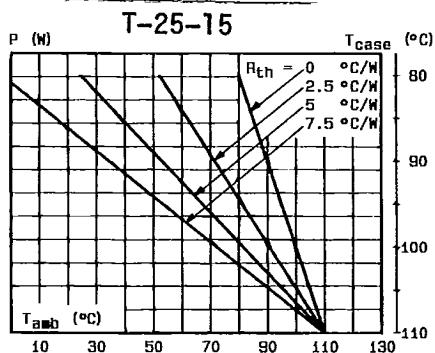


Fig.2 - Correlation between maximum mean power dissipation and maximum allowable temperatures (T_{amb} and T_{case}) for different thermal resistances heatsink + contact.

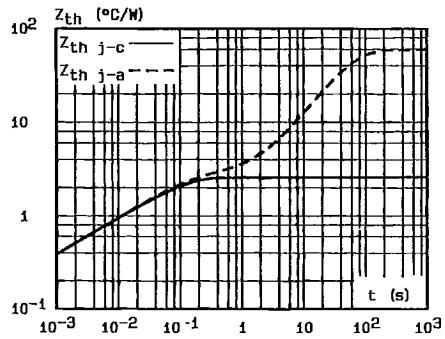


Fig.4 - Thermal transient impedance junction to case and junction to ambient versus pulse duration.

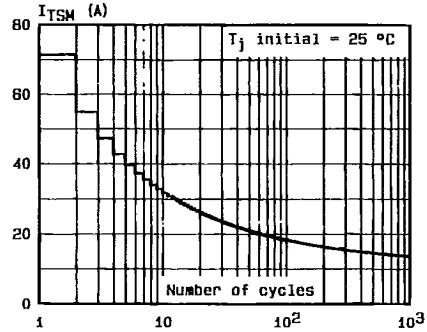


Fig.6 - Non repetitive surge peak on-state current versus number of cycles.

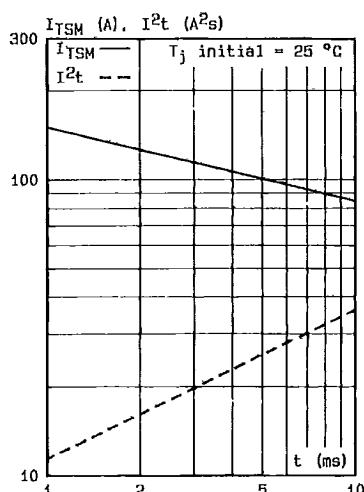


Fig.7 - Non repetitive surge peak on-state current for a sinusoidal pulse with width : $t \leq 10$ ms, and corresponding value of I^2t .

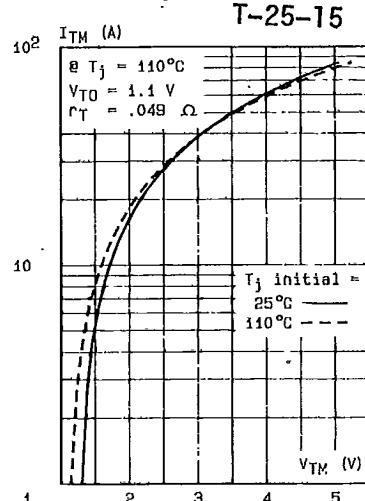


Fig.8 - On-state characteristics (maximum values).

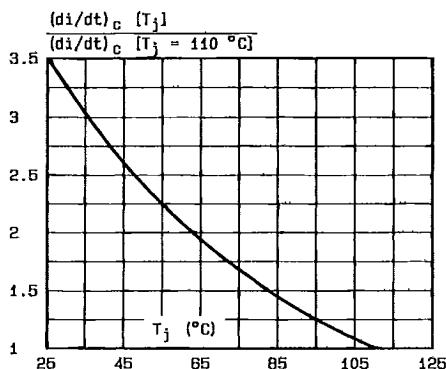


Fig.9 - Relative variation of $(di/dt)_c$ versus junction temperature.

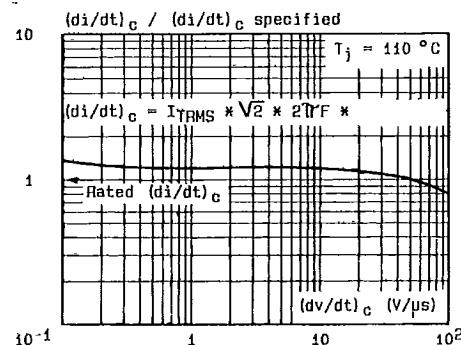


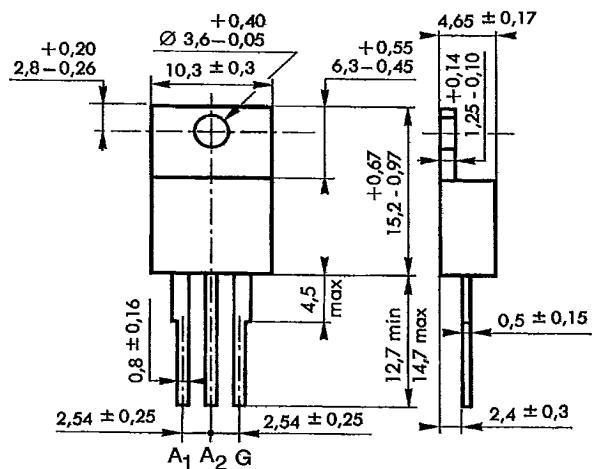
Fig.10 - Relative variation of $(di/dt)_c$ versus $(dv/dt)_c$ (inductive load) (typical values).

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PACKAGE MECHANICAL DATA

TO 220 AB (CB-415) Plastic

T-25-15



Cooling method : by conduction (method C)

Marking : type number

Weight : 2 g