

SPDT SWITCH

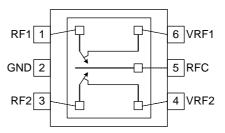
RoHS Compliant & Pb-Free Product Package Style: QFN, 6-pin, 2x2

Features

- Low Insertion Loss 0.3dB at 1GHz
- High Isolation 26dB at 1GHz
- Low Control Voltage 2.6V to 5.0V
- Harmonics H2: -80dBc@1GHz
- GaAs pHEMT Process

Applications

- Cellular Handset Applications
- Antenna Tuning Applications
- Multi-Mode GSM, W-CDMA Applications
- IEEE802.11b/g WLAN Applications
- GSM/GPRS/EDGE Switch Applications
- Cellular Infrastructure Applications



Functional Block Diagram

Product Description

The RF1200 is a single-pole double-throw (SPDT) high power switch specially designed to handle GSM power applications. The RF1200 features low insertion loss, low control voltage, high linearity, and very good harmonic characteristics. It is fabricated with 0.5 μ m GaAs pHEMT process, and is packaged in a very compact 2mmx2mm, 6-pin, leadless QFN package.

Ordering Information

RF1200 SPDT Switch

RF1200PCBA-410 Fully Assembled Evaluation Board

Optimum Technology Matching® Applied

☐ GaAs HBT	☐ SiGe BiCMOS	☑ GaAs pHEMT	☐ GaN HEMT
☐ GaAs MESFET	☐ Si BiCMOS	☐ Si CMOS	
☐ InGaP HBT	☐ SiGe HBT	☐ Si BJT	

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Absolute Maximum Ratings

Parameter	Rating	Unit
Voltage	7.0	V
Maximum Input Power (OGHz to 2.5 GHz)	+36	dBm
Operating Temperature	-30 to +85	°C
Storage Temperature	-35 to +100	°C



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RoHS status based on EUDirective 2002/95/EC (at time of this document revi-

Davamatav	Specification		1124	O andition	
Parameter	Min.	Тур.	Max.	Unit	Condition
					Temp=25°C, V _{CONTROL} =2.65V
Insertion Loss					
RF>ANT		0.3	0.4	dB	RF ON, 0.88 GHz
RF>ANT		0.4	0.5	dB	RF ON, 1.88GHz
RF>ANT		0.5	0.6	dB	RF ON, 2.10GHz
RF>ANT		0.55	0.65	dB	RF ON, 2.45 GHz
RF>ANT Isolation					
RF>ANT	25	26		dB	RF ON, 0.88 GHz
RF>ANT	21	22		dB	RF ON, 1.88GHz
RF>ANT	19	20		dB	RF ON, 2.10GHz
RF>ANT	17	18		dB	RF ON, 2.45 GHz
0.8 GHz to 1 GHz Harmonics					
Second Harmonic		-80		dBc	P _{IN} =34.5dBm, 0.88GHz, 2f ₀
Third Harmonic		-75		dBc	P _{IN} =34.5dBm, 0.88GHz, 3f ₀
1.7 GHz to 2.0 GHz Harmonics					
Second Harmonic		-80		dBc	P _{IN} =31.5dBm, 1.9GHz, 2f ₀
Third Harmonic		-80		dBc	P _{IN} =31.5dBm, 1.9GHz, 3f ₀
2.45 GHz Harmonics					
Second Harmonic		-90		dBc	P _{IN} =31.5dBm, 1.9GHz, 2f ₀
Third Harmonic		-90		dBc	P _{IN} =31.5dBm, 1.9GHz, 3f ₀
IMD Due to Out-of-Band Blocker					
RF>ANT		-105		dBm	P _{IN} =20dBm @ 1950MHz, P _{BLOCK} =-15dBm @ 4090MHz
RF Port Return Loss					
RF>ANT		15		dB	0.5 GHz to 2.5 GHz
Input Power at 0.1dB Compression Point					
	37			dBm	0.88GHz
	34			dBm	1.88GHz
Switching Speed					
			5	us	

Note: Parameters hold at 25 $^{\circ}\text{C}$ and $\text{V}_{\text{CONTROL}}\text{=}2.65\,\text{V}.$





Switch Control Settings

,	Control Signals		Signal Paths		
·	VRF1	VRF2	RF1-RFC	RF2-RFC	
Valid States	1	0	Closed	Open	
1	0	1	Open	Closed	
Invalid			Indetermin	inate State*	
States	1	1	Indeterminate State*		

0: Logic level low, 0V~0.2V

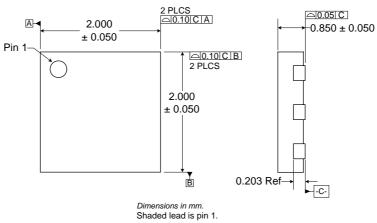
1: Logic level high, 2.6V~5.0V

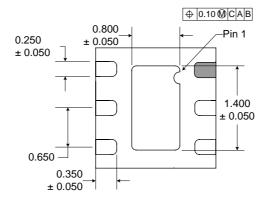
Note: In indeterminate states, both sigal paths are closed with degraded performance.



Pin	Function	Description	Interface Schematic
1	RF1	First RF connection.	
2	GND	Ground.	
3	RF2	Second RF connection.	
4	VRF2	Second RF control.	
5	RFC	Common RF connection.	
6	VRF1	First RF control.	
Pkg	GND		
Base			

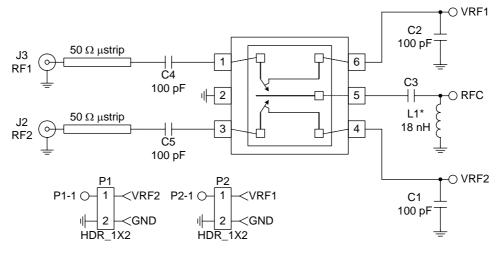
Package Drawing







Evaluation Board Schematic

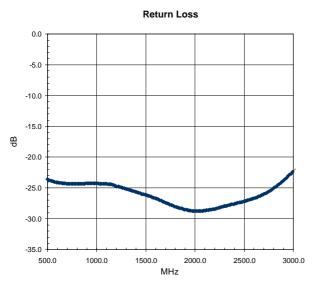


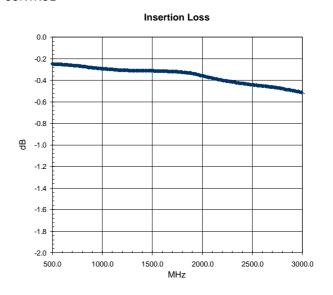
*L1 is optional for IEC61000-4-2 ESD protection.

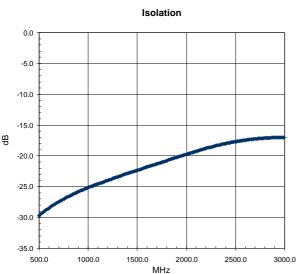


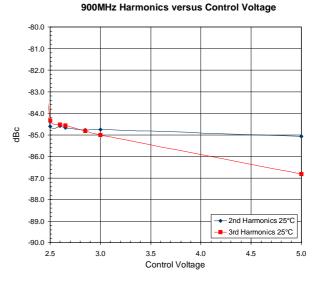
Typical Performance

Temp=25°C, V_{CONTROL}=2.65V













1900MHz Harmonics versus Control Voltage

