

STD1766

NPN Silicon Transistor

Descriptions

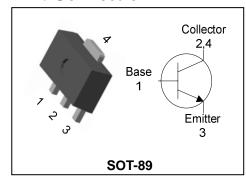
• Medium power amplifier

Features

- P_C(Collector power dissipation)=2W (Ceramic substrate of 250 mm² × 0.8t used)
- Low collector saturation voltage : V_{CE(sat)}=0.5V(Typ.)
- Complementary pair with STB1188
- "Green" device and RoHS compliant device
- Available in full lead (Pb)-free device

RoHS (N

PIN Connection



Ordering Information

Type NO.	Marking	Package Code
STD1766	B2 □YWW	SOT-89

B2: DEVICE CODE, \square : h_{FE} rank, YWW(Y: Year code, WW: Weekly code)

Absolute maximum ratings

(Ta=25°C)

Characteristic	Symbol	Ratings	Unit	
Collector-Base voltage	V_{CBO}	40	V	
Collector-Emitter voltage	V_{CEO}	32	V	
Emitter-Base voltage	V_{EBO}	5	V	
Collector current	I _C	2	A(DC)	
Conector current	I _{CP} *	4	A(Pulse)	
Collector power dissipation	P _C	0.5	W	
Collector power dissipation	P _C **	1	VV	
Junction temperature	TJ	150	°C	
Storage temperature	T _{stg}	-55~150	°C	

Characteristic		Symbol	Тур.	Max	Unit	
Thermal resistance	Junction-ambient	$R_{th(J\text{-}A)}$	-	250.0	°C/W	
		R _{th(J-A)} **	-	125.0	C/W	

^{*:} Single pulse, tp= 300 μ s

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^{**:} When mounted on ceramic substrate(250 m²×0.8t)

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Electrical Characteristics

(Ta=25°C)

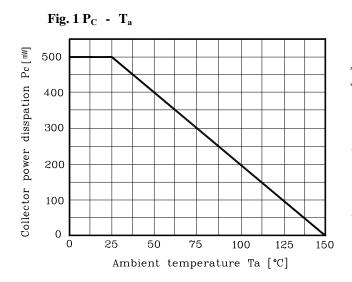
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-Base breakdown voltage	BV _{CBO}	I _C =50 μA, I _E =0	40	-	-	V
Collector-Emitter breakdown voltage	BV _{CEO}	$I_C=1$ MA, $I_B=0$	32	-	-	V
Emitter-Base breakdown voltage	BV _{EBO}	I _E =50 μA, I _C =0	5	-	-	V
Collector cut-off current	I _{CBO}	$V_{CB} = 20V, I_{E} = 0$	-	-	1	μA
Emitter cut-off current	I _{EBO}	V _{EB} =4V, I _C =0	-	-	1	μA
DC current gain	h _{FE} *	$V_{CE} = 3V$, $I_{C} = 0.5A$	100	-	320	-
Collector-Emitter saturation voltage	V _{CE(sat)}	I _C =2A, I _B =200 mA	-	0.5	0.8	V
Transition frequency	f _T	V _{CB} =5V, I _C =500 mA	-	100	-	MHz
Collector output capacitance	C _{ob}	$V_{CB}=10V$, $I_{E}=0$, $f=1$ MHz	-	30	-	pF

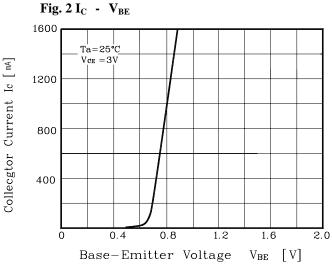
^{* :} h_{FE} rank / O : 100~200, Y : 160~320

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Electrical Characteristic Curves





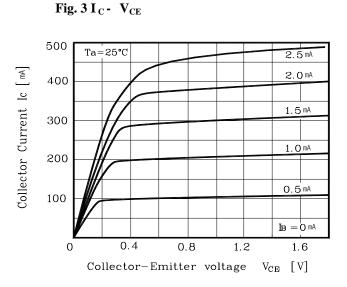
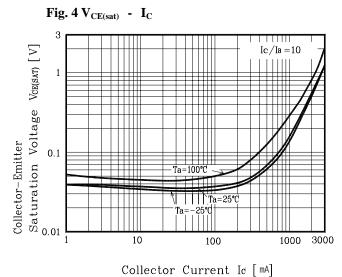
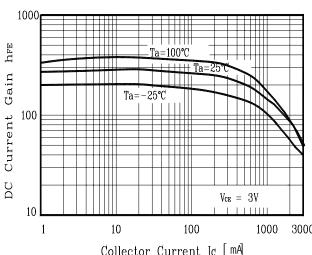
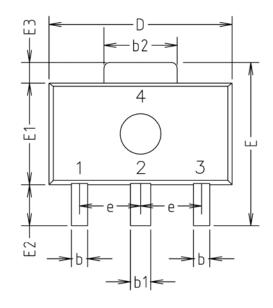


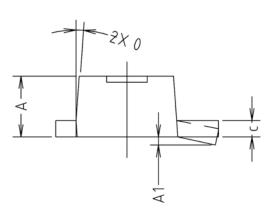
Fig. 5 h_{FE} - I_C





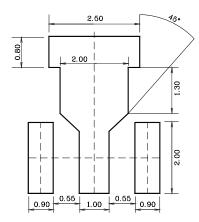
Outline Dimension(mm)





		MILLIMETERS	_	Т
CYANDOL		NOTE		
SYMBOL	MINIMUM	NOMINAL	MAXIMUM	INOTE
Α	1.40	1.50	1.60	
A1	0.00	_	0.10	
b	0.38	0.42	0.48	
b1	0.48	0.52	0.58	
b2	1.79	1.82	1.87	
С	0.40	0.42	0.46	
D	4.40	4.50	4.70	
Ε	3.70	4.00	4.30	
E1	2.40	2.50	2.70	
E2	0.80	1.00	1.20	
E3	0.40	0.50	0.60	
е		1.50 TYP.		
0		4° TYP.		

***Recommend PCB solder land [Unit: mm]**



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